EXCO 지하 1F Lobby

일 시 : 2월 25일(목) 14:40-18:20
세션명 : [TP2] Poster Session II

A. Interconnect & Package 분과

TP2-1  High Power LED Packaging by MOAMP (Multi-chip on Aluminum Metal Plate) Technology  
저자: 지미희, 남충모  
소속: 한국산업기술대학교 전자공학과

TP2-2  Atomic Layer Deposition of Nickel Thin Films and Application to Area Selective Deposition for Nanoscale Contact  
저자: Woo-Hee Kim1, Han-Bo-Ram Lee1, Young Kuk Lee2, Taek-Mo Chung2, Chang Gyoun Kim2, and Hyungjun Kim1  
소속: 1School of Materials Science and Engineering, Yonsei University, 2Advanced Materials Division, Korea Research Institute of Chemical Technology

TP2-3  Improvement of Electrochemical Migration Resistance by Sn Protective Barrier on Cu electrode in Printed Circuit Board  
저자: Min-Suk Jung1, Shin-Bok Lee1, Ho-Young Lee1, ChangSup Ryu2, YoungGwan Ko2, HongSeok Min2, and Young-Chang Joo1  
소속: 1Department of Materials Science and Engineering, Seoul National University, 2ACI Division, Samsung Electro-Mechanics Co., Ltd.

TP2-4  A Novel Positive Type Photosensitive Polyimide for Buffer Coat Film  
저자: Sang Woo Kim, Chan Hyo Park, Se Jin Shin, Hye Won Jeong, Jung Ho Jo, and Kyung Jun Kim  
소속: IT & E Materials R&D, LG Chem Research Park

TP2-5  열 응력 흡수 구조체를 이용한 칩 내장형 인쇄회로기판 제조방법  
저자: 서원1, 구영모2, 박세훈3, 강남기3, 김구성  
소속: 1강남대학교, 2ESIP Lab., EPWorks Co., Ltd., 3전자부품연구원

TP2-6  Effects of Accelerator on the Cu Metallization Using Electroplating Process  
저자: Sung-kyu Kang1, Sa-Kyun Rha1, Jung-Hye Seo2, Yoon-Seoung Lee2, Young-Ho Ryu3, and Kimin Hong3  
소속: 1Department of Materials Engineering, Hanbat National University, 2Department of Information Communication Engineering, Hanbat National University, 3Department of Physics, Chungnam National University

B. Patterning 분과
TP2-7  부유형 탑침법을 이용한 전자온도, 이온밀도의 2 차원 공간분포의 실시간 진단
저자: 김영철, 장성호, 김건호, 정진욱
소속: 한양대학교 반도체 공정체어 연구실

TP2-8  Fabrication of Highly Conductive Micro-wires on Templated Flexible Substrate by Inkjet Printing Technique
저자: Jinbo Shim, Yu-Jin Na, Seong-Min Cho, and Sin-Doo Lee
소속: School of Electrical Engineering and Computer Science, Seoul National University

TP2-9  A Study of the Mechanisms Responsible for Black Silicon Phenomenon in Deep Trench Gate Etching with SF$_6$ /O$_2$ / HBr Mixture
저자: 홍정표, 김재승, 하승철, 장영민, 정지훈, 강동우, 이정관, 김재영
소속: 동부하이텍 Etch팀

TP2-10  Inorganic Thin Film Transfer Printing Using Atomic Layer Deposition
저자: Su H. Kim, Byoung H. Lee, and Myung M. Sung
소속: Department of Chemistry, Hanyang University

TP2-12  Inert Gas Control을 이용한 SOH (Spin On Hard mask) Patterniing 공정특성 연구
저자: 사공영채, 김진영, 안주현
소속: 삼성전자공과대학교 반도체공학과 / 삼성전자 반도체연구소 공정개발팀

K. Memory (Design & Process Technology) 분과

TP2-13  3-D Stacked NAND Flash String with Single–Crystal Si Channel by Adopting Si/SiGe Selective Etch Process
저자: Ju-Wan Lee$^1$, Min-Kyu Jeong$^1$, Hyuck-In Kwon$^2$, Byung–Gook Park$^1$, Hyungcheol Shin$^1$, and Jong–Ho Lee$^1$
소속: $^1$School of Electrical Engineering and Computer Science and Inter–university Semiconductor Research Center, Seoul National University, $^2$School of Electronic Engineering, Daegu University

TP2-14  Thickness Dependence of Bias Field in Ferroelectric Polymer Thin Film
저자: Woo Young Kim$^1$, Du Youn Ka$^2$, Yong Soo Lee$^1$, Sang Youl Kim$^2$, and Hee Chul Lee$^1$
소속: $^1$Electrical Engineering, KAIST, $^2$Chemistry, KAIST

TP2-15  Fluctuation–Robust Extended Word–line and Extended Bit–line (EWEB) NAND Flash Memory
저자: Jang–Gn Yun, Il Han Park, Jong Duk Lee, Hyungcheol Shin, and Byung–Gook Park
소속: Inter–university Semiconductor Research Center and School of Electrical Engineering, Seoul National University

TP2-16  One–Time Programmable Nonvolatile Memory Device and Its Array Based on Metal–Insulator–Semiconductor Structure: Operation and Fabrication Method
저자: Seongjae Cho, Jung Hoon Lee, Won Bo Shim, Se Hwan Park, and Byung–Gook Park
소속: Inter–university Semiconductor Research Center and School of Electrical Engineering and Computer Science, Seoul National University
TP2-17 Scaling of Nano–Electro–Mechanical System (NEMS) Nonvolatile Memory Cells Based on Finite Element Analysis (FEA)
저자: Seung Hyeun Roh and Woo Young Choi
소속: Department of Electronic Engineering, Sogang University

TP2-18 A Novel Self–Aligned 4-bit SONOS-Type Non–Volatil Memory Cell with T–Gate and I–Shaped FinFET Structure and Low Current Sense Amplifier
소속: School of Electrical Engineering, Kookmin University

TP2-19 Charge Transport in Charge Trap Flash Memory Utilizing Gate Injection Switching Method
저자: Yujeong Seo1, Ho Myoung An1, Hee Dong Kim1, Yongjie Zhang1, In Rok Hwang2, Sa Hwan Hong2, Bae Ho Park2, and Tae Geun Kim1
소속: 1School of Electrical Engineering, Korea University, 2School of Physics, Konkuk University

TP2-20 Current Bistable Behavior of Polyimide Layer with In2O3 Nanocrystals Formed on Sapphire Substrate
저자: Dong Uk Lee1, Seon Pil Kim1, Dong Seok Han1, Eun Kyu Kim1, Min Soo Kim2, Won–Ju Cho3, and Young–Ho Kim3
소속: 1Quantum–Function Spinics Laboratory and Department of Physics, Hanyang University, 2Department of Electronic Materials Engineering, Kwangwoon University, 3Devisio of Materials Science and Engineering, Hanyang University

TP2-21 Characteristics of SONOS Embedded Flash Memory by Logic Gate Oxidation Method
저자: 선종원, 신희재, 박지환, 정희돈, 권영준, 김대일, 김남윤, 주인재, 박성근, 금동렬, 이윤중, 양택승, 한재원
소속: 동부하이텍 반도체부문 MF공정개발팀

TP2-22 Charge Trapping Characteristics of HfO2 Compared to Si3N4 Layer with Various Thicknesses for Non Volatile Flash Memory
저자: H. W. You and W. J. Cho
소속: Department of Electronic Materials Engineering, Kwangwoon University

L. Analog Design 분과

TP2-23 A CMOS Baseband Circuit with Digitally Controlled DC–offset Cancellation for Multi–band and Multi–mode Direct Conversion Receiver
저자: 유병주, 지한규, 박규상, 정덕균
소속: 서울대학교 반도체 공동 연구소 집적 시스템 설계 연구실

TP2-24 A Design of Wide Range and Fast Locking ADPLL
저자: 지한규, 유병주, 박규상, 김현창, 정덕균
소속: 서울대학교 반도체 공동 연구소 집적 시스템 설계 연구실

TP2-25 A Monitoring Circuit for the Random Variations of MOSFET Parameters Using
Time Delay Amplifier, VCDL and TDC  
저자: 권혜정, 이재승, 심재윤, 박홍준  
소속: Analog IC Systems Lab., Pohang University of Science and Technology

TP2-26  
A Low Power PDP Driver Interface Utilizing Idle Periods for Mass-Production  
저자: Sangcheol Lee, Kwangsun Yoon, Wookon Son, Hong Shik Moon, and Kuk Tae Hong  
소속: System IC Business Team, LG Electronics Inc.

TP2-27  
A Low Jitter PLL Design Using Voltage Supplied VCO  
저자: 김지현, 박용현, 이승현, 홍국태  
소속: LG전자 System IC MCS Gr. Circuit Core part

TP2-28  
Video Signal (YC) Mixing and Low Impedance Driving Circuit in Digital TV System  
저자: Jung-Suk Shim, Jae-Yup Lee, Jong-Chul Lim, Woo-Yol Lee, and Kirt Hong  
소속: Mixed Core Solution Group, System IC Business Team, LG Electronics Inc.

TP2-29  
Open-Loop Mode에서 동작하는 DLL을 사용한 고해상도 Time-to-Digital 변환기  
저자: 이형민, 신우열, Nan Xing, 김선권, 심태용, 홍기문, 김수환  
소속: 서울대학교 반도체공동연구소 집적시스템연구실

TP2-30  
A Low-Voltage Differential BGR with Reference Voltage Driver for High-Performance Pipelined ADC  
저자: 이현중, 이상훈, 우종관, 이상윤, 김동혁, 정덕균, 김수환  
소속: 서울대학교 반도체공동연구소 집적시스템설계연구실

TP2-31  
DisplayPort 1.1a Standard용 2.7Gb/s 송신기의 설계  
저자: 김두호, 박영석, 성창경, 김왕수, 임진수, 최우영  
소속: 연세대학교 전기전자공학과