

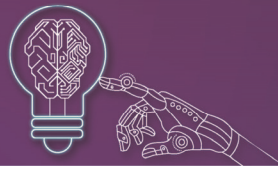
[FP] 포스터세션

2026년 1월 30일(금), 09:00~15:30
(공식발표시간: 14:00~15:30)

A. Interconnect & Package 분과

FP-001 ZONE 2-1 (5층, 로비)	CST Studio Suite를 활용한 MIPI D-PHY Re-Timer의 정적 열 분포 해석 및 Pin-Type 히트 싱크 효과 검증 최영수 알텐코리아
FP-002 ZONE 2-1 (5층, 로비)	Effects of Temperature and Pressure on Cu-to-Cu Hybrid Bonding Quality HeeJe Jo ¹ , YeoJin Kim ² , HyunDong Lee ² , and HooJeong Lee ¹ ¹ Department of Advanced Materials Science and Engineering, Sungkyunkwan University, ² Department of Smart Fab. Technology, Sungkyunkwan University
FP-003 ZONE 2-1 (5층, 로비)	Integrated Structural Defect Screening and Dynamic PAT for Simultaneous Quality and Productivity Optimisation in Semiconductor Package Backend Kim Hyo-Jun, Kim Jeong-Tae, Kim Kyu-Il, and Yoo Chang-Ho Keysight Technologies
FP-004 ZONE 2-1 (5층, 로비)	차세대 하이브리드 본딩을 위한 플라즈마 개질 구리/폴리머 계면의 화학적 기계적 연마 전찬수 ¹ , 강석경 ² , 김산하 ² , 김경민 ¹ ¹ 한국과학기술원 신소재공학과, ² 한국과학기술원 기계공학과
FP-005 ZONE 2-1 (5층, 로비)	SRAM 3D-V Cache 기술이 적용된 상업용 Cu/Cu 접합구조 차호일, 박경진, 박가현, 박윤창 나노종합기술원 분석평가센터
FP-006 ZONE 2-1 (5층, 로비)	Cu/Sn 마이크로 범프의 in-Situ TEM 가열 및 전도도 측정 서윤경, 박경진, 차호일, 박윤창 나노종합기술원 분석평가센터
FP-007 ZONE 2-1 (5층, 로비)	Mixed Ionic-Electronic Conductors-Based Radio-Frequency Switches with Interdigitated Channel Yongwoo Lee ¹ , Hyeongjun Kim ² , Kyungsun Kim ² , Haksoon Jung ¹ , Yurim Choi ¹ , Sungmin Eum ² , and Jimin Kwon ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Graduate School of Semiconductor Materials and Devices Engineering, UNIST
FP-008 ZONE 2-1 (5층, 로비)	상온 스테이지 면레이저 열압착 공정 및 에폭시 기반 접합 소재를 적용한 30 μ m 피치 범프 접합부의 금속간화합물 및 신뢰성 분석 이가은 ^{1,2} , 엄용성 ¹ , 최광문 ¹ , 신정호 ¹ , 이찬미 ¹ , 장기석 ¹ , 오진혁 ¹ , 김성철 ^{1,2} , 윤미리 ^{1,2} , 이승윤 ² , 최광성 ¹ , 주지호 ¹ ¹ 한국전자통신연구원, ² 국립한밭대학교 응용소재공학과
FP-009 ZONE 2-1 (5층, 로비)	하이브리드 본딩 Cu CMP 공정의 Dishing 개선 및 거동 분석 강수정, 김도균, 박경호 한국나노기술원 차세대패키지 TF
FP-010 ZONE 2-2 (5층, 로비)	iCVD 고분자 박막을 이용한 플라즈마-프리 하이브리드 본딩 김형준 ¹ , 송상호 ² , 오채은 ¹ , 김민주 ¹ ¹ 단국대학교 공과대학 전자전기공학부, ² 단국대학교 공과대학 고분자공학부 고분자공학과

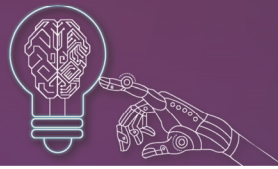
FP-011 ZONE 2-2 (5층, 로비)	Fabrication of a Two-Layer RDL with Sub-2 μm L/S and 5 μm Vias Using a Polyimide Dielectric Hae-Soo Jeong, Won-Youl Shin, Sang-Soo Kim, Hae-Seong Lee, Sung-Chun Moon, Seo-Rin Kim, and Kyung-Ho Park Advanced Packing TF, KANC
FP-012 ZONE 2-2 (5층, 로비)	유한요소해석법 기반 신축성 이방성 전도성 필름의 기계적 응력 및 변형 연구 김상현 ¹ , 장세윤 ² , 이준기 ² , 김병준 ¹ ¹ 한국공학대학교 신소재공학과, ² MiDAS H&T Korea
FP-013 ZONE 2-2 (5층, 로비)	Improvement of Interconnection Properties Using Ultra-Thin Al_2O_3 Film Deposited via ALD process Eun-Chae Noh, Eun-Su Jang, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University
FP-014 ZONE 2-2 (5층, 로비)	Light Energy Assisted Fine-Pitch u-Bump Interconnections with a Ni Layer 장은수, 노은채, 윤정원 충북대학교 신소재공학과
FP-015 ZONE 2-2 (5층, 로비)	Fan-Out 3D-Printed Packages with Embedded Curved Through-Hole Interconnections Haksoon Jung ¹ and Jimin Kwon ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Graduate School of Semiconductor Materials and Devices Engineering, UNIST
FP-016 ZONE 2-2 (5층, 로비)	Design Rule Development of Electrohydrodynamic Jet Printing for UCle Based High-Speed Interconnections Hyeongjun Kim ¹ , Yongwoo Lee ² , Seongju Kim ³ , and Jimin Kwon ^{1,2} ¹ Graduate School of Semiconductor Materials and Devices Engineering, UNIST, ² Department of Electrical Engineering, UNIST, ³ Department of Creative Convergence Engineering, Hanbat National University
FP-017 ZONE 2-2 (5층, 로비)	Sn/Cu/Sn 및 Sn/Ni/Sn 적층 프리폼을 이용한 Transient Liquid Phase 접합 특성 비교 연구 이동복, 윤정원 충북대학교 신소재공학과
FP-018 ZONE 2-2 (5층, 로비)	Enhancement of Cu-Film Bulk CMP Performance Using a Zwitter-Ion for Achieving High Polishing Rate and Low Surface Roughness Jin-Woo Kim ¹ , Jea-Gun Park ² , and Jin-Sub Park ^{1,2} ¹ Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ² Department of Nanoscale Semiconductor Engineering, Hanyang University
FP-019 ZONE 2-2 (5층, 로비)	Computational Investigation of DEA and ND Pathways for Selective Bond Dissociation in 1-Bromo-4-Chlorobenzene and Dimethyldimethoxysilane based on Electron-Molecule Collision Theory Hayun Kim, Wonchul Kee, Soyeong Heo, and Hyun-Dam Jeong Department of Chemistry, Chonnam National University
FP-020 ZONE 2-2 (5층, 로비)	고신뢰성 하이브리드 접합을 위한 Cu-Mn 합금 기반 금속-절연체 직접 접합 연구 이용규 ¹ , 변창우 ² , 김희수 ² , 김병준 ¹ ¹ 한국공학대학교 신소재공학과, ² 차세대융합기술원 차세대전자재료연구실
FP-021 ZONE 2-2 (5층, 로비)	Molecular Free-Space Engineering of O-CASILON Structures for Ultra-Low-k Dielectric Materials in Advanced Semiconductor Devices Wonchul Kee ¹ , Hyun-Sung Yoon ¹ , Seungyong Baek ¹ , Hayun Kim ¹ , Seong-Bin Park ² , Jung-Hyung Kim ² , and Hyun-Dam Jeong ¹ ¹ Department of Chemistry, Chonnam National University, ² KRISS



FP-022 ZONE 2-2 (5층, 로비)	Digital Twin Framework for Bias-Dependent Material Property Prediction in IGBT Devices Na-Yeon Choi, Tae-Woon Yoon, and Sung-Uk Zhang Digital Twin Laboratory amd Center for Brain Busan 21 Plus Program, Dong-Eui University
FP-023 ZONE 2-2 (5층, 로비)	Electro-Thermal Performance Prediction and Design Parameter Analysis of Cu-Clip Bonded TO-220 Package Using PIML Na-Yeon Choi, Tae-Woon Yoon, and Sung-Uk Zhang Digital Twin Laboratory amd Center for Brain Busan 21 Plus Program, Dong-Eui University
FP-024 ZONE 2-2 (5층, 로비)	Topological Metal Phosphide Nanowires for Next-Generation Low-Resistivity Interconnects Jieun Seo and HyeukJin Han Department of Materials Science and Engineering, Sungshin Women's University
FP-025 ZONE 2-2 (5층, 로비)	Synthesis and Transport Properties of Single-Crystal CoSi Nanowires for Interconnect Applications Eugene Cho and Hyeuk Jin Han Department of Material Science and Engineering, Sungshin Women's University
FP-026 ZONE 2-2 (5층, 로비)	Development of H₂-Based ALD Ru Passivation to Overcome Sputtering Limitations for Low-Temperature Cu Bonding Tae Hun Jeon and Jong Kyung Park Department of Semiconductor Engineering, Seoul National University of Science & Technology
FP-027 ZONE 2-2 (5층, 로비)	Ascorbic Acid Vapor Assisted-Cu Direct Bonding for Oxidation Prevention in 3D Semiconductor Packaging Jeehoo Na ^{1,2} , Eunhye Lee ¹ , Dongwoo Lee ² , and Tae-Ik Lee ¹ ¹ Advanced Packaging Integration Center, Regional Industry Innovation Department (Growth Engine), KITECH, ² Data-driven Materials Design Lab, School of Mechanical Engineering, Sungkyunkwan University
FP-028 ZONE 2-2 (5층, 로비)	Ru Silicide 기반 통합형 플러그 구조의 저온 구현 및 신뢰성 평가 최서연, 성동윤, 이지윤, 홍슬기 서울과학기술대학교 지능형반도체공학과
FP-029 ZONE 2-2 (5층, 로비)	Interfacial Recovery Behavior of Cu-Cu Bonding under Dishing-Induced Nonplanarity Seung Hwan Oh and Seul Ki Hong Department of Semiconductor Engineering, Seoul National University of Science & Technology

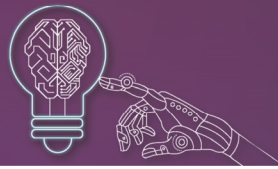
A. Interconnect & Package 분과

FP-030 ZONE 3 (6층, 로비)	pV3D3 기반 금속-고분자 하이브리드 본딩 구조 제안 및 신뢰성 평가 성동윤, 이지윤, 최서연, 홍슬기 서울과학기술대학교 지능형반도체공학과
FP-031 ZONE 3 (6층, 로비)	Low-Temperature Curable KrF-Photoimageable Ultra Low-k Material for AI Semiconductor Ultra-Large Packaging Hyun-Sung Yoon, Singha Pronab Kumar, and Hyun-Dam Jeong Department of Chemistry, Chonnam National University
FP-032 ZONE 3 (6층, 로비)	Plasma-Free KOH-Based Surface Activation for Enhanced SiO ₂ -SiO ₂ Bonding Strength Youngjoon Cha ¹ and Rino Choi ² ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-033 ZONE 3 (6층, 로비)	대면적 반도체 패키지의 휨 변형 및 응력 제어를 위한 열계면 재료의 최적 패턴 디자인 해석 연구 선동진 ¹ , 이규재 ³ , 김도현 ³ , 김병준 ² ¹ 한국공학대학교 IT반도체융합공학과, ² 한국공학대학교 신소재공학과, ³ 엔트리움 제품개발팀
FP-034 ZONE 3 (6층, 로비)	Low-Thermal-Budget Cu Direct Bonding via Microwave Annealing Min Kim, Gyohyuk Gu, Byeonghun Lee, Hoesung Ha, and Hyunsang Hwang Department of Materials Science and Engineering, POSTECH
FP-035 ZONE 3 (6층, 로비)	Effect of TiC Nanoparticle and Soldering Process on the Microstructure and Mechanical Properties of Sn-Bi/Sn-Ag-Cu Hybrid Solder Joints Kwan Soo Lim and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University
FP-036 ZONE 3 (6층, 로비)	저온 Cu-Cu 접합에서 Ag 층이 계면 반응 및 확산 거동에 미치는 영향 (Effect of Ag Layer on Interfacial Reaction and Diffusion Behavior in Low-Temperature Cu-to-Cu Bonding) 백송연, 윤정원 Department of Advanced Materials Engineering, Chungbuk National University
FP-037 ZONE 3 (6층, 로비)	Thermal Reliability Evaluation of Vapor Chamber Considering Plastic Deformation for Semiconductor Packaging Jung-Soo Park ¹ , Jeong-Hyeon Park ¹ , Kyeong-Bin Kim ¹ , Jaechoon Kim ⁴ , Hyeongjun Jeon ⁴ , and Eun-Ho Lee ^{1,2,3} ¹ Department of Mechanical Engineering, Sungkyunkwan University, ² Department of Smart Fab. Technology, Sungkyunkwan University, ³ Department of Intelligent Robotics, Sungkyunkwan University, ⁴ Division of the Semiconductor R&D Center, Samsung Electronics Co., Ltd.
FP-038 ZONE 3 (6층, 로비)	High-Performance Interconnects Using Metal-Interlayer-Carbon Composites for Future Device Scaling Junseong Bae ^{1,2} , Chang-Seok Lee ³ , Minwoo Lee ^{1,2} , Jinsu Choi ^{1,2} , Shubham Patil ¹ , Batyrbek Alimkhanuly ¹ , Taemin Sim ^{1,2} , Heegoo Kim ³ , Sangwon Kim ³ , and Seunghyun Lee ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Kyung Hee University, ³ Device Research Center, Samsung Electronics Co., Ltd.
FP-039 ZONE 3 (6층, 로비)	Bottom-Up Filling of TSVs Achieved with a Tris-Ammonium-Based Suppressor as a Single Additive Haejin Kwak, Yun Ha, Hui Won Eom, Seolim Yoon, and Myung Jun Kim School of Chemical Engineering, Sungkyunkwan University



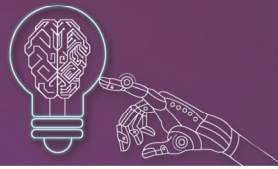
FP-040 ZONE 3 (6층, 로비)	Cu-Ag Electrodeposition in MSA and NH₄OH Electrolytes with Nitrate Ions Yubin Kim, Jiwoo Oh, Dong Kun Cha, and Myung Jun Kim School of Chemical Engineering, Sungkyunkwan University
FP-041 ZONE 3 (6층, 로비)	High-Speed TGV Filling by Cu Electrodeposition with Alkylammonium and Bromide Ions Hui Won Eom, Haejin Kwak, Seonjin Yang, and Myung Jun Kim School of Chemical Engineering, Sungkyunkwan University
FP-042 ZONE 3 (6층, 로비)	Role of Cl⁻ ion in CTAB-Mediated Cu Electrodeposition for TSV Bottom-Up Filling Yun Ha, Hui Won Eom, Haejin Kwak, Seolim Yoon, and Myung Jun Kim School of Chemical Engineering, Sungkyunkwan University
FP-043 ZONE 3 (6층, 로비)	Pulse Electrodeposition of Sn-Bi Low-Temperature Solder Sung Min Lee, Yunseo Choi, Jungyeon Choi, and Myung Jun Kim School of Chemical Engineering, Sungkyunkwan University
FP-044 ZONE 3 (6층, 로비)	DIC와 유한요소해석을 활용한 패키징 전후 SiC 칩의 물성 변화 분석 이윤재, 김동현, 장성욱 동원대학교 디지털 트윈 연구소, Center for Brain Busan 21 Plus Program
FP-045 ZONE 3 (6층, 로비)	Overcoming Wire Loop Height Limitations in Ultra-Thin Packages through Edge-Insulated Epoxy Coating Technology Jaebum Choi ^{1,2} , Jongwon Lee ^{1,2} , and Jae Woo Lee ² ¹ Package Development, SK hynix, ² Department of Electronics and Information Engineering, Korea University
FP-047 ZONE 3 (6층, 로비)	미세 기판 Core 소재에 따른 신호 손실 특성 분석 정청하, 강지웅, 박창준, 김구성 강남대학교 전자패키지연구소
FP-048 ZONE 3 (6층, 로비)	Enhancing SiO₂ Bond Strength for Cu/SiO₂ Hybrid Bonding via H₂O-Assisted Plasma Treatment Gyeongyeol Lee ^{1,2} and Rino Choi ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-050 ZONE 3 (6층, 로비)	Theoretical Screening of Co- & Mo- Binary Alloys for Beyond Cu Metal Interconnects Seong Woo Kang and Jiwon Chang Department of System Semiconductor Engineering, Yonsei University
FP-051 ZONE 3 (6층, 로비)	Supervia 적용을 위한 ALD 공정 조건이 Ru/SiO₂ 계면접착에너지에 미치는 영향 이수연 ¹ , 정대윤 ¹ , 이원일 ² , Shixian Ha ² , 남창용 ^{2,3} , 박영배 ¹ ¹ 국립경국대학교 반도체신소재공학과, ² Department of Materials Science and Chemical Engineering, Stony Brook University, ³ Center for Functional Nanomaterials, Brookhaven National Laboratory
FP-052 ZONE 3 (6층, 로비)	Modeling Surface Conductivity in Topological Semimetal for Beyond-Cu Interconnect via Green's Function and Fermi's Golden Rule Analysis Sukhyeong Youn ¹ and Jiwon Chang ² ¹ Department of System Semiconductor Engineering, Yonsei University, ² Department of Materials Science & Engineering, Yonsei University
FP-053 ZONE 3 (6층, 로비)	Hybrid Solder Joints Using Sn-Bi-In Paste and SAC305 Balls: Reflow Temperature Effects on Reliability and Fracture Modes HooJeong Lee ¹ , JuHyeon Kim ² , HaeGeun Lee ² , and Jiwon Kang ² ¹ Department of Smart Fab. Technology, Sungkyunkwan University

FP-054 ZONE 3 (6층, 로비)	Oxide Reduction Strategies for Reliable Cu-Cu Hybrid Bonding Siye Lee ¹ , Injoo Kim ¹ , Minji Kang ¹ , Hyein Jin ¹ , Soohyun Ko ² , and Sungdong Kim ² ¹ Department of Mechanical Design and Robot Engineering, Seoul National University of Science & Technology, ² Department of Mechanical System Design Engineering, Seoul National University of Science & Technology
FP-055 ZONE 3 (6층, 로비)	Atomic Layer Deposition of Ru as a Multifunctional Layer for Cu TGV on Glass Interposer Na-Gyeong Kang, Min-Ji Ha, Eun-Su Chung, and Ji-Hoon Ahn Department of Materials Science and Chemical Engineering, Hanyang University
FP-056 ZONE 3 (6층, 로비)	Impact of Queue Time and Plasma Activation on SiO₂ Bonding in Hybrid Bonding Injoo Kim ¹ , Siye Lee ¹ , Hyein Jin ¹ , Minji Kang ¹ , Jaemin Yu ² , and Sungdong Kim ² ¹ Department of Mechanical Design and Robot Engineering, Seoul National University of Science & Technology, ² Department of Mechanical System Design Engineering, Seoul National University of Science & Technology
FP-057 ZONE 3 (6층, 로비)	A Simulation Study on Electrodeposition Profiles of Multiscale Patterns Using COMSOL Multiphysics Han Geun Lee ¹ , Jun Yeong Lee ² , and Jae Yong Song ^{1,2,3} ¹ Graduate School of Materials Science and Engineering, POSTECH, ² Graduate School of Semiconductor Technology, POSTECH, ³ Department of Materials Science and Engineering, Department of Semiconductor Engineering, POSTECH
FP-058 ZONE 3 (6층, 로비)	Stepwise Microstructural Evolution of Sn-Ag-Cu/Ni/Cu Solder Bump Interfaces under Temperature Ramp : In-situ TEM/4D-STEM Analysis Jin Young Kim ¹ , Ye Rim Kang ¹ , Yun Jae Jung ¹ , Ji Min Lee ¹ , Hoil Cha ² , Yun Chang Park ² , Yoon Kyung Seo ² , and Young Heon Kim ¹ ¹ Department of Analytical Science and Technology, Graduate School of Analytical Science and Technology (GRAST), Chungnam National University, ² NNFC
FP-059 ZONE 3 (6층, 로비)	Characterization of Multi-Layer RDL with Polymeric Dielectrics for Interposer Applications Minji Kang ¹ , Sunghwan Joo ² , Injoo Kim ¹ , Siye Lee ¹ , Hyein Jin ¹ , and Sungdong Kim ³ ¹ Department of Mechanical Design and Robot Engineering, Seoul National University of Science & Technology, ² Department of Electrical and Information Engineering, Seoul National University of Science & Technology, ³ Department of Mechanical System Design Engineering, Seoul National University of Science & Technology
FP-060 ZONE 3 (6층, 로비)	반도체 CMP 공정용 시너지스트 폴리머를 이용한 세리아 슬러리의 연마성능 및 안정성 평가에 관한 연구 조재영 ¹ , 황소희 ² , 김채연 ¹ , 정재영 ³ , 김운중 ^{1,2} ¹ 한남대학교 나노반도체공학과, ² 한남대학교 화학과, ³ 한국전자통신연구원
FP-061 ZONE 3 (6층, 로비)	High Modulus Low-k Films Deposited by Plasma-Enhanced Chemical Vapor Deposition Using Tris(Trimethylsiloxy)Silane Precursor Jaeyeon Kim, Kyubeom Bae, Chanyong Seo, Dahyun Kim, Donghee Lee, and Donggeun Jung Department of Physics, Sungkyunkwan University
FP-062 ZONE 3 (6층, 로비)	Microstructural Characterization and Property of Electrodeposited Bi-Sn-In Low-Melting-Point Solder Dae Yeon Kim, Ji Min Lee, Jin Young Kim, Ye Rim Kang, and Young Heon Kim Department of Analytical Science and Technology, Graduate School of Analytical Science and Technology, Chungnam National University
FP-063 ZONE 3 (6층, 로비)	Thermal Management of Epoxy/Benzoxazine Composite and Its Potential Uses in Power Module System Kyu Hwan Kwon ¹ , Hui Tae Yang ² , Sang Wook Nam ³ , Ji Min Jeong ¹ , Ji Won Lee ¹ , and Nam Il Kim ¹ ¹ Chemical Engineering Composite Material Lab, Department of Chemical Engineering, Hannam University, ² Chemical Engineering Composite Material Lab, Department of Nano-Semiconductor Engineering, Hannam University, ³ NAON C.S Inc.



FP-064 ZONE 3 (6층, 로비)	탄산세륨 전구체 하소 조건에 따른 세리아 슬러리의 연마특성 연구 장수성 ¹ , 황소희 ¹ , 안세영 ² , 정재영 ³ , 김운중 ¹ ¹ 한남대학교 생명나노과학대학 화학과, ² (주)엠에스머트리얼즈, ³ 한국전자통신연구원
FP-065 ZONE 3 (6층, 로비)	초박형 Si Capacitor 실장을 위한 고신뢰성 초미세 피치 공정 Highly Reliable Ultra-Fine Pitch Assembly Process for Ultra-Thin Si Capacitor 김다정, 오철민 한국전자기술연구원 융복합전자소재연구센터
FP-066 ZONE 3 (6층, 로비)	Molecular Dynamics Study of Electromigration in Cu Interconnects for Hybrid Bonding Applications Junhyeok Park, Juheon Kim, and Hayoung Chung Department of Mechanical Engineering, UNIST
FP-067 ZONE 3 (6층, 로비)	AS-ALD 공정 억제제의 구조적 특성에 따른 저해 성능 평가: 분자동역학 시뮬레이션 연구 안현승 ¹ , SUMMAL ² , 이한보람 ² , 김병조 ¹ ¹ 울산과학기술원 반도체 소재 • 부품 대학원, ² 인천대학교 신소재공학과
FP-068 ZONE 3 (6층, 로비)	High-k Dielectric-Based Low-Temperature Wafer-Level Bonding for Advanced 3D Integration Wooseong Son ¹ , Sunbum Kim ² , Kyoungyeon Min ¹ , Dugkyu Han ² , Jaeho Lee ² , Sehyeon Choi ² , Kangbaek Seo ¹ , and Changhwan Choi ^{1,2} ¹ Department of Semiconductor Engineering, Hanyang University, ² Division of Materials Science and Engineering, Hanyang University
FP-069 ZONE 3 (6층, 로비)	Multi-layer Cu/Polymer RDLs Fabrication for Glass Interposer Hyein Jin ¹ , Jeongmin Yim ² , Bokyung Kong ³ , JungHyun Noh ³ , Doowon Lee ³ , Jaewon Lee ³ , Kwangchul Park ³ , Eunho Choi ³ , and Sungdong Kim ² ¹ Department of Mechanical Design and Robot Engineering, Seoul National University of Science & Technology, ² Department of Mechanical System Design Engineering, Seoul National University of Science & Technology, ³ Corning Inc.
FP-071 ZONE 3 (6층, 로비)	Adhesion Enhancement of Glass-Metal Interface Using Metal Oxides for TGV Packaging Applications Sunbum Kim ¹ , Dugkyu Han ¹ , Kyoungyeon Min ² , Sehyeon Choi ¹ , Jaeho Lee ¹ , Kangbaek Seo ² , Taehun Kim ² , Seohyeon Cho ² , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-072 ZONE 3 (6층, 로비)	Enhanced Low-Temperature Cu-Cu Bonding via Sequential N ₂ /Ar Plasma-Treated Cobalt Passivation Layer for Vertical 3D Interconnects Jaeho Lee ¹ , Sunbum Kim ¹ , Kyoungyeon Min ² , Dugkyu Han ¹ , Sehyeon Choi ¹ , Kangbaek Seo ² , Wooseong Son ² , Taehun Kim ² , Seohyeon Cho ² , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-073 ZONE 3 (6층, 로비)	Low-Temperature, Low-Pressure Polymer Hybrid Bonding via PSPI Plasma Activation: Toward Low-Void Interfaces Kyoungyeon Min ¹ , Sun-Bum Kim ² , Dug-Kyu Han ² , Jae-Ho Lee ² , Se-Hyeon Choi ² , Kang-Baek Seo ¹ , and Changhwan Choi ^{1,2} ¹ Department of Semiconductor Engineering, Hanyang University, ² Division of Materials Science and Engineering, Hanyang University
FP-074 ZONE 3 (6층, 로비)	Interfacial Reliability and Process Optimization of Fluxless Bonding for Fine-Pitch Semiconductor Interconnections Yun-Chan Kim ^{1,2} , Dong-yurl Yu ^{1,2} , Junhyuk Son ¹ , Tae-Young Lee ³ , Dong-jin Byun ² , and Junghwan Bang ¹ ¹ Regional Industry Innovation Department, KITECH, ² Department of Materials Science and Engineering, Korea University, ³ Shared Equipment Support Center, Tech University of Korea

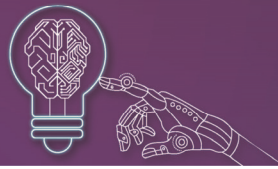
FP-075 ZONE 3 (6층, 로비)	Process Optimization of Cu/SiO₂ Hybrid Bonding by Controlling via Size and CMP Dishing under Fixed Edge Geometry Sehyeon Choi ¹ , Sunbum Kim ¹ , Kyoungyeon Min ² , Dugkyu Han ¹ , Jaeho Lee ¹ , Kangbaek Seo ² , Wooseong Son ² , Taehun Kim ² , Seohyeon Cho ² , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-076 ZONE 3 (6층, 로비)	FEM-DIC 연계 기반 반도체 패키지 솔더 접합부 신뢰성 및 전역 힘 다중 스케일 평가 기법 최성규 ^{1,2} , 정나경 ¹ , 이은호 ² , 이태익 ¹ ¹ 한국생산기술연구원 첨단반도체패키징집적센터(APIC), ² 성균관대학교 지능형로봇학과
FP-077 ZONE 3 (6층, 로비)	고집적 프로브 카드의 신호 무결성 향상을 위한 MLC 기판 내 신호 선 임계 이격 거리 최적화 지동준 ¹ , 이두원 ² ¹ 국립공주대학교 스마트모빌리티공학과, ² 국립공주대학교 반도체정보공학전공
FP-078 ZONE 3 (6층, 로비)	Effects of Cu Surface Roughness and Signal Characteristics in PSPI-Based Redistribution Layer Dug Kyu Han ¹ , Sun Bum Kim ¹ , Kyoung Yeon Min ² , Kang Baek Seo ² , Jae Ho Lee ¹ , Sae Hyun Choi ¹ , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-079 ZONE 3 (6층, 로비)	Low-Temperature Area-Selective Cobalt Deposition for Advanced Hybrid Bonding Applications Min Seok Kim ¹ , Hyun Ji Son ² , Minyoung Jo ² , Sungdong Kim ³ , and Jeong Hwan Han ¹ ¹ Department of Materials Science and Engineering, Seoul National University of Science & Technology, ² UP Chemical Co., Ltd., ³ Department of Mechanical System Design Engineering, Seoul National University of Science & Technology
FP-080 ZONE 3 (6층, 로비)	고성능 컴퓨팅을 위한 액상 침지형 방열 패키지의 성능 평가 Jaehyuk An, Simon Cho, Hyoseo Choi, Jian Lee, Minji Choi, Hojong Kwak, Sewoong Ahn, Byeongyu Kim, Anbumalar Murugan, and Young-Jae Min Department of Electric and Electronic Engineering, Halla University



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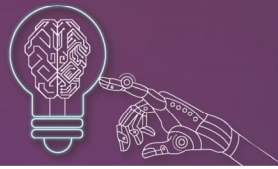
FP-081 ZONE 1 (4층, 로비)	Multichannel Encoding via Bimodal HfO₂-Based Volatile Memristors for High-Dimensional Reservoir Computing Yuseong Jang ^{1,2,3} and Hee-dong Kim ^{1,2,3} ¹ Department of Semiconductor Systems Engineering, Sejong University, ² Convergence Engineering for Intelligent Drone, Sejong University, ³ Institute of Semiconductor and System IC, Sejong University
FP-082 ZONE 1 (4층, 로비)	Noise Reduction in WSe₂ Phototransistors for Enhanced Photodetection Sunggyu Ryoo ¹ , Jinwoo Sim ¹ , Taehee Kim ² , Seungjun Jeong ³ , Juntae Jang ¹ , Daeheum Cho ³ , Kyungjune Cho ⁴ , and Takhee Lee ¹ ¹ Department of Physics and Astronomy, Seoul National University, ² Korean Minjok Leadership Academy, ³ Department of Chemistry and Green-Nano Materials Research Center, Kyungpook National University, ⁴ Convergence Research Center for Solutions to Electromagnetic Interference in Future-Mobility, KIST
FP-083 ZONE 1 (4층, 로비)	Oxygen Plasma-Engineered MoS₂ Synaptic Transistor Enabling Optoelectronic Physical Reservoir Computing Ojun Kwon ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-084 ZONE 1 (4층, 로비)	Light-Stimulated Synaptic Plasticity of PN Heterojunction-Based Te/InGaZnO with Al₂O₃ Passivation Layer Shinhoi Kim ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-085 ZONE 1 (4층, 로비)	Localized Trap Engineering in Oxygen-Rich IGZO Synaptic Transistors for Reservoir Computing Applications Minyoung Choi ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-086 ZONE 1 (4층, 로비)	Graphene-Gated Te-MoS₂ CMOS for Steep-Slope and Energy-Efficient Logic Circuits Yeoneun Kwon ^{1,2} , Ojun Kwon ^{1,2} , and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-087 ZONE 1 (4층, 로비)	Charge Trap Engineering for Tunable Synaptic Plasticity in Two-Dimensional MoS₂ Neuromorphic Transistors Yujung Kim ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-088 ZONE 1 (4층, 로비)	2D Tellurium Memtransistor Engineered by Interfacial TiO_{2-x} Layer Kyungmin Lee ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
FP-089 ZONE 1 (4층, 로비)	A New Paradigm of Carbon Nanotube Transistors: Multi-Valued Logic through Vertical Stacking Seonghyeon Jeong, Hanbin Lee, So-Jeong Park, Jeong Yeon Im, Ji Won Park, Youngmin Kim, Dae Hwan Kim, Yoon Jung Lee, and Sung-Jin Choi School of Electronics and Electrical Engineering, Kookmin University

FP-090 ZONE 1 (4층, 로비)	Facile Interface Engineering based on Two-Dimensional MoS_xTe_y Alloyed Layer for High-Performance MoS_2 Transistors Yedam Kim ^{1,2} and Byungjin Cho ^{1,2} ¹ Department of Advanced Materials Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
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FP-095 ZONE 1 (4층, 로비)	End-to-End Process-to-Application Demonstration of Variability Engineered Underlap CNT Network FETs for PUFs Ji Won Park ¹ , Hanbin Lee ¹ , So-Jeong Park ¹ , Jeong Yeon Im ¹ , Seonghyeon Jeong ¹ , Youngmin Kim ¹ , Dae Hwan Kim ¹ , Yoon Jung Lee ¹ , Min-Ho Kang ² , and Sung-Jin Choi ¹ ¹ School of Electronics and Electrical Engineering, Kookmin University, ² Department of Nanoprocess, NNFC
FP-096 ZONE 1 (4층, 로비)	Substrate-Induced Doping Modulates Double-Resonant Scattering in Bilayer Graphene Jiwon Song ^{1,2} , Chanwoo Kim ² , and Hyuksang Kwon ^{1,2} ¹ Chungnam National University Graduate School of Analytical and Technology, ² KRISS
FP-097 ZONE 1 (4층, 로비)	Ion-Gel-Gated 2D Transistor with Programmable Synaptic Dynamics for Edge Reservoir Computing Hanbin Cho, Gilhwan Do, Junseo Shin, and Joonki Suh Department of Chemical and Biomolecular Engineering, KAIST
FP-098 ZONE 1 (4층, 로비)	Wake-Up-Free Ferroelectric HLO for Negative Capacitance Field-Effect Transistors (NCFET) via Aqueous Route Shahid Hussain ¹ , Pavan Pujar ² , and Sunkook Kim ¹ ¹ School of Advanced Materials Science and Engineering, Sungkyunkwan University, ² Department of Ceramic Engineering, Indian Institute of Technology (BHU) Varanasi
FP-099 ZONE 1 (4층, 로비)	Understanding the Interfaces in Dimension-Limited Self-Alignment for Highly Ordered Carbon Nanotube Monolayers Yehyun Shin ¹ , Sangmin Eom ¹ , Sungmin Eum ¹ , Haksoon Jung ² , Seunghun Baek ¹ , Byungjo Kim ¹ , and Jimin Kwon ^{1,2} ¹ Graduate School of Semiconductor Materials and Devices Engineering, UNIST, ² Department of Electrical Engineering, UNIST



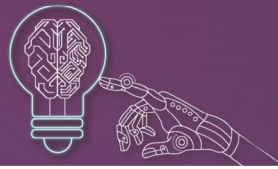
FP-100 ZONE 1 (4층, 로비)	Oxygen Plasma-Induced Enhancement of Ferroelectric Materials Polarization Chaeyeon Jung ¹ , Hyunwoo Shim ² , and Taesung Kim ² ¹ Advanced Institute of Nanotechnology, Sungkyunkwan University, ² School of Semiconductor Convergence Engineering, Sungkyunkwan University, ³ School of Mechanical Engineering, Sungkyunkwan University
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FP-105 ZONE 1 (4층, 로비)	Unsorted Carbon Nanotube Synaptic Transistors by Reversible Ion-Doping Donghee Shin and Beomjin Jeong School of Chemical Engineering, Pusan National University
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FP-119 ZONE 1 (4층, 로비)	Quantifying Effects of Bulk and Interface in 2D Tin Halide Perovskite Field-Effect Transistors Jaeyong Woo ¹ , Seongmin Ko ¹ , Youngmin Song ¹ , Yeeun Kim ¹ , Sunggyu Ryoo ¹ , Keehoon Kang ² , and Takhee Lee ¹ ¹ Department of Physics and Astronomy, Seoul National University, ² Department of Materials Science and Engineering, Seoul National University



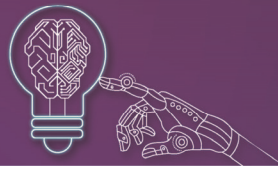
FP-120 ZONE 1 (4층, 로비)	High-Performance Polyimide-Based RRAM with Robust High-Temperature Retention for Harsh Environments Junyeong Jang ¹ , Changwoo Pyo ¹ , and Myungsoo Kim ^{1,2} ¹ Graduate School of Semiconductor Materials and Devices Engineering, UNIST, ² Department of Electrical Engineering, UNIST
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FP-123 ZONE 1 (4층, 로비)	Tuning the Band Structure and Defect Properties of Bi₂Se₃ Thin Films via Sulfur Substitution Junha Park ^{1,2} , Cheolho Jeon ^{1,3} , and Mun Seok Jeong ^{2,3} ¹ Research Center of Materials Analysis, KBSI, ² Department of Semiconductor Engineering, Hanyang University, ³ Department of Physics, Hanyang University
FP-124 ZONE 1 (4층, 로비)	Nanoscale Defect and Structural Analysis of Van der Waals Semiconductor via Tip-Enhanced Spectroscopy Ji-hong Kim, Hyeong Chan Suh, Dong Hyeong Kim, Do Hyeon Lee, Dong-Joon Yi, and Mun Seok Jeong Department of Physics, Hanyang University
FP-125 ZONE 1 (4층, 로비)	Integration of HfO₂ Ferroelectric and WSe₂ Semiconductor for Efficient Neuromorphic Computing Jae Hyun Kim ^{1,2} , Yohan Lee ^{1,2} , Jihoon Huh ^{1,2} , Jeongyeol Park ^{1,2} , Seung Ji Lo ^{1,2} , Narae Lee ^{1,2} , Jaehyeong Park ¹ , and Moonsang Lee ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-126 ZONE 1 (4층, 로비)	Hierarchical Porous Cu Nanosheet Film-Based Internal Structural Modification toward Multifunctional Triboelectric Nanogenerator Applications Dae-Hong Kim ¹ , Ju-Hyoung Yu ² , Cheoljae Lee ³ , Min-Young Seo ¹ , Seungyeon Kim ¹ , Hyeonji Joo ¹ , Young-Seok Song ¹ , Sukang Bae ^{4,5} , Ju-Hyuck Lee ³ , Seoung-Ki Lee ² , and Tae-Wook Kim ^{1,4} ¹ Department of Flexible and Printable Electronics, LANL-JBNU Engineering Institute-Korea, Jeonbuk National University, ² School of Materials Science and Engineering, Pusan National University, ³ Department of Energy Science and Engineering, DGIST, ⁴ Department of JBNU-KIST Industry-Academia Convergence Research, Jeonbuk National University, ⁵ Functional Composite Materials Research Center, Institute of Advanced Composite Materials, KIST
FP-127 ZONE 1 (4층, 로비)	Drain-Field-Switchable Ambipolar Synaptic Devices based on Bulk WSe₂ for Efficient Neuromorphic Computing Seung-Ji Lo ^{1,2} , Jihoon Huh ^{1,2} , Jeongyeol Park ^{1,2} , Yohan Lee ^{1,2} , Narae Lee ^{1,2} , Jae Hyun Kim ^{1,2} , Jaehyeong Park ¹ , and Moonsang Lee ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
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FP-129 ZONE 1 (4층, 로비)	Multi-Dimensional Metal Nanosheet-Nanowire Networks as Porous Electrodes for Low-Voltage Vertical Organic Transistors Young-Seok Song ¹ , Dae-Hong Kim ¹ , Sanghun Oh ¹ , and Tae-Wook Kim ^{1,2} ¹ Department of Flexible and Printable Electronics, Jeonbuk National University, ² Department of JBNU-KIST Industry-Academia Convergence Research, Jeonbuk National University
FP-130 ZONE 1 (4층, 로비)	Switching Dynamics and Floating-Gate Charge Trapping Mechanism in CIPS-Based Ferroelectric Field-Effect Transistors Young Joo Yu, Seungho Bang, Chaewon Lee, and Mun Seok Jeong Department of Physics, Hanyang University
FP-131 ZONE 1 (4층, 로비)	Organic Phototransistor for NIR Detection Using Y6 Bung Chan Ko, HwaPyeong Noh, Swarup Biswas, and Hyeok Kim School of Electrical and Computer Engineering, Center for Smart Sensor System of Seoul, University of Seoul
FP-132 ZONE 1 (4층, 로비)	Low-Temperature Lateral Growth of Tellurium Using Atomic Layer Deposition for High-Performance p-Type Thin-Film Semiconductors Heeseo Kim, Dai Cuong Tran, Taeun Kim, Heemin Kang, and Myung Mo Sung Department of Chemistry, Hanyang University
FP-133 ZONE 1 (4층, 로비)	자기-억제 메커니즘 기반의 대비 향상형 레티노모픽 인-센서 컴퓨팅 백주환, 강동호 광주과학기술원 전기전자컴퓨터공학부
FP-134 ZONE 1 (4층, 로비)	Stochastic Optical Programming in 2D Heterostructure and Its Application for True Random Number Generator Yuseok Choi and Dongho Kang Department of Electrical Engineering and Computer Science, GIST
FP-135 ZONE 1 (4층, 로비)	Comparison of p-Type Performance in Monolayer WSe₂ FETs Effects of Contact Geometry and UV-Ozone Treatment Method Daeyeon Kim and Juwon Lee Department of Electrical and Computer Engineering, Sungkyunkwan University
FP-136 ZONE 1 (4층, 로비)	Thermoelectric Generators Using Electrochemically Exfoliated MoS₂ with Enhanced Electrical Conductivity Eun Go ^{1,2} , Yuntaek Cho ^{1,3} , Seungjun Chung ² , Keehoon Kang ³ , and Kyungjune Cho ¹ ¹ Convergence Research Center for Solutions to Electromagnetic Interference in Future-mobility, KIST, ² School of Electrical Engineering, Korea University, ³ Department of Materials Science and Engineering, Research Institute of Advanced Materials, Seoul National University
FP-137 ZONE 1 (4층, 로비)	Hybrid Copper and Silver Nanosheet Films with Hierarchical Architecture for EMI Shielding Performance Sanghun Oh ¹ , Dae-Hong Kim ² , Young-Seok Song ² , Seungyeon Kim ² , Min-Young Seo ² , and Tae-Wook Kim ^{1,2} ¹ Department of Flexible and Printable Electronics, Jeonbuk National University, ² Department of JBNU-KIST Industry-Academia Convergence Research, Jeonbuk National University
FP-139 ZONE 1 (4층, 로비)	120keV급 고정밀 이온주입 장치 제작 및 이온빔 특성 연구 길재근, 김범석, 김명진 (주)라드피온 빔응용사업부
FP-140 ZONE 1 (4층, 로비)	Crosstalk-Free Skin-Compatible Stacked Organic Photodetectors for Motion-Robust Wearable Oximetry Jaebin Jeong, Jae-Hyun Kim, and Sungjun Park Department of Intelligence Semiconductor Engineering, Ajou University



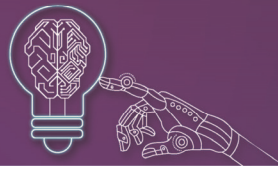
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FP-145 ZONE 1 (4층, 로비)	Engineering Second Threshold Voltage via Bare Aluminum Deposition on IGZO Yu-Jun Jeong ¹ , Hyeon Jung Park ² , Yeong Soe Kim ¹ , Sae Hyun Jeong ³ , Hong Seok Oh ³ , and Mun Seok Jeong ^{1,2} ¹ Department of Semiconductor Engineering, Hanyang University, ² Department of Physics, Hanyang University, ³ Department of Physics, Soongsil University
FP-146 ZONE 1 (4층, 로비)	Ultra-Flexible Memristive Reservoir Array for On-Skin Computing Hyojin Shin ¹ , Haein Cho ² , and Gunuk Wang ^{1,3} ¹ KU-KIST Graduate School of Converging Science & Technology, Korea University, ² Department of Electrical and Computer Engineering, University of Virginia, ³ Department of Integrative Energy Engineering, Korea University
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FP-148 ZONE 1 (4층, 로비)	Effects of Low-Energy Electron Beams Irradiation on 2D Tellurium FETs Hongmin Park, YoungJun Song, Hyeon Jung Park, Seungho Bang, and Mun Seok Jeong Department of Physics, Hanyang University
FP-149 ZONE 1 (4층, 로비)	Doping-Induced Metallization in MoS₂ via Fermi Level Tuning Dohyeong Kim ¹ , Seungho Bang ¹ , Wooyoung Kang ¹ , Chan Kwon ¹ , Jaekak Yoo ¹ , Hyeongyeong Bae ¹ , Seungyoon Han ¹ , Youngjoo Yu ¹ , Jihong Kim ¹ , Hayoung Ko ² , Kikang Kim ² , and Mun Seok Jeong ¹ ¹ Department of Physics, Hanyang University, ² Department of Energy Science, Sungkyunkwan University

FP-150 ZONE 1 (4층, 로비)	Laser-Cut Vertical Organic Electrochemical Transistor Array for High-Density Neuromorphic Plasticity Inho Lee ² , Jonghyun Won ² , Wonseok Shin ¹ , Namhee Kim ¹ , and SungjunPark ^{1,2} ¹ Department of Electrical and Computer Engineering, Ajou University, ² Department of Intelligence Semiconductor Engineering, Ajou University
FP-151 ZONE 1 (4층, 로비)	Understanding the Electrical Characteristic Changes in ReS₂ FETs under Drain Bias Stress Beom Jun Kim, Hyeong Jin Choi, Chan Sol Mun, and Gyu-Tae Kim Korea University
FP-152 ZONE 1 (4층, 로비)	1T Ternary Content-Addressable Memory Using Charge-Trapping WSe₂ Reconfigurable Field-Effect Transistors Eunyeong Yang ¹ , Changwook Lee ¹ , Young-Eun Choi ² , Jiwon Ma ¹ , Junwon Kim ^{1,3} , Kyung Rok Kim ² , and Jiwon Chang ^{1,3} ¹ Department of Materials Science Engineering and System Semiconductor Engineering, Yonsei University, ² Department of Electrical Engineering, UNIST, ³ BK21 Graduate Program in Intelligent Semiconductor Technology
FP-153 ZONE 1 (4층, 로비)	Tunable Graphene Quantum Dot-Graphene Oxide Films for Scalable Optoelectronic and Display Applications Kang Hyeon Ji, Yun Ho Lee, and Byoung Don Kong Department of Electrical Engineering, POSTECH
FP-154 ZONE 1 (4층, 로비)	Ultralow-Voltage and Reliable Switching in Hybrid aBN-hBN Memristors Haejoon Ha ¹ , Yuhyun Shin ² , Jaesik Eom ³ , and Juwon Lee ¹ ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Chemistry, Sungkyunkwan University, ³ Department of Physics, Sungkyunkwan University
FP-155 ZONE 1 (4층, 로비)	Electrical Characterization of 2H-WS₂ with Selective Passivation-Layer-Induced Mobility Improvement Lae Hyeong Jeong, Inkyu Yoon, Yoonhye Jang, Donghyun Kim, Sang Hyeok Kim, So Mi Lee, Seung Gyu Lee, and Jae Woo Lee Department of Electronics & Information Engineering, Korea University
FP-156 ZONE 1 (4층, 로비)	Boosted Thermoelectric Power Factor of MoS₂ through Selective Surface Charge-Transfer Doping Sooyeon Moon ^{1,3} , Hyejin Jang ¹ , Kyungjune Cho ^{2,3} , and Seungjun Chung ⁴ ¹ Department of Material Science and Engineering, Seoul National University, ² Electronic and Hybrid Materials Research Center, KIST, ³ Convergence Research Center for Solutions to Electromagnetic Interference in Future-mobility, KIST, ⁴ School of Electrical and Engineering, Korea University
FP-157 ZONE 1 (4층, 로비)	Light-Mediated Photomultiplication via Cascade Energy Transfer in Organic Photodiode Jae-Hyun Kim ¹ , Jaebin Jeong ¹ , and Sungjun Park ^{1,2} ¹ Department of Intelligence Semiconductor Engineering, Ajou University, ² Department of Electrical and Computer Engineering, Ajou University
FP-158 ZONE 1 (4층, 로비)	Ambient Aging-Induced Electrical Modulation in p-Type AsP Devices via Conduction Path Reconfiguration Chan Sol Mun ¹ , Hyeran Cho ¹ , Doyoon Kim ¹ , Eui-Hyoun Ryu ² , and Gyu-Tae Kim ¹ ¹ School of Electrical Engineering, Korea University, ² Center for Quantum Technology, KIST
FP-159 ZONE 1 (4층, 로비)	Transfer-Free Fabrication of 2D Semimetal Electrodes for van der Waals FET Arrays Jaehong Im, Jihoon Yang, and Soon-Yong Kwon Department of Materials Science and Engineering and Graduate School of Semiconductor Materials and Devices Engineering, UNIST
FP-160 ZONE 1 (4층, 로비)	Ferroelectric Semiconductor Field-Effect Transistors (FeS-FETs) based on Indium Selenide Grown by Hypotaxy Jiwon Lee, Donghoon Moon, Jinwoo Kim, and Gwan-Hyoung Lee Department of Materials Science & Engineering, Seoul National University



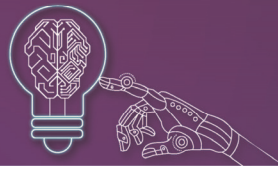
FP-161 ZONE 1 (4층, 로비)	Phase-Controlled vdW Epitaxy of Tin Sulfides Jaehyeok Lee, Jinwoo Kim, and Gwan-Hyeong Lee Department of Materials Science and Engineering, Seoul National University
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FP-164 ZONE 1 (4층, 로비)	Projected Terahertz Performance of a Graphene-Emitter Vacuum-Channel Transistor via Transconductance and Scaling Analysis Chae Ho Won, Jun Yeong Choe, Yeong Jin Ahn, Cheul Hyeon Yoon, and Byoung Don Kong Graduate School of Semiconductor Technology, POSTECH
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FP-166 ZONE 1 (4층, 로비)	Assessment of Charge-Trap Sensitivity and Stability in MoTe₂ and WS₂ FETs Gi Dan Shim ¹ , Ji Won Heo ¹ , Jaewook Yoo ³ , Hagyoul Bae ³ , and Tae Wan Kim ^{1,2,4} ¹ Department of Intelligent Semiconductor Engineering, University of Seoul, ² School of Advanced Fusion Studies, University of Seoul, ³ Department of Electronic Engineering, Jeonbuk National University, ⁴ 2D Epi inc.
FP-167 ZONE 1 (4층, 로비)	Ternary Inverter Operation for Multivalued Logic in WSe₂ Multilayers Hyejin Kim, Seunghyun Woo, Danbi Park, and Min-Kyu Joo Department of Applied Physics, Sookmyung Women's University
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FP-171 ZONE 1 (4층, 로비)	A Photoelectron Spectroscopy Study on the GDW-Modified PEDOT:PSS and Energy Level Alignment at the Interface with ZnPc Woojin Shin ^{1,2} , Seungsun Choi ^{1,2} , Jooyeon Moon ^{2,3} , and Hyunbok Lee ^{1,2,3} ¹ Department of Semiconductor Physics, Kangwon National University, ² Institute of Forest Science, Kangwon National University, ³ Interdisciplinary Program in Earth Environmental System, Kangwon National University
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FP-173 ZONE 1 (4층, 로비)	Electrical Characteristics of 2D Channel Based Impact Ionization FET Donggi Lee ¹ , Hanggyo Jung ¹ , and Jongwook Jeon ² ¹ Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ² Department of Electrical and Computer Engineering, Sungkyunkwan University
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FP-175 ZONE 1 (4층, 로비)	Tunable Charge-Trapping Conjugated Polymer Synaptic Transistors for Neuro-Inspired Artificial Intelligence Systems Jae Eun Kim, Bum Hwan Kim, Hyung Soo Ahn, Won Bae Cho and Young Tea Chun Division of Electronics and Electrical Information Engineering, Korea Maritime & Ocean University
FP-176 ZONE 1 (4층, 로비)	Screen-Printed Ag₂Te-P3HT Composite for Low-Cost, Eye-Safe SWIR Image Sensors Jinbeom Kwon ¹ , Yuntae Ha ² , Uksu Han ² , and Donggeon Jung ² ¹ Department of Semiconductor Engineering, Kyungwoon University, ² Advanced Mobility System Group, KITECH
FP-177 ZONE 1 (4층, 로비)	Analysis of the Electrical and Physical Characteristics of HfO₂ for ReRAM Switching Film Seonggil Ham, Ingon Bae, Sunwoo Lee, Tae Woo Kim, In Hee Chang, Seung Hoon Lee, Yong Han Lee, Hyeontae Lee, Yong Keon Choi, and Sang Gi Lee Development 2 Team, DB HiTek
FP-178 ZONE 1 (4층, 로비)	Tailored Thermoelectric Performance of Carbon Nanotubes Films via Selective Surface Charge Transfer Doping Woojin Kim and Seungjun Chung School of Electrical Engineering, Korea University
FP-179 ZONE 1 (4층, 로비)	CNTFET True Random Number Generator Exploiting Individual CNT Bridges by Inkjet Printing Process Jungha Lee ¹ , Eunui Kim ¹ , Soohyun Park ² , Yoonhee Lee ² , and Hongki Kang ³ ¹ Department of Electrical Engineering and Computer Science, DGIST, ² Division of Biomedical Technology, DGIST, ³ Department of Biomedical Engineering, Seoul National University College of Medicine
FP-180 ZONE 1 (4층, 로비)	Overexposed-PMMA Suspended MoS₂ Transistors for Enhanced On-Current Seunghyun Oh, Hyeonseo Lee, and Junhong Na Department of Semiconductor Physics, Kangwon National University
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FP-183 ZONE 1 (4층, 로비)	Modification of Oxygen Vacancies and Electronic Structure in TiO₂ Nanorods via UV-Ozone Treatment Jooyeon Moon ^{1,2} , Seungsun Choi ^{2,3} , Woojin Shin ^{2,3} , Seongwoo Bae ^{2,4} , Joohyun Lim ^{2,4} , and Hyunbok Lee ^{1,2,3} ¹ Interdisciplinary Program in Earth Environmental System Science & Engineering, Kangwon National University, ² Institute of Forest Science, Kangwon National University, ³ Department of Semiconductor Physics, Kangwon National University, ⁴ Department of Chemistry, Kangwon National University



FP-184 ZONE 1 (4층, 로비)	Achieving Low Contact Resistance of Semimetal Contacts through Alloying-Based Fermi-Level Modulation in WSe₂ Transistors Yeon Ho Kim ¹ , Hyun-Young Choi ² , Dong Beom Seo ¹ , Youngdeuk Youn ² , Gwan-Hyoung Lee ² , and Chul-Ho Lee ¹ ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Department of Materials Science and Engineering, Seoul National University
FP-185 ZONE 1 (4층, 로비)	Electroluminescent Perovskite QDs-Based Neural Networks for Energy-efficient and Accelerate Multi-tasking Learning Young Ran Park ¹ and Gunuk Wang ^{1,2} ¹ KU-KIST Graduate School of Converging Science and Technology, Korea University, ² Department of Integrative Energy Engineering, Korea University
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FP-192 ZONE 1 (4층, 로비)	Multi-Value Synaptic Plasticity in Sliding Ferroelectric γ-InSe Chan Kwon, Seungho Bang, and Mun Seok Jeong Department of Physics, Hanyang University
FP-193 ZONE 1 (4층, 로비)	Functionalized Ti₃C₂T_x-Gated 2D MeSFETs for Nanoscale Electronics Bumsoo Kim ^{1,2} , Hyeonseo Hong ^{1,2} , Keehoon Kang ² , and Kyungjune Cho ¹ ¹ Convergence Research Center for Solutions to Electromagnetic Interference in Future-mobility (SEIF), KIST, ² Department of Materials Science and Engineering, Seoul National University

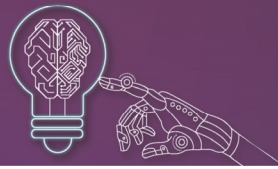
FP-194 ZONE 1 (4층, 로비)	Resistive Switching and Synaptic Behavior via Cu Electrode Induced Cu⁺ Ion Migration in CuInP₂S₆ RRAM Wooyoung Kang ¹ , Seungho Bang ¹ , Dong-joon Yi ² , Yeongseo Kim ³ , and Mun Seok Jeong ^{1,3} ¹ Department of Physics, Hanyang University, ² Department of Electronic Engineering, Hanyang University, ³ Department of Semiconductor Engineering, Hanyang University
FP-195 ZONE 1 (4층, 로비)	High-Mobility p-Type WSe₂ Transistors Enabled by Self-Aligned Remote Modulation Doping Sungmin Park, Yeon Ho Kim, Jaeho Lee, Donghyun Lee, Tae Hyun Yoon, and Chul-Ho Lee Department of Electrical and Computer Engineering, Seoul National University
FP-196 ZONE 1 (4층, 로비)	Vertically Stacked WSe₂ Complementary Field-Effect Transistors Enabled by Al₂O₃ Charge-Transfer Doping and Thickness Optimization through Multiple Wet-Transfer of Monolayer WSe₂ Eunyeong Yang ¹ , Junwon Kim ^{2,3} , Jiwon Ma ¹ , Changwook Lee ¹ , and Jiwon Chang ^{2,3} ¹ Department of Materials Science and Engineering, Yonsei University, ² Department of System Semiconductor Engineering, Yonsei University, ³ BK21 Graduate Program in Intelligent Semiconductor Technology
FP-197 ZONE 1 (4층, 로비)	Conduction Type Conversion and Threshold Voltage Modulation in Carbon Nanotube Transistors with Atomic Layer Deposited Dielectrics Jiyoung Baek ¹ , Haerim Kim ¹ , Yujeong Hwang ² , Min-Hoi Kim ² , and Bongjun Kim ¹ ¹ Department of Electrical Engineering, Sookmyung Women's University, ² Department of Creative Convergence Engineering, Hanbat National University
FP-198 ZONE 1 (4층, 로비)	Fabrication and Photoresponse of Heterojunction Schottky Diode based on SnSe₂/ReSe₂ Jiwon Ha, Seok Hyun Byun, Byeonghoon Kim, Mingi Choi, and Jongtae Ahn Department of Physics, Changwon National University
FP-199 ZONE 1 (4층, 로비)	Understanding the Switching Mechanism of Organic-Inorganic Halide Perovskite Memristors for Enhanced Stability and Uniformity SuA Han ¹ , Ik Jae Park ² , and Hae Jin Kim ¹ ¹ Department of Materials Science and Engineering, Myongji University, ² Department of Materials Physics, Sookmyung Women's University



K. Memory (Design & Process Technology) 분과

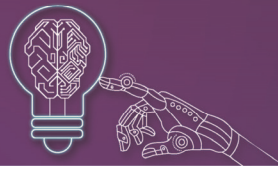
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FP-206 ZONE 2-1 (5층, 로비)	Time-Varying Reservoir Computing with Monolithic 1T1R Array for Complex Temporal Data Analysis Sung Keun Shim ^{1,2} , Wonho Choi ^{1,2} , and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
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FP-208 ZONE 2-1 (5층, 로비)	In-memory Bayesian Machine Using Vertical Cu_{0.3}Te_{0.7}/HfO₂/TiN Memristive Crossbar Array In Kyung Baek ^{1,2} , Sunwoo Cheong ^{1,2} , Hyun Wook Kim ^{1,2} , and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University

FP-209 ZONE 2-1 (5층, 로비)	Influence of mg Content and Bottom Electrode on the Electrical Properties of ZnMgO-Based Resistive Switching Devices Hyunju Kim ^{1,2,3} , Yeunwoo Kwon ^{1,2,3} , Yeon Jun Kim ^{1,2,3} , and Jeounghun Kwak ^{1,2,3} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University, ³ SOFT Foundry Institute, Seoul National University
FP-210 ZONE 2-1 (5층, 로비)	Improvement of Variation and Switching Characteristics of Al₂O₃-Based RRAM Using Wake-up Effect Byeongchan Oh and Tae-Hyeon Kim Seoul National University of Science & Technology
FP-211 ZONE 2-1 (5층, 로비)	차세대 신경모방 하드웨어를 위한 iCVD 기반 밴드엔지니어링 전하 포획형 유·무기 하이브리드 시냅스 메모리 배열 김태훈 ¹ , 김형준 ² , 한상혁 ² , 박태원 ² , 김명진 ² , 신민규 ² , 최은준 ³ , 김민주 ^{1,2,3} ¹ 단국대학교 공과대학 파운드리공학부, ² 단국대학교 공과대학 전자전기공학부, ³ 단국대학교 공과대학 융합반도체공학과
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FP-213 ZONE 2-1 (5층, 로비)	Cyclic Plasma Treatment for Enhancing Performance of Bulk-Conductive Resistive Switching Memory Hyungjun Park ^{1,2} , Janguk Han ^{1,2} , Kyung Seok Woo ³ , and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University, ³ Graduate School of Semiconductor Materials and Devices Engineering, UNIST
FP-214 ZONE 2-1 (5층, 로비)	Reconfigurable Logic Gates Using a Single Ferroelectric-Tunnel-FET Jaemin Yeom ^{1,2} , Minjeong Ryu ^{1,2} , Jae Seung Woo ^{1,2} , Jinwook Lee ^{1,2} , and Woo Young Choi ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
FP-216 ZONE 2-1 (5층, 로비)	Enhancing Memory Window of Hf_{0.5}Zr_{0.5}O₂-Based Ferroelectric Field-Effect Transistors by Controlling Ozone Dose Time in Atomic Layer Deposition Hyun Min Kim ^{1,2} , Kilhwa Pi ^{1,2} , and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
FP-217 ZONE 2-1 (5층, 로비)	Optimization of Programming Pulse Duration for Energy-Efficient Program Operation in Vertical NAND Flash Memory Dongbeen Shin ^{1,2} , Sung-Ho Park ^{1,2} , Mingyun Oh ^{1,2} , Jong-Ho Lee ^{1,2} , and Gyuweon Jung ^{1,2,3} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University, ³ School of Transdisciplinary Innovations, Seoul National University
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FP-219 ZONE 2-1 (5층, 로비)	Analog Memristive System for Pulse Width Modulation Circuit Byeong Su Kim ^{1,2} and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University



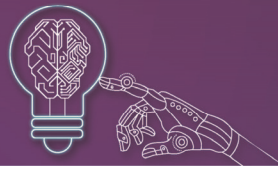
FP-220 ZONE 2-1 (5층, 로비)	Effect of Hf:Zr Ratio on Phase Transition and Cycling Behavior in $\text{Hf}_{1-x}\text{Zr}_x\text{O}_2$ Thin Films Jongho Ji ^{1,2} , Ryun-Han Koo ^{1,2} , Seungwhan Kim ^{1,2} , Sangwoo Ryu ^{1,2} , and Jong-Ho Lee ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
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FP-224 ZONE 2-1 (5층, 로비)	DRAM Cell Transistor with Wrap-Around Word-Line (WAW) Structure for Enhancing Read/Write Performance Dol Sohn, Sang-Min Kang, Da-Eun Bang, Eui-Cheol Yun, Moon-Kwon Lee, Hyo-Jun Park, Min-Woo Kim, and Jun-Young Park School of Semiconductor Engineering, Chungbuk National University
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FP-228 ZONE 2-1 (5층, 로비)	High-Performance Pt/HZO/TiN Ferroelectric Tunnel Junctions through Sequential Top-Electrode Engineering Kwang Min Jeong ¹ and You Seung Rim ^{1,2} ¹ Department of Semiconductor Systems Engineering and Convergence Engineering for Intelligent Drone, Sejong University, ² Institute of Semiconductor and System IC, Sejong University
FP-229 ZONE 2-1 (5층, 로비)	Self-Rectifying Ferroelectric Diode-Based Crossbar Array to Map Non-Euclidean Graph Data Jae-Hyun Lee, Jun-Young Park, and Joon-Kyu Han Department of Material Science and Engineering, Seoul National University
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FP-237 ZONE 2-1 (5층, 로비)	A Configurable Bit-Width DCIM Macro Supporting Flexible Weight and Input Precision Junhyeok Lee ^{1,2} , Munhyeon Kim ³ , and Jae-Joon Kim ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University, ³ Department of Electrical and Information Engineering, Seoul National University of Science & Technology
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FP-242 ZONE 2-1 (5층, 로비)	Electrical Characteristics of Size-Specific Mesh-Type Floating Gate Based Flash Transistors Manufactured by Electron Beam Lithography Hyeonseok Jeong ¹ , Hyeongjin Chae ¹ , Taehwan Koo ¹ , Juyeong Chae ¹ , Kangmin Yoo ¹ , Dongyeop Kim ² , Munseok Jung ² , Joonhyuk kim ² , and Moongyu Jang ^{1,2} ¹ School of Nano Convergence Technology, Hallym University, ² School of Semiconductor Display Technology, Hallym University
FP-243 ZONE 2-1 (5층, 로비)	3D Vertical PCM-Based Analog In-Memory Computing and Mixer Co-Design for Scalable AI Acceleration Sejeung Choi ¹ , Jinha Choi ² , and Sangbum Kim ^{1,2,3} ¹ Inter-university Semiconductor Research Center, Seoul National University, ² Department of Materials Science and Engineering, College of Engineering, Seoul National University, ³ Research Institute of Advanced Materials, Seoul National University
FP-244 ZONE 2-1 (5층, 로비)	Experimental Demonstration of Enhanced Memory Window in Interface Layer-Free HZO FeFETs via Low Work-Function Gate Youngkyoung Kim ¹ , Simin Chen ¹ , Hyewon Cho ² , Jaekyun Kim ¹ , Jihoon Ahn ² , and Younghyun Kim ¹ ¹ Department of Photonics and Nanoelectronics, BK21 FOUR ERICA-ACE Center, Hanyang University, ² Department of Materials Science and Chemical Engineering, Hanyang University



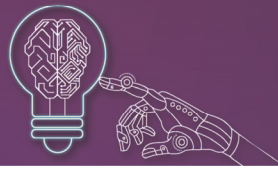
FP-245 ZONE 2-1 (5층, 로비)	Minimizing Device-to-Device Variation in IGZO Floating-Gate Synaptic Transistors via Source/Drain Contact Structure Modification Sunyeol Bae ^{1,2} , Junhyeong Park ^{1,2} , Yumin Yun ^{1,2} , Donghyeon Lee ^{1,2} , Ji-Ho Lee ^{1,2} , and Soo-Yeon Lee ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
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FP-250 ZONE 2-1 (5층, 로비)	RWL 전압 부스트에 따른 3T Gain Cell eDRAM의 성능 분석 임채혁 ¹ , 김건 ¹ , 김동영 ¹ , 김수연 ¹ , 박제원 ¹ , 김신욱 ¹ , 강륜 ¹ , 김소원 ¹ , 이주원 ¹ , 서현아 ¹ , 윤정현 ¹ , 이혜린 ¹ , 최우진 ² , 김어진 ² , 정민우 ² , 이명진 ² ¹ 전남대학교 지능전자컴퓨터공학과, ² 전남대학교 전자컴퓨터공학부
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FP-252 ZONE 2-1 (5층, 로비)	Electrochemical Random Access Memory with InGaZnO Thin Films: Structural Design and Synaptic Behavior Jung Kyu Lee ^{1,2} and Cheol Seong Hwang ^{1,2} ¹ Department of Materials Science and Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University
FP-253 ZONE 2-1 (5층, 로비)	High Capacitance and Low Leakage Current Hf_xZr_{1-x}O₂ Capacitors Using Morphotropic Phase Boundary Formation with a TiO₂:HfO₂ Composite Seed Layer for DRAM Applications Sungju Jung ^{1,2} and Rino Choi ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-254 ZONE 2-1 (5층, 로비)	Highly Uniform and Low-Power Chiral Perovskite Resistive Random-Access Memory In-Kook Hwang, Si-Yeol Lee, and Seung-heon Chris Baek Center for Semiconductor Technology, KIST
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FP-256 ZONE 2-1 (5층, 로비)	후면 강유전체 기반 이중 게이트 소자의 다결정 실리콘/강유전체 계면 La₂O₃ 삽입을 통한 메모리 특성 향상 정원목 ¹ , 정재중 ¹ , 신건학 ² , 유찬미 ² , 김승훈 ¹ , 이동규 ² , 박영근 ¹ , 김민승 ¹ , 강대현 ¹ , 박동욱 ¹ , 조병진 ^{1,2} ¹ 한국과학기술원 전기 및 전자공학부, ² 한국과학기술원 반도체공학대학원

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FP-259 ZONE 2-1 (5층, 로비)	Monolithic TiO₂ Synaptic Phototransistors with Tri-Dendritic Input Integration and Cross-Modal STDP Youngbin Yoon ¹ , Jaehee Lee ^{1,2} , Jeongwook Kwon ³ , Youngki Kim ³ , Myunghun Shin ³ , and Jung Wook Lim ^{1,2} ¹ ETRI, ² University of Science and Technology, ³ Korea Aerospace University
FP-260 ZONE 2-1 (5층, 로비)	Asymmetric Oxynitride Bilayer with Artificially Engineered Conductive Path for Unsupervised Learning Seokgi Kim and Sungkyu Kim Department of Nanotechnology and Advanced Materials Engineering, Sejong University
FP-261 ZONE 2-1 (5층, 로비)	Microwave-Treated Transparent Short-Term Memory Device for Brain-Inspired Reservoir Computing Jinyeol Lee and Hee Dong Kim Department of Semiconductor Systems Engineering, Sejong University
FP-262 ZONE 2-1 (5층, 로비)	Program Gate Free Reconfigurable FET : Drain-Polarity-Controlled Ambipolar Transistor for CMOS-Compatible Adaptive Logic Application Seung-Hyun Lee, Seung-Hwa Choi, and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University
FP-263 ZONE 2-1 (5층, 로비)	Dynamic Gate-Switchable Dual-Gate Reconfigurable Field-Effect Transistor for CMOS-Compatible Logic Applications Seung-hwa Choi, Seung-hyun Lee, and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University
FP-264 ZONE 2-1 (5층, 로비)	Sub-Femtojoule 3D Ferroelectric Memory Enabled by Graphene Electrodes Shubham V. Patil ^{1,2} , Minwoo Lee ^{1,2} , Batyrbek Alimkhanuly ^{1,2} , Seungwoo Moon ^{1,2} , Junseong Bae ^{1,2} , Jinsu Choi ^{1,2} , Gisung Lee ^{1,2} , Yongjun Kim ^{1,2} , and Seunghyun Lee ^{1,2} ¹ Department of Electronic Engineering, Kyung Hee University, ² Department of Semiconductor Engineering, Kyung Hee University
FP-265 ZONE 2-1 (5층, 로비)	DRAM Passing Word Line Row Hammering 취약점과 이를 개선한 BCAT 구조 제안 김수연 ¹ , 김동영 ¹ , 김신욱 ¹ , 박제원 ¹ , 강륜 ¹ , 김소원 ¹ , 임채혁 ¹ , 윤정현 ¹ , 서현아 ¹ , 이혜린 ¹ , 이주원 ¹ , 최우진 ² , 김어진 ² , 정민우 ² , 김태희 ² , 이명진 ¹ ¹ 전남대학교 지능전자컴퓨터공학과, ² 전남대학교 전자컴퓨터공학부
FP-266 ZONE 2-1 (5층, 로비)	도핑 농도에 따른 BCAT 누설 전류 예측 이주원 ¹ , 김동영 ² , 김수연 ² , 김신욱 ² , 박제원 ² , 강륜 ² , 김소원 ² , 임채혁 ² , 윤정현 ² , 서현아 ² , 이혜린 ² , 최우진 ² , 김어진 ² , 정민우 ² , 김태희 ² , 이명진 ² ¹ 전남대학교 지능전자컴퓨터공학과, ² 전남대학교 전자컴퓨터공학부



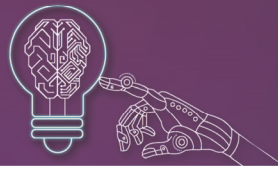
FP-267 ZONE 2-1 (5층, 로비)	Enhancing the Reliability of HfO₂-Based CBRAM Using HfO₂/MgO Bilayer Structures Si-yeol Lee ^{1,2} , Seung-Hwan Kim ³ , Kwangkyo Jung ⁴ , Hamin Park ⁴ , Hyun-yong Yu ² , and Seung-heon Chris Baek ¹ ¹ Center for Semiconductor Technology, KIST, ² Department of Electrical Engineering, Korea University, ³ Department of Electronics and Computer Engineering, Chonnam University, ⁴ Department of Electronic Engineering, Kwangwoon University
FP-268 ZONE 2-1 (5층, 로비)	Investigation of High-Performance Synaptic Devices based on Zr-Doped ZnO Dong-Min Kim ¹ , Yu-Bin Kim ¹ , Sung-Ho Kim ¹ , Shivam Kumar Gautam ¹ , Hyuk-Min Kwon ² , Yong-Goo Kim ³ , and Hi-Deok Lee ¹ ¹ Department of Electronics Engineering, Chungnam National University, ² School of Electronic & Electrical Engineering, Hankyong National University, ³ Department of Green Semiconductor System, Korea Polytechnic
FP-269 ZONE 2-1 (5층, 로비)	Artificially Engineered Oxygen-Gradient in a-IGZO Memristors for Reliable Analog Switching and Self-Rectifying Neuromorphic Computing Sunwoo Nam, Seokgi Kim, Bongsu Han, Seeun Park, Minju Cho, and Sungkyu Kim Department of Nanotechnology and Advanced Materials Engineering, Sejong University
FP-270 ZONE 2-1 (5층, 로비)	The Effect of Inserting an Oxygen-Vacancy-Rich Interlayer Between Switching Layers on Resistive Switching Sung-Ho Kim ¹ , Yu-Bin Kim ¹ , Dong-Min Kim ¹ , Shivam Kumar Gautam ¹ , Sun-Young Kwak ¹ , Hyuk-Min Kwon ² , Yong-Goo Kim ³ , and Hi-Deok Lee ¹ ¹ Department of Electronics Engineering, Chungnam National University, ² School of Electronic & Electrical Engineering, Hankyong National University, ³ Department of Green Semiconductor System, Korea Polytechnics
FP-271 ZONE 2-1 (5층, 로비)	Development of a Gate Injection-Based Field-Effect Transistor with Highly Reliable and Linear Conductance Modulation Seokho Seo ¹ , Yujin Kim ¹ , Dae-won Kim ² , Hakcheon Jeong ¹ , See-On Park ¹ , Yoonho Cho ¹ , and Shinhyun Choi ^{1,2} ¹ School of Electrical Engineering, KAIST, ² Graduate School of Semiconductor Technology, KAIST
FP-272 ZONE 2-1 (5층, 로비)	Wafer-Scale Aligned SWCNT-Based 3D Vertical Ferroelectric Diode Jinsu Choi ^{1,2} , Batyrbek Alimkhanuly ^{1,2} , Shubham V. Patil ^{1,2} , Junseong Bae ^{1,2} , Minwoo Lee ^{1,2} , Gising Lee ^{1,2} , Hanjoo Ji ^{1,2} , Hyunwoo Sohn ^{1,2} , Yongjun Kim ^{1,2} , and Seunghyun Lee ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Kyung Hee University
FP-273 ZONE 2-1 (5층, 로비)	Dual-Mode Charge Trap Flash Memory Using Nickel-Silicide Schottky-Junction Reconfigurable FETs via Channel Modulation Engineering Ki-Ju Park, Seong-Hwan Lim, and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University
FP-274 ZONE 2-1 (5층, 로비)	Multifunctional Monolithic 3D Integrated Sensor-Memory Hardware for Homomorphic Associative Computing Batyrbek Alimkhanuly ^{1,2} , Minwoo Lee ^{1,2} , Seunghyun Lee ^{1,2} , Junseong Bae ^{1,2} , Jinsu Choi ^{1,2} , Yongjun Kim ^{1,2} , and Dr. Seunghyun Lee ^{1,2} ¹ Department of Electronic Engineering, Kyung Hee University, ² Department of Semiconductor Engineering, Kyung Hee University
FP-275 ZONE 2-1 (5층, 로비)	Ion Energy Barrier Modulation and Enhanced Reliability Effects via Fluorine Doping for Neuromorphic Systems Jongmin Bae ¹ , Choah Kwon ² , See-On Park ¹ , Hakcheon Jeong ¹ , Taehoon Park ¹ , Taehwan Jang ¹ , Yoonho Cho ¹ , Sangtae Kim ^{2,3} , and Shinhyun Choi ¹ ¹ School of Electrical Engineering, KAIST, ² Department of Nuclear Engineering, Hanyang University, ³ Department of Material Science and Engineering, Hanyang University

FP-277 ZONE 2-1 (5층, 로비)	Optimized TCAM Read Schemes for Oxygen Barrier Incorporated RRAM Device Properties Hyeokjun Na and Sungjoon Kim Department of AI Semiconductor Engineering, Korea University
FP-278 ZONE 2-1 (5층, 로비)	Thermal Conversion of Sputter-Deposited VN Films into VO₂ for Non-Volatile Memory Applications Jehoon Yeon ¹ , Samyugghanayaki Ramuvel ¹ , and Hyungtak Seo ^{1,2} ¹ Department of Energy Systems Research, Ajou University, ² Department of Materials Science and Engineering, Ajou University
FP-279 ZONE 2-1 (5층, 로비)	Analysis of Bandwidth Characteristics of Sequential Access Using Selector-Only-Memory In seong Choi and Jae-Joon Kim Department of Electrical and Computer Engineering, Seoul National University
FP-280 ZONE 2-1 (5층, 로비)	Effect of a Single-Void in Word-Line Metal on DRAM Cell Characteristics Min Ji Kim ¹ , Ji Young Kim ¹ , Young Woo Son ² , Byung Hyun Lee ² , and Woo Young Choi ¹ ¹ Department of Electrical and Computer Engineering and Inter-university Semiconductor Research Center, Seoul National University, ² DRAM Process Architecture Team, Samsung Electronics Co., Ltd.
FP-281 ZONE 2-1 (5층, 로비)	Microscopic Pulse Probing of Polarization Switching Properties in Ultra-Thin Ferroelectric Films via Conductive AFM Hyunjun Kim ¹ , Wonwoo Kho ² , Hyo-Bae Kim ³ , Gunho Kim ³ , Hyunjoo Hwang ² , Dongjun Min ¹ , Narim Lee ¹ , Ji-Hoon Ahn ³ , and Seung-Eon Ahn ^{1,2} ¹ Department of Nano & Semiconductor Engineering, Tech University of Korea, ² Department of IT • Semiconductor Convergence Engineering, Tech University of Korea, ³ Department of Materials Science and Chemical Engineering, Hanyang University
FP-282 ZONE 2-1 (5층, 로비)	Control of Morphotropic Phase Boundary Characteristics in Hf_{0.66}Zr_{0.33}O₂ Ferroelectric Layer Using an Oxygen-Deficient Seed Layer Hyun Deok Yoo ^{1,2} and Rino Choi ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-283 ZONE 2-1 (5층, 로비)	SWIR-Addressable TiO₂/PbS QD ReRAM for Optoelectronic Synapse and Neuromorphic Computing at 1550 nm Il Young Moon and Yoon Jang Chung Department of Chemical and Biological Engineering, Korea University
FP-284 ZONE 2-1 (5층, 로비)	MoS₂ Field Effect Transistor with HfO₂-Based Memristor Controlled Multi-State Conduction Sangyoon Lee and Juwon Lee Department of Electrical and Computer Engineering, Sungkyunkwan University
FP-285 ZONE 2-1 (5층, 로비)	Enhanced Capacitive Memory Window of MFM Capacitors Using FE/AFE Bilayer for Non-Destructive Read CIM Application Seunghyun Jung and Rino Choi Materials Science and Engineering/Program in Semiconductor Convergence, Inha University
FP-286 ZONE 2-1 (5층, 로비)	Synaptic Behaviors of AuNP-DNA/HfO₂ Based Memristor for Neuromorphic Gas Sensor Chanmin Hwang ¹ , Myoungsu Chae ² , and Hee-dong Kim ¹ ¹ Department of Semiconductor Systems Engineering, and Convergence Engineering for Intelligent Drone, Institute of Semiconductor and System IC, Sejong University, ² Institute of Industrial Science, Department of Electrical Engineering, The University of Tokyo
FP-287 ZONE 2-1 (5층, 로비)	Solution-Processed IGZO 2T0C DRAM for Low-Cost and High-Throughput Manufacturing Hyosang Kim ¹ , Yubin Kim ² , Seong Jun Kang ² , and Hongseok Oh ¹ ¹ Department of Intelligent Semiconductors, Soongsil University, ² Department of Materials Science and Engineering, Kyung Hee University



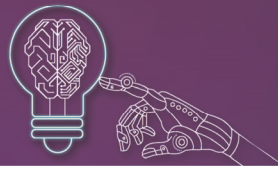
FP-289 ZONE 2-1 (5층, 로비)	Ultra-Low Current Phase-Change Memory via Forming Phase-Changeable Nano-Filament See-On Park ¹ , Su-Jin Sung ² , Dawon Kim ¹ , Seokho Seo ¹ , Hakcheon Jeong ¹ , Jeehwan Kim ^{3,4} , and Shinhyun Choi ^{1,2} ¹ School of Electrical Engineering, KAIST, ² Graduate School of Semiconductor Technology, KAIST, ³ Research Laboratory of Electronics, MIT, ⁴ Department of Mechanical Engineering, MIT
FP-290 ZONE 2-1 (5층, 로비)	A Design Guideline for 3D Cylindrical MFMIS IGZO FeFETs Seongwon Choi ¹ , Byeongjun Bae ¹ , Jung-Kyun Kim ² , Younjin Jang ² , Kyoocho Jung ² , Jee-Eun Yang ² , Na-Rae Han ² , Sangwook Kim ² , and Woo Young Choi ¹ ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Thin Film Technical Unit, Device Research Center, SAIT, Samsung Electronics
FP-291 ZONE 2-1 (5층, 로비)	Reliable Multilevel Memristive Device Utilizing Filament Confinement and Buffer-Layer Design in an Amorphous Matrix See-On Park ¹ , Seokho Seo ¹ , and Shinhyun Choi ^{1,2} ¹ School of Electrical Engineering, KAIST, ² Graduate School of Semiconductor Technology, KAIST
FP-292 ZONE 2-1 (5층, 로비)	Demonstration of a Highly Reliable and Self-Rectifying Dynamic Memristor with Oxygen Concentration-Gradient Layer See-On Park, Hakcheon Jeong, Yoonho Cho, and Shinhyun Choi School of Electrical Engineering, KAIST
FP-293 ZONE 2-1 (5층, 로비)	Reliable Interface-Type TiO_x RRAM Array for Neuromorphic Systems Seonjeong Lee ¹ , Seohyeon Ju ² , Sungjun Kim ² , and Yoon Kim ¹ ¹ School of Electrical and Computer Engineering, University of Seoul, ² Division of Electronics and Electrical Engineering, Dongguk University
FP-294 ZONE 2-1 (5층, 로비)	Self-Supervised Video Processing with Self-Calibration on an Analog Computing Platform based on a Selector-less Memristor Array Hakcheon Jeong ¹ , Seungjae Han ¹ , See-On Park ¹ , Tae Ryong Kim ¹ , Jongmin Bae ¹ , Taehwan Jang ¹ , Yoonho Cho ¹ , Seokho Seo ¹ , Hyun-Jun Jeong ¹ , Seungwoo Park ¹ , Taehoon Park ¹ , Juyoung Oh ² , Jeongwoo Park ³ , Kwangwon Koh ⁴ , Kang-Ho Kim ⁴ , Dongsuk Jeon ^{5, 6} , Inyong Kwon ⁷ , Young-Gyu Yoon ¹ , and Shinhyun Choi ¹ ¹ School of Electrical Engineering, KAIST, ² Graduate School of Convergence Science and Technology, Seoul National University, ³ Department of Electrical and Computer Engineering, Sungkyunkwan University, ⁴ ETRI, ⁵ Department of Intelligence and Information, Seoul National University, ⁶ Interuniversity Semiconductor Research Center, Seoul National University, ⁷ Department of Radiological Science, Yonsei University
FP-295 ZONE 2-1 (5층, 로비)	Optimization of Interlayer Engineering in MFIS-Type Ferroelectric Tunnel Junctions for Enhanced Reliability with Minimal Polarization Degradation Junseo Lee and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology
FP-296 ZONE 2-1 (5층, 로비)	Memtransistor-Based Neural Network for Associative Learning with Memory Interference Circuit Ria Choi ¹ , Yesong Yu ¹ , Heerak Wi ¹ , Chaei Lee ¹ , Hae Lyn Choi ¹ , Eunbee Choi ¹ , Dae Kyu Lee ² , Min Jee Kim ² , and Joon Young Kwak ¹ ¹ Division of Electronic and Semiconductor Engineering, Ewha Womans University, ² Center for Semiconductor Technology, KIST
FP-297 ZONE 2-1 (5층, 로비)	Reliable Resistive Switching and Neuromorphic Applications of SiO₂/BN Heterostructure-Based Memristor Eunbin Jo ¹ , Seeun Kim ¹ , Sunah Park ² , Lee Heejuon ² , and Doowon Lee ¹ ¹ Division of Electrical, Electronic and Control Engineering, Kongju National University, ² Mirtech R&D Corporation

FP-298 ZONE 2-1 (5층, 로비)	Effect of Ion-Migration Induced Barrier Modulation in Self-Rectifying CsCu₂I₃ ReRAM HyeonSeok Park ¹ , UiJin Jung ² , Dohyun Kyun ² , and JinSub Park ¹ ¹ Department of Artificial Semiconductor, Hanyang University, ² Department of Industrial Engineering, Hanyang University
FP-299 ZONE 2-1 (5층, 로비)	Characteristics of High-k Hafnium Oxide Films Deposited by Remote Plasma ALD for 3D NAND Applications Do Han Kim, So Won Kim, and Hee Chul Lee Department of Advanced Materials Engineering, Tech University of Korea
FP-300 ZONE 2-1 (5층, 로비)	Process-Driven Enhancement of Ferroelectric Properties in Superlattice HZO Films via Dual-Plasma ALD Jae Hyuk Choi, So Won Kim, and Hee Chul Lee Department of Advanced Materials Engineering, Tech University of Korea
FP-301 ZONE 2-1 (5층, 로비)	웨이퍼 본딩 3D NAND 구조에서 벌크 이레이즈 동작이 가능한 구조 설계 강지원 ^{1,4} , 박기홍 ^{2,4} , 김윤 ^{1,3,4} ¹ Department of Electrical and Computer Engineering, University of Seoul, ² Department of Intelligent Semiconductor Engineering, University of Seoul, ³ IM Electronics co., ⁴ Center for Semiconductor Research, University of Seoul
FP-302 ZONE 2-1 (5층, 로비)	Reference-Offset Tolerant On-Chip Training Method for Bufferless Analog Processing-in-Memory Gaeun Heo ¹ , Seyoung Kim ^{1,2} ¹ Graduate School of Semiconductor Technology, POSTECH, ² Department of Electrical Engineering, POSTECH
FP-303 ZONE 2-1 (5층, 로비)	Probabilistic Bit Implementation based on Random Switching Resistive RAM Sion Kim ¹ , Minsu Kang ¹ , Yuna Kim ¹ , Eungcheol Kim ² , Sarah Yoon ² , Songye Lim ² , Jin Pyo Kang ³ , Hamin Park ³ , and Daeseok Lee ¹ ¹ Department of Semiconductor Engineering, Kwangwoon University, ² Department of Electronic Materials Engineering, Kwangwoon University, ³ Department of Electronic Engineering, Kwangwoon University
FP-304 ZONE 2-1 (5층, 로비)	Optimizing Reset Pulse Width for Gradual Conductance Programming in Al₂O₃/TiO_x-Based RRAM Hyeonseong Lim and Tae-Hyeon Kim Department of Semiconductor Engineering, Seoul National University of Science & Technology
FP-305 ZONE 2-1 (5층, 로비)	EcFET: IGZO-Based Electrochemical Field Effect Transistor for Physical Reservoir Computing Hyejin Kim ¹ , Jinho Byun ¹ , Junghoon Park ³ , Kiheun Lee ³ , Daewon Ha ³ , and Seyoung Kim ^{1,2} ¹ Department of Electrical Engineering, POSTECH, ² Graduate School of Semiconductor Technology, POSTECH, ³ Semiconductor Research and Development Center
FP-306 ZONE 2-1 (5층, 로비)	Unveiling Polarity-Induced Threshold Voltage Shift Phenomenon in Te-Based Ovonic Threshold Switching Materials Siwon Park ^{1,2} , Young-Min Kim ^{1,2} , Sangyeop Kim ^{1,2} , and Jong-Souk Yeo ^{1,2} ¹ School of Integrated Technology, College of Computing, Yonsei University, ² BK21 Graduate Program in Intelligent Semiconductor Technology, Yonsei University
FP-307 ZONE 2-1 (5층, 로비)	Characterization of Nonlinear Current-Voltage Characteristic in RRAM for Reliable Hardware Neural Network Implementation Dohyeong Kim, Sungjoon Kim, and Sungmin Hwang Department of AI Semiconductor Engineering, Korea University



FP-308 ZONE 2-1 (5층, 로비)	Photoresistive MoS₂-Based Artificial Synaptic Transistor for Wavelength-Dependent Physical Reservoir Computing Won Suk Oh ¹ , Geonho Lee ² , Mingeun Park ³ , Seongwon Gim ² , Hyunjun Baek ² , and Hongseok Oh ^{1,4} ¹ Department of Intelligent Semiconductors, Soongsil University, ² Department of Physics, Sogang University, ³ School of Electrical and Electronics Engineering, Chung-Ang University, ⁴ Department of Physics, Soongsil University
FP-309 ZONE 2-1 (5층, 로비)	Resolving the Diffusivity-Dependent Switching Dynamics of ECRAM for Analog In-Memory Computing Woochan Song, Seungmin Han, and Seyoung Kim Department of Electrical Engineering, POSTECH
FP-310 ZONE 2-1 (5층, 로비)	SHAP 기반 DRAM 오프셋 보정 감지 증폭기 분석 이혜린 ¹ , 김건 ¹ , 김동영 ¹ , 김수연 ¹ , 박제원 ¹ , 김신욱 ¹ , 강륜 ¹ , 임채혁 ¹ , 이주원 ¹ , 서현아 ¹ , 윤정현 ¹ , 김소원 ¹ , 최우진 ² , 김어진 ² , 정민우 ² , 이명진 ² ¹ 전남대학교 지능전자컴퓨터공학과, ² 전남대학교 전자컴퓨터공학부
FP-311 ZONE 2-1 (5층, 로비)	Ferroelectric Feedback Field-Effect Transistors Architecture for Low-Voltage Programming Operation Hongjune Park and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology
FP-312 ZONE 2-1 (5층, 로비)	Modeling and Optimization of Diode-Connected DRAM Sense Amplifier for Enhanced Statistical Margin Dongyeong Kim ¹ , Geon Kim ¹ , Suyeon Kim ¹ , Jewon Park ¹ , Shinwook Kim ¹ , Ryun Kang ¹ , Sowon Kim ¹ , Chaehyuk Lim ¹ , Hyeona Seo ¹ , Jeonghyeon Yun ¹ , Juwon Lee ¹ , Hyerin Lee ¹ , Ujin Choi ² , Eojin Kim ² , Minwoo Jeong ² , and Myoungjin Lee ² ¹ Department of Intelligent Electronics and Computer Engineering, Chonnam National University, ² Department of Electronics and Computer Engineering, Chonnam National University
FP-313 ZONE 2-1 (5층, 로비)	Forming-free TiO_x/Al₂O₃ RRAM With Thermally Induced Filaments for Reliable In-Memory Computing Applications Seongbin Kwon, Sungmin Hwang, and Sungjoon Kim Department of AI Semiconductor Engineering, Korea University
FP-314 ZONE 2-1 (5층, 로비)	Study on Proton Radiation Effects of Metal Ion Injected ZnO RRAM for Next Generation Space Memory Applications Yu-Mi Kim ¹ , Young-Seok Hwang ¹ , Eun-Joo Oh ¹ , So-Yeon Kwon ² , Hye-Ri Hong ² , Woon-San Ko ² , and Ga-Won Lee ² ¹ Korea Atomic Energy Research Institute, ² Chungnam National University
FP-315 ZONE 2-1 (5층, 로비)	Sensing Margin Improvement and Disturbance Mitigation in 2T0C DRAM Using a GAA IGZO FET and a GAA 2D FET Geon Kim ¹ , Minseop Kim ² , and Jongwook Jeon ³ ¹ Department of Display Engineering, Sungkyunkwan University, ² Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³ Department of Electrical and Computer Engineering, Sungkyunkwan University
FP-316 ZONE 2-1 (5층, 로비)	Selective Erase Operation Utilizing Hot Hole Injection for 3D NAND Flash Memory Byeongjun Joo ¹ , Moonchurl Kim ² , and Jongwook Jeon ¹ ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Display Engineering, Sungkyunkwan University
FP-317 ZONE 2-1 (5층, 로비)	Remote- Plasma ALD of High- k Charge Trap Memory: τ- Resolved Mapping of Interface and Border Traps in Depth and Energy Analysis Chan Hee Lee, So Won Kim, and Hee Chul Lee Department of Advanced Materials Engineering, Tech University of Korea

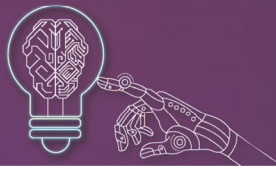
FP-318 ZONE 2-1 (5층, 로비)	Ferroelectric Capacitor Compact Model Development for Processing-in-Memory Junseo Park ¹ , Juhwan Park ² , Joonhyeok Lee ² , and Jongwook Jeon ² ¹ Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ² Department of Electrical and Computer Engineering, Sungkyunkwan University
FP-319 ZONE 2-1 (5층, 로비)	A 65nm 4KB Approximate 3T1C e-DRAM Macro Delivering 630us Refresh Period at 120°C for CNN Ik-Joon Chang School of Electronics Engineering, Kyung Hee University
FP-320 ZONE 2-1 (5층, 로비)	Organic-Inorganic Hybrid Layers for Stable Ferroelectric Memory Hyowon Jang ¹ , Yongju Lee ^{1,2} , Swarup Biswas ¹ , and Hyeok Kim ¹ ¹ University of Seoul, ² University of Paris



K. Memory (Design & Process Technology) 분과

FP-321 ZONE 2-2 (5층, 로비)	Low-Power and Highly Reliable Resistive Switching Characteristics of $\text{HfO}_2/\text{ZnO}/\text{Al}_2\text{O}_3$ Tri-Layer Devices Yu-Bin Kim ¹ , Sung-Ho Kim ¹ , Dong-Min Kim ¹ , Sun-Young Kwak ¹ , Hyuk-Min Kwon ² , Yong-Goo Kim ³ , and Hi-Deok Lee ¹ ¹ Department of Electronics Engineering, Chungnam National University, ² School of Electronic & Electrical Engineering, Hankyong National University, ³ Department of Green Semiconductor System, Korea Polytechnic
FP-322 ZONE 2-2 (5층, 로비)	CMOS 호환 Dual-Via MFIM 구조의 HZO 기반 FTJ 소자 개발 구분성, 양희수, 김우석, 이상화, 전강혁, 권윤아, 설우석 나노종합기술원 나노소자기술실
FP-323 ZONE 2-2 (5층, 로비)	Device-Aware Nonlinear Neural-Network Training Framework for Analog In-Memory Computing Juhwan Park ¹ , Junseo Park ² , Joonhan Kim ³ , and Jongwook Jeon ¹ ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³ Department of Display Engineering, Sungkyunkwan University
FP-324 ZONE 2-2 (5층, 로비)	Optimization of DRAM Sense Amplifier Delay-Power Trade-Off Using LUT-Based Retarget Model and NSGA-II Framework Sueyeon Kim ¹ , Jaeweon Kang ² , Suyeong Hwang ³ , Jaekyoung Son ¹ , and Jongwook Jeon ¹ ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³ Department of Display Engineering, Sungkyunkwan University
FP-326 ZONE 2-2 (5층, 로비)	Automated, Physics-Guided AI Framework for Asymmetry-Aware Ferroelectric Compact Models Joonhan Kim ¹ , Joonhyeok Lee ² , Juhwan Park ² , Minseop Kim ³ , Jaewon Kang ³ , and Jongwook Jeon ² ¹ Department of Display Engineering, Sungkyunkwan University, ² Department of Electrical and Computer Engineering, Sungkyunkwan University, ³ Department of Semiconductor Convergence Engineering, Sungkyunkwan University
FP-327 ZONE 2-2 (5층, 로비)	Enhanced Memory Performance of Flash Memory Using Doped ALD HfO_2 Based Charge Trap Layer Yunseo Lim ¹ , Dohyun Lee ² , Jisu Park ² , Sangmyun Lim ² , Seongho Lee ¹ , Hansol Lee ³ , Hyungjun Kim ³ , Jaehyun Yang ³ , Youngseon Son ³ , Bio Kim ³ , Hanmei Choi ³ , and Changhwan Choi ^{1,2} ¹ Division of Materials Science & Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University, ³ Memory Process Development Team, Samsung Electronics Co., Ltd.
FP-328 ZONE 2-2 (5층, 로비)	The Study of Interfacial Layer Effects for Enhanced ReRAM Characteristics Ingon Bae, Seong Gil Ham, Sun Woo Lee, Hyeontae Lee, Yong Han Lee, Seunghoon Lee, Inhee Chang, Tae Woo Kim, Yong Keon Choi, and Sang Gi Lee Development Team2, DB HiTek
FP-329 ZONE 2-2 (5층, 로비)	A 2T0C DRAM-Based Ising Solver Harnessing Intrinsic Decaying Currents for Simulated Annealing Homin Kang ^{1,2} and Dongseok Kwon ² ¹ Division of Physics and Semiconductor Science, Dongguk University, ² Department of Semiconductor Engineering, GIST
FP-330 ZONE 2-2 (5층, 로비)	이온-겔 멀티 게이트 GFET 의 수상돌기 연산을 통한 방향 선택성 구현 박준하 ^{1,2} , 김호연 ¹ , 장현재 ¹ , 김재욱 ¹ , 박종길 ¹ , 박성식 ¹ , 김인호 ¹ , 이수연 ¹ , 황규원 ¹ , 박종국 ¹ , 정연주 ¹ , 주병관 ² , 이경석 ¹ ¹ 한국과학기술연구원 반도체기술연구단, ² 고려대학교 전기전자공학과

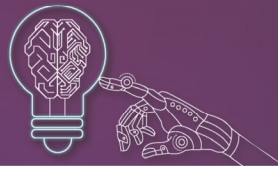
FP-331 ZONE 2-2 (5층, 로비)	Realization of Selector-Only Memory Behavior through Polarity-Dependent Switching in Toxic-Element-Free Ge-Te Binary Chalcogenide Devices Minju Kim ¹ , Saegyoung Song ¹ , Byeongchan Sim ¹ , Dong Il Kim ¹ , Hui Gu Lee ² , Jeongwoo Seo ¹ , Hyunwee Cho ¹ , and Jin Pyo Hong ^{1,2} ¹ Research Institute of Natural Science, Department of Physics, Hanyang University, ² Research Institute for Convergence of Basic Sciences, Division of Nanoscale Semiconductor Engineering, Hanyang University
FP-332 ZONE 2-2 (5층, 로비)	Interface Layer Insertion for Stable Bias-Polarity-Dependent Switching in Selector-Only-Memory Devices Byeongchan Sim ¹ , Jeongwoo Seo ¹ , Saegyoung Song ¹ , Minju Kim ¹ , Huigu Lee ² , Dong Il Kim ¹ , Hyunwee Cho ¹ , and Jin Pyo Hong ^{1,2} ¹ Department of Physics, Hanyang University, ² Division of Nano-Scale Semiconductor Engineering, Hanyang University
FP-333 ZONE 2-2 (5층, 로비)	Transition Metal Active Electrode Based Synapse Device for Energy-Efficient Neuromorphic Computing Sang Min Lee ^{1,2} , Sung min Yoo ^{1,3} , Seung kyu Kang ¹ , Kyeong-Seok Lee ¹ , Gyu Weon Hwang ¹ , YeonJoo Jeong ¹ , Jaewook Kim ¹ , Jongkil Park ¹ , Seongsik Park ¹ , Hyun Jae Jang ¹ , Jong-Keuk Park ¹ , Byeong-Kwon Ju ^{2,3} , and Inho Kim ¹ ¹ Center for Semiconductor Technology, KIST, ² Department of Micro/Nano Systems, Korea University, ³ School of Electrical Engineering, Korea University
FP-334 ZONE 2-2 (5층, 로비)	Investigation of Top Electrode Thickness Effects on RRAM Switching through TCAD-KMC Simulation Heerak Wi ¹ , Ria Choi ¹ , Dae Kyu Lee ² , Yesong Yu ¹ , Chaei Lee ¹ , Hae Lyn Choi ¹ , Min Jee Kim ² , and Joon Young Kwak ¹ ¹ Division of Electronic and Semiconductor Engineering, Ewha Womans University, ² Center for Semiconductor Technology, KIST
FP-335 ZONE 2-2 (5층, 로비)	Enhancement of Data Retention and Thermal Stability in Si-Doped Ge₂Sb₂Te₅ Phase Change Memory Seungmin kang ¹ , Ji Su Park ¹ , Woo Seong Son ¹ , and Changhwan Choi ^{1,2} ¹ Department of Semiconductor Engineering, Hanyang University, ² Division of Material Science and Engineering, Hanyang University
FP-336 ZONE 2-2 (5층, 로비)	Impact of Zr Doping on Trap Level Energy and Memory Window in HfO₂-Based Charge Trapping Layers for 3D NAND Flash Jisu Park ¹ , Dohyun Lee ¹ , Sangmyun Lim ¹ , Gyu Min Hwang ¹ , Hoon Pyo ¹ , Yun Seo Lim ² , Seongho Lee ² , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-337 ZONE 2-2 (5층, 로비)	Enhanced Synapse Performances of Cu:Te/TaOx/IGZO Memristor by Interface Engineering Sungmin Yoo ^{1,2} , Sangmin Lee ^{1,2} , Seung Kyu Kang ¹ , Kyeong-Seok Lee ¹ , Gyu Weon Hwang ¹ , YeonJoo Jeong ¹ , Jaewook Kim ¹ , Jongkil Park ¹ , Seongsik Park ¹ , Hyun Jae Jang ¹ , Byeong-Kwon Ju ² , Inho Kim ¹ , and Jong Keuk Park ¹ ¹ Center for Semiconductor Technology, KIST, ² Display and Nanosystem Laboratory, School of Electrical Engineering, Korea
FP-338 ZONE 2-2 (5층, 로비)	Enhancement of Capacitive Memory Window in HZO-Based Ferroelectric Capacitors by Incorporating an Optimized IGZO Layer Sookyung Kim ^{1,2} and Rino Choi ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University



FP-339 ZONE 2-2 (5층, 로비)	Effect of Nitrogen Incorporation in TiN Bottom Electrode on Morphotropic Phase Boundary Formation of Hafnium-Zirconium Oxide Thin Film Soo Min Lee ^{1,2} and Rino Choi ^{1,2} ¹ Department of Materials Science and Engineering, Inha University, ² Program in Semiconductor Convergence, Inha University
FP-340 ZONE 2-2 (5층, 로비)	Enhancing Endurance and Uniformity in ZnO-Based RRAM by Double-Layer SiCN Oxygen Blocking and Storage Structures Woon San Ko, So Yeong Park, Hye Ri Hong, Seong Jo Jo, and Ga Won Lee Chungnam National University
FP-341 ZONE 2-2 (5층, 로비)	Correlation Between Rare-Earth-Induced Trap Depth and Memory Retention in High-k ZrO₂-Based Charge-Trapping Layers Gyumin Hwang ¹ , Yunseo Lim ² , Dohyun Lee ¹ , Seongho Lee ² , Jisu Park ¹ , Sangmyun Lim ¹ , Jinyeong Lee ¹ , Hansol Oh ³ , Yongjoo Park ³ , and Changhwan Choi ^{1,2} ¹ Department of Semiconductor Engineering, Hanyang University, ² Division of Materials Science and Engineering, Hanyang University, ³ Advanced Research Development Team, SK trichem Co., Ltd.
FP-342 ZONE 2-2 (5층, 로비)	Enhanced Memory Characteristics of MIFIS Gate Stack Using Doped HfO₂ Gate Interlayer Yunseo Lim ¹ , Kyungsoo Park ¹ , Hyeoncheol Jeong ² , and Changhwan Choi ^{1,2} ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Semiconductor Engineering, Hanyang University
FP-343 ZONE 2-2 (5층, 로비)	Geometry-Dependent Optimization of Resistive Switching Characteristics in Laser-Induced Graphene Memristors Ha-seop Jeong ¹ and Doowon Lee ² ¹ Major in Semiconductor & Information Engineering, Kongju National University, ² Division of Electrical, Electronic and Control Engineering, Kongju National University

P. Device for Energy (Solar Cell, Power Device, Battery, etc.) 분과

FP-344 ZONE 2-2 (5층, 로비)	Performance Enhancement of 3.3 kV 4H-SiC Junction Barrier Schottky Diode Using a Ditch Structure Dong Hoon Lee, Sung Mo Koo, Myung Hwan Lee, Weon Chan Kim, Dong Ju Kang, and Sang Gi Kim EYEQ Lab Research Center
FP-345 ZONE 2-2 (5층, 로비)	Structural and Electrical Properties of Al-Doped NiO Thin Films Deposited Using Optimized Ceramic Targets Saem Yoon, Jae Ho Park, Min Joon Kim, Hyoung Wook Kim, Thi Muoi Vo, Woo Hyoung Noh, and Chung Wung Bark Department of Semiconductor Engineering, Gachon University
FP-346 ZONE 2-2 (5층, 로비)	Proton-Irradiation Effects on Graphene Supercapacitor as a Energy Storage Device for Space Applications Selvaraj David ^{1,2} , Suyeon oh ¹ , Jongdae Sohn ³ , and Yung Ho Kahng ^{2,4} ¹ Department of Earth Science Education, Chonnam National University, ² Department of Physics Education, Chonnam National University, ³ Space Science Division, KASI, ⁴ Center for Quantum Technologies, Chonnam National University
FP-347 ZONE 2-2 (5층, 로비)	Plasma 처리를 통한 GaON 형성과 Stack Metal Oxide를 통한 AlGaIn / GaN HEMTs의 전기적 특성 향상 분석 김동찬 ¹ , 한석현 ¹ , 이지훈 ¹ , 김윤석 ² , 김남훈 ² , 송준오 ³ , 한영훈 ³ ¹ 한국공학대학교 IT반도체융합공학과, ² 한국공학대학교 반도체공학부, ³ 주웨이브로드
FP-348 ZONE 2-2 (5층, 로비)	반도체 세정용 RPS의 플라즈마 생성 및 유지를 위한 전력변환장치 기술 개발 이예지, 김무영, 유승종, 장희범, 정의훈 명지대학교 전기전자공학부
FP-349 ZONE 2-2 (5층, 로비)	Improved Moisture Stability in n-i-p Perovskite Solar Cells with Li-Doped NiO Hole Transport Layers Hyung Wook Kim, Jae Ho Park, Yoon Same, Min Joon Kim, and Chung Wung Bark Department of Semiconductor Engineering, Gachon University
FP-350 ZONE 2-2 (5층, 로비)	Modified Screen-Printing Front Side Metallization for Perovskite/Si Tandem Solar Cells via TCO Interface Engineering and Pressure-Assisted Sintering Md Halim Hossain ^{1,2} , Gwoncheol Choi ^{1,3} , Jeongho Lee ^{1,4} , Giryeong Kwon ¹ , Dohkwon Lee ¹ , and Inho Kim ^{1,2} ¹ KIST, ² UST, ³ Seoul National University of Science & Technology, ⁴ Korea University
FP-351 ZONE 2-2 (5층, 로비)	p-GaN 식각 조건에 따른 E-mode AlGaIn/GaN HEMT의 전기적 특성 분석 이지훈 ¹ , 한석현 ¹ , 김동찬 ¹ , 김윤석 ² , 김남훈 ² , 송준오 ³ , 한영훈 ³ ¹ 한국공학대학교 IT반도체융합공학과, ² 한국공학대학교 반도체공학부, ³ 주웨이브로드
FP-352 ZONE 2-2 (5층, 로비)	Facile Synthesis of Sputtered Steel Modified with Selenium Electrocatalyst for Oxygen Evolution Reaction D. D. Megersa ^{1,2} and H. K. Yu ^{1,2} ¹ Department of Energy Systems Research, Ajou University, ² Department of Materials Science and Engineering, Ajou University
FP-353 ZONE 2-2 (5층, 로비)	A Flexible Porous Energy Storage System Using Flash Lamp Annealing with Iodine-Based Redox Mediator for High Performance Jeong Seo Lee ¹ , Hyoung Woo Yang ² , and A-Rang Jang ¹ ¹ Division of Electrical, Electronic and Control Engineering, Kongju National University, ² Firbl



FP-354 ZONE 2-2 (5층, 로비)	Ultra-Thin Electromagnetic Wave-Absorbing Transparent Shielding Materials for 6G Band Applications Youngho Kim and Hak Ki Yu Department of Materials Science and Engineering & Department of Energy Systems Research, Ajou University
FP-356 ZONE 2-2 (5층, 로비)	PVDF-HFP 기반 고분자 전해질 분리막의 제조공정에 따른 구조 특성 및 이온전도도 연구 염지원, 이상현 이화여자대학교 화공신소재공학과 양자화학공학연구소
FP-357 ZONE 2-2 (5층, 로비)	A Normally-Off Double Superjunction GaN CAVET with a Source Reverse Path Diode (SPRD) for Enhanced Switching Characteristics Jeon-yun Lee, Byeong-Jun Park, and Sung-Ho Hahm School of Electronic and Electrical Engineering, Kyungpook National University
FP-358 ZONE 2-2 (5층, 로비)	Synthesis and Energy Harvesting Applications of Ba-Doped Ceramics/PDMS Composite Films Hong Mu Park, Anand Kurakula, and Jae Su Yu Kyung Hee University
FP-359 ZONE 2-2 (5층, 로비)	Influence of Oxygen Vacancies Introduced via Acceptor (Gadolinium) Doping to the Pseudocapacitive Properties of Nano-Sized Cerium Oxide Yeeun Kim, Soyoung Kim, Haewon Jin, Youngim Jo, Myung Jung Lee, and Seong K. Kim Department of Chemical Engineering, Hannam University
FP-360 ZONE 2-2 (5층, 로비)	생체 적합 유연 에너지 하베스터를 이용한 체내 자력 무선 전송 손민곤, 안재훈, 이건재 Department of Material Science and Engineering, KAIST
FP-361 ZONE 2-2 (5층, 로비)	Electro-Thermal Analysis of Trap-Induced Degradation in GaN HEMTs Using TCAD Dongchan Kim ¹ , Jaejun Woo ² , Johyeon Kim ² , Soomin Kim ³ , and Jongwook Jeon ¹ ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³ Department of Display Engineering, Sungkyunkwan University
FP-362 ZONE 2-2 (5층, 로비)	Design and Experimental Realization of Optimized Cross-Groove Texture in Micro-Scale for Perovskite/Silicon Tandem Solar Cells Jeongho Lee ^{1,3} , Md Halim Hossain ^{1,5} , Gwoncheol Choi ^{1,6} , Kwon Giryong ^{2,4} , Doh-Kwon Lee ² , and Inho Kim ¹ ¹ Center for Semiconductor Technology, KIST, ² Advanced Photovoltaics Research Center, KIST, ³ School of Electronics and Electrical Engineering, Korea University, ⁴ Department of Materials Science and Engineering, Korea University, ⁵ Nanoscience and Technology, University of Science and Technology, ⁶ Material Science and Engineering, Seoul National University of Science & Technology
FP-363 ZONE 2-2 (5층, 로비)	Micro-networked Silver Current Collector Fabrication Using Self-cracked WO₃ Thin Film as Sacrificial Layer on PDMS Substrates Jun Ho Yoon ^{1,2} and Hak Ki Yu ^{1,2} ¹ Department of Energy Systems Research, Ajou University, ² Department of Materials Science and Engineering, Ajou University
FP-364 ZONE 2-2 (5층, 로비)	Synergistic Dual-Nucleation Approach for Supercooling Suppression and Thermal Buffering in Sodium Sulfate Decahydrate Jin Guk Kim ^{1,2} and Hak Ki Yu ^{1,2} ¹ Department of Materials Science and Engineering, Ajou University, ² Department of Energy Systems Research, Ajou University

FP-365 ZONE 2-2 (5층, 로비)	One-Step Synthesis of Bi_2SiO_5 Film for Uniform UV-B Photodetector Using Chemical Vapor Deposition (CVD) Jeong-Ho Jang ¹ and Hak Ki Yu ^{1,2} ¹ Department of Energy Systems Research, Ajou University, ² Department of Materials Science and Engineering, Ajou University
FP-366 ZONE 2-2 (5층, 로비)	Impact of Structural Variations on the Performance of GaN Merged-PiN-Schottky(MPS) Devices Ju Eun An and Ho-Jun Lee Department of Electrical and Electronics Engineering, Pusan National University
FP-367 ZONE 2-2 (5층, 로비)	Defect-engineered Hexagonal SnSe_2 Nanoflakes with MXene Conductive Nanosheet for Piezoelectric-coupled Hybrid Triboelectric Nanogenerator Geunchul Kim ¹ and Daewon Kim ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Institute for Wearable Convergence Electronics, Kyung Hee University
FP-368 ZONE 2-2 (5층, 로비)	Quantitative Investigation of Junction Dependent Light Responsive Output Enhancement in Perovskite-Based Triboelectric Nanogenerator Jonghwan Lee ¹ and Daewon Kim ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Institute for Wearable Convergence Electronics, Kyung Hee University
FP-369 ZONE 2-2 (5층, 로비)	Enhanced Performance of Self-Powered UV-C Photodetectors via Surface Treatment and Polyethyleneimine Integration with Wide-Bandgap Semiconductors Maro Kim and Chung Wung Bark Department of Electrical Engineering, Gachon University
FP-370 ZONE 2-2 (5층, 로비)	Enhancement of a Field-Effect-Transistor-Mechanism Droplet-Based Triboelectric-Magnetoelastic Generator via Fluorine-Doped Graphene Jaekang Kim ¹ and Daewon Kim ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Institute for Wearable Convergence Electronics, Kyung Hee University
FP-371 ZONE 2-2 (5층, 로비)	Synergetic Hybrid of 1D Carbon Nanotubes and 0D Silver Nanoparticles for High Dielectric Triboelectric Nanogenerator Yuseob Lee ¹ and Daewon Kim ^{1,2} ¹ Department of Semiconductor Engineering, Kyung Hee University, ² Department of Electronic Engineering, Institute for Wearable Convergence Electronics, Kyung Hee University
FP-372 ZONE 2-2 (5층, 로비)	Contact-Mediated Long-Range p-Doping Using NiSe_2 Ji Kwon Bae ^{1,2,3,4} and Hak Ki Yu ^{1,2} ¹ Department of Energy Systems Research, Ajou University, ² Department of Materials Science Engineering, Ajou University, ³ Department of Materials Science and Engineering, Seoul National University, ⁴ Research Institute of Advanced Materials, Seoul National University