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(공식발표시간: 17:40-19:00)

ZONE1 (4층, 로비)

[TP] 포스터세션

G. Device & Process Modeling, Simulation and Reliability 분과

TP-193	<p>Mechanistic Evaluation of IGZO-Based 1T-1C Memory Cell via Fast I-V Measurements and HSPICE Modeling</p> <p>Juneseong Choi^{1,2}, Minsub Um^{1,2}, Jinheon Choi^{1,2}, Sahngik Aaron Mun^{1,2}, Seoryong Park^{1,2}, Jaewon Ham^{1,2}, Hyungjeung Kim^{1,2}, Shihyun Kim^{1,2}, Subin Moon^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-194	<p>Experimental Demonstration of Analog Multi-Level Operation in a 16×16 2T0C Array</p> <p>Hyungjeung Kim^{1,2}, Néstor Ghenzi^{1,2}, Shihyun Kim^{1,2}, Yonghee Lee^{1,2}, Jinheon Choi^{1,2}, Sahngik Aaron Mun^{1,2}, Juneseong Choi^{1,2}, Jaewon Ham^{1,2}, Seoryong Park^{1,2}, Subin Moon^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-195	<p>A 2T0C Cell Enabling Multi-Timescale Reservoir Computing</p> <p>Shihyun Kim^{1,2}, Yonghee Lee^{1,2}, Hyungjeung Kim^{1,2}, Néstor Ghenzi^{1,2}, Sahngik Aaron Mun^{1,2}, Jinheon Choi^{1,2}, Junseong Choi^{1,2}, Jaewon Ham^{1,2}, Seoryong Park^{1,2}, Subin Moon^{1,2}, Minsub Um^{1,2}, Jeongyoon Lee^{1,2}, Kyung Seok Woo^{3,4}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³Graduate School of Semiconductor Materials and Devices Engineering, ⁴UNIST, ⁴Department of Materials Science and Engineering, UNIST</p>



TP-196	<p>Artificial Neural Network Modeling of Feedback Field-Effect Transistor Considering Current State for Implementing Circuit Simulation Jong Hyeok Oh, Seung Su Jeong, and Yun Seop Yu Major of ICT & Robotics Engineering, Hankyong National University</p>
TP-197	<p>First-Principles Study of Amorphous Oxide Semiconductors Using DFT+U+V Method Yumi Kim, Subeen Lim, and Yeonghun Lee Department of Electronics Engineering, Incheon National University</p>
TP-198	<p>Transport in Amorphous Semiconductors Using Effective Medium Theory Hyeonseok Choi and Yeonghun Lee Department of Electronics Engineering, Incheon National University</p>
TP-199	<p>A Hafnia-Based Ferroelectric Tunnel-FET with a Stepped Gate Stack for Reliable Ternary Content Addressable Memory Implementation Jiwon Park^{1,2} and Woo Young Choi^{1,2} ¹Department of Electrical and Computer Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-200	<p>Metal to Insulator Transition in Rutile- and Anatase-Based Polytypes of MO_2 ($M=V, Nb, Ta$) by First Principles Calculations Dohyun Kim^{1,2,3}, Taeyoung Jeong^{1,2,3}, Seungjae Yoon^{1,2,3}, Yunjae Kim^{1,2,3}, Cheol Seong Hwang^{2,3}, and Jung-Hae Choi¹ ¹Electronic and Hybrid Materials Research Center, KIST, ²Department of Materials Science and Engineering, Seoul National University, ³Inter-university Semiconductor Research Center, Seoul National University</p>
TP-201	<p>Effect of Field Crowding Induced by Corner-Rounding on NBTI in Vertically Stacked Nanosheet MOSFET Kyeong Min Lim, Jin Park, Won Suk Koh, Gang San Yun, Soo Bean Song, and In Man Kang School of Electronic and Electrical Engineering, Kyungpook National University</p>
TP-202	<p>플라즈마 처리된 포토레지스트 잔류물이 MoS₂ 전자 구조에 미치는 영향: MD 및 DFT 연구 엄상민¹, 김다현², 김명수², 김병조¹ ¹울산과학기술원 반도체소재부품대학원, ²울산과학기술원 전기전자공학과</p>



TP-203	<p>Gated Recurrent Unit 을 활용한 Feedback Field-Effect Transistor 의 Compact SPICE 모델 연구</p> <p>손승원¹, 우솔아^{1,2}</p> <p>¹국립부경대학교 지능로봇학과, ²국립부경대학교 전자공학과</p>
TP-204	<p>Role of Electron-Phonon Interaction on Transport in Amorphous Semiconductors: A First-Principles Study</p> <p>Subeen Lim, Do Lee, and Yeonghun Lee</p> <p>Department of Electronics Engineering, Incheon National University</p>
TP-205	<p>게이트 구조에 따른 Nanosheet FBFET의 전기적 특성 분석</p> <p>박태호, 조경아, 김상식</p> <p>고려대학교 전기전자공학부</p>
TP-206	<p>Optimization of Recess-Structured Metal-Oxide TFTs for Footprint Reduction Using TCAD Simulation</p> <p>Hyeon Jeong Park, Kook Cheol Moon, and Yong-Sang Kim</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-207	<p>Photoresponsive Characterization for Energy-Mapping of Donor- and Acceptor-Like Interface Traps in 5-Stage Si-NW GAA FETs</p> <p>Soohyun Lim¹, Jaewoo Yoo¹, Hongseung Lee¹, Seongbin Lim¹, Minah Park¹, Seohyeon Park¹, Sojin Jung¹, Donghyeon Lee¹, Dongsun Shin¹, Junhui Park¹, Sieun Lee¹, Yang-kyu Choi², and Hagooul Bae¹</p> <p>¹Jeonbuk National University, ²KAIST</p>
TP-208	<p>Research of the Electrical Characteristics of Floating-Gate-Based Nanoscale Neuromorphic Transistors</p> <p>Kangmin Yoo¹, Hyeonseok Jeong¹, Juyeong Chae¹, Taehwan Koo¹, Hyeongjin Chae¹, Joonhyuk Kim³, Munseok Jung³, Dongyeop Kim³, and Moongyu Jang^{1,2}</p> <p>¹School of Nano Convergence Technology, Hallym University, ²Center of Nano Convergence Technology, Hallym University, ³School of Semiconductor and Display, Hallym University</p>



TP-210	<p>Compact Modeling of Self-Rectifying MoS₂-Based RRAM with Schottky Barrier-Induced Rectification Yoojin Shin and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology</p>
TP-211	<p>Automated Optical Detection and Thickness Estimation of 2D Flakes for Large-Scale FET Data Acquisition Sanghyun Lee¹, Haksoon Jung¹, Sumin Hong², Minho Park¹, and Jimin Kwon^{1,2} ¹Department of Electrical Engineering, UNIST, ²Graduate School of Semiconductor Materials and Devices Engineering, UNIST</p>
TP-212	<p>PN Separation Piling Process for Performance Optimization of 2 nm Gate-All-Around Field-Effect Transistors Jaehyuk Lim¹, Hyejin Park², Seonghan Lee³, and Changhwan Shin³ ¹Research Institute of Semiconductor Technology, Korea University, ²School of Chemical Engineering, Soongsil University, ³School of Electrical Engineering, College of Engineering, Korea University</p>
TP-213	<p>Atomistic Modeling on the Effects of Interface and Geometry in NiTe₂-Te Contact for p-Type Te-Channel Thin-Film Transistors Seungjae Yoon^{1,2,3}, Wonho Choi^{2,3}, Byongwoo Park^{2,3}, Taeyoung Jeong^{1,2,3}, Dohyun Kim^{1,2,3}, Yunjae Kim^{1,2,3}, Cheol Seong Hwang^{2,3}, and Jung-Hae Choi¹ ¹Electronic and Hybrid Materials Research Center, KIST, ²Department of Materials Science and Engineering, Seoul National University, ³Inter-university Semiconductor Research Center, Seoul National University</p>
TP-214	<p>VacHopPy: Automating Effective Parameter Extraction for Vacancy-Mediated Diffusion from Molecular Dynamics Taeyoung Jeong^{1,2}, Kun Hee Ye^{1,2}, Seungjae Yoon^{1,2}, Dohyun Kim^{1,2}, Yunjae Kim^{1,2}, Cheol Seong Hwang², and Jung-Hae Choi¹ ¹Electronic and Hybrid Materials Research Center, KIST, ²Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University</p>



TP-215	<p>Vertical Electric Field Modulation by Ionic Dynamics in Dielectrics</p> <p>Jiyoon Kim¹, Jaeuk Bang², Seong Chu Lim², and Hyunjin Ji¹</p> <p>¹Department of Electrical, Electronic and Computer Engineering, University of Ulsan, ²Department of Energy Science, Sungkyunkwan University</p>
TP-216	<p>종이온 조사에 따른 매립채널 어레이 트랜지스터의 신뢰성 분석</p> <p>김소원¹, 김건¹, 김동영¹, 김수연¹, 박제원¹, 김신욱¹, 강륜¹, 임채혁¹, 이주원¹, 서현아¹, 윤정현¹, 이해린¹, 최우진², 김어진², 정민우², 이명진²</p> <p>¹전남대학교 지능전자컴퓨터공학과, ²전남대학교 전자컴퓨터공학부</p>
TP-217	<p>A Study on the Simultaneous Improvement of Performance and NBTI Reliability in PMOSFETs Using Passivation Tensile-SiN Process</p> <p>Icksu-Jeon, Sangwon-Yun, Chaemin-Yeom, Hoyeon-Choi, Manlyun Ha, and Sanggi Lee</p> <p>CIS Process Dev. Team/ Reliability Test Team, DB HiTek</p>
TP-218	<p>Process-Induced Warpage Behavior in Backside Power Delivery Network (BSPDN) Fabrication</p> <p>Seong-Ji Min, Jun-hyeok Lee, Hyeong-kyu Jin, Won-chan Kim, and Hyun-Yong Yu</p> <p>Korea University</p>
TP-219	<p>Hydrogen Effects in Indium–Gallium–Zinc–Oxide Thin Films via High-Pressure Deuterium Annealing</p> <p>Inkyu Yoon, Donghyun Kim, Sang Hyeok Kim, Sukyeong Kang, Sangpil Yoon, and Jae Woo Lee</p> <p>Department of Electronics & Information Engineering, Korea University</p>
TP-220	<p>New Weight Modulation Method for Charge Trap Flash-Based Synaptic Devices</p> <p>Jeong-hyun Kim and Jong-ho Lee</p> <p>Seoul National University</p>
TP-221	<p>Investigation of the Buried Photodiode Structure for Dark Noise Suppression in Silicon Infrared Image Sensors</p> <p>Hyeong-Soo Kim, Yu-Jin Cho, E Kyoung Kim, Areum Han, and Moon Hee Kang</p> <p>School of Semiconductor Engineering, Chungbuk National University</p>



TP-222	<p>NiO/β-Ga₂O₃ p-n 다이오드의 동적 회로 설계</p> <p>최윤영¹, 조영관¹, 서다원², 김동빈³, 조병진³, 신명훈¹</p> <p>¹한국항공대학교 우주시스템융합전공, ²한국항공대학교 전자및항공전자공학, ³한국과학기술원 전기및전자공학부</p>
TP-223	<p>First-Principles Investigation of Inhomogeneous Ferroelectric Switching Pathways in Wurtzite AlN</p> <p>Yunjae Kim^{1,2,3}, Kun Hee Ye^{1,2,3}, Taeyoung Jeong^{1,2,3}, Seung Jae Yoon^{1,2,3}, Dohyun Kim^{1,2,3}, Cheol Seong Hwang^{2,3}, and Jung-Hae Choi¹</p> <p>¹Electronic and Hybrid Materials Research Center, KIST, ²Department of Materials Science and Engineering, Seoul National University, ³Inter-university Semiconductor Research Center, Seoul National University</p>
TP-224	<p>CFD-Based Analysis of Flow-Through Line-Charging Effects on Deposition Efficiency in Atomic Layer Deposition</p> <p>Seulwon Choi¹, Yunseok Kim¹, and Hwanyeol Park^{1,2}</p> <p>¹Department of Electronic Materials, Devices, and Equipment Engineering, Soonchunhyang University, ²Department of Display Materials Engineering, Soonchunhyang University</p>
TP-225	<p>Computational Fluid Dynamics Analysis of Ternary HfAlO_x Spatial ALD with Dynamic Wafer Motion</p> <p>Huichan Kang¹, Yunseok Kim¹, and Hwanyeol Park^{1,2}</p> <p>¹Department of Electronic Materials, Devices, and Equipment Engineering, Soonchunhyang University, ²Department of Display Materials Engineering, Soonchunhyang University</p>
TP-226	<p>Multiscale CFD-DFT Coupled Modeling for Growth Mechanism Control in Spatial ALD of Al₂O₃</p> <p>Yunseok Kim^{1,2}, Seulwon Choi^{1,2}, and Hwanyeol Park^{1,2}</p> <p>¹Department of Electronic Materials, Devices, and Equipment Engineering, Soonchunhyang University, ²Department of Display Materials Engineering, Soonchunhyang University</p>



TP-227	<p>Comparative Kinetic-Thermodynamic Analysis of Ru Thenium Precursors on TiN for ALD by DFT Calculations Mai Phuong Uyen Vu¹ and Hwanyeol Park^{1,2} ¹Department of Electronic Materials and Devices Convergence Engineering, Soonchunhyang University, ²Department of Display Materials Engineering, Soonchunhyang University</p>
TP-228	<p>Comparative of PMA and PDA Ferroelectric HZO MFM Devices with New Noise Analysis Somi Lee, Donghyun Kim, Inkyu Yoon, Seung Gyu Lee, Lae Hyeong Jeong, Yoonhye Jang, and Jae Woo LEE Department of Electronics & Information Engineering, Korea University</p>
TP-229	<p>“내 칩 제작 서비스”를 통한 반도체 소자/공정/설계 융합교육 전략 김규태¹, 김용신¹, 심규현², 국일호³, 노태문³, 박건식³, 정동윤³ ¹고려대학교 전기전자공학부, ²한성대학교 컴퓨터공학과, ³한국전자통신연구원</p>
TP-230	<p>Temperature-Dependent Analysis of Leakage and Disturb Phenomena in 1z-Generation DRAM Subin Lee and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology</p>
TP-231	<p>Effect of Poly-Silicon Grain Boundaries on the Electrical Characteristics of Positive Feedback Field-Effect Transistors Damin Kim and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology</p>
TP-232	<p>Analysis of Sneak Path Current Characteristics in Resistive Random-Access Memory Arrays Using a Compact Model Minsang Kim and Min-woo Kwon Department of Electronic Engineering, Seoul National University of Science & Technology</p>



TP-233	<p>Deep Learning-Based Pareto Optimization Framework for LDMOS Transistors</p> <p>Shinwook Kim¹, Geon Kim¹, Dongyeong Kim¹, Suyeon Kim¹, Jewon Park¹, Ryun Kang¹, Sowon Kim¹, Chaehyuk Lim¹, Hyeona Seo¹, Jeonghyeon Yun¹, Juwon Lee¹, Hyerin Lee¹, Ujin Choi¹, Eojin Kim², Minwoo Jeong², and Myoungjin Lee²</p> <p>¹Department of Intelligent Electronics and Computer Engineering, Chonnam National University, ²Department of Electronics and Computer Engineering, Chonnam National University</p>
TP-234	<p>Experimental Investigation of Structural Parameter Effects on Switching Behavior of Dual-Gate Feedback Field-Effect Transistors</p> <p>Hyunjin Kang and Min-woo Kwon</p> <p>Department of Electronic Engineering, Seoul National University of Science & Technology</p>
TP-235	<p>Pulse IV를 이용한 Te FET의 Hysteresis 특성 분석</p> <p>최우석¹, 김민재², 이병훈^{1,2,3}</p> <p>¹Graduate school of semiconductor technology, POSTECH, ²National Institute for Nanomaterials Technology, POSTECH, ³Department of Electrical Engineering, POSTECH</p>
TP-236	<p>고에너지 양성자를 이용한 고유전 절연막의 신뢰성 개선 연구</p> <p>이찬빈¹, 최우석², 이용수⁴, 김병관², 황현준⁵, 강창구⁴, 이병훈^{1,2,3}</p> <p>¹Department of Electrical Engineering, POSTECH, ²Graduate school of Semiconductor Technology, POSTECH, ³Center for Semiconductor Technology Convergence, POSTECH, ⁴Advanced Radiation Technology Institute, KAERI, ⁵Department of Semiconductor Engineering, Mokpo National University</p>
TP-237	<p>2D TMD 기반 FET에서 고속 중성자 조사로 유도된 채널 결함 및 전기적 열화</p> <p>백승용¹, 유재욱³, 심기단¹, 허지원¹, 임수빈⁵, 정봉기⁵, 배학열³, 김태완^{1,2,4}</p> <p>¹서울시립대학교 지능형반도체학과, ²서울시립대학교 첨단융합학부, ³전북대학교 전자공 학과, ⁴(주)투디에피, ⁵(주)큐빔솔루션</p>
TP-238	<p>Modeling of the Ferroelectric/antiferroelectric Capacitor and Related Memory Systems</p> <p>Seungyong Byun^{1,2}, Seunghyun Choi^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>



TP-239	<p>A Study on ESD BJT Devices with Reduced Electric Fields Employing ESD Implant Layer Ji-Hoon HAN and Young Chul Kim ESD/TCAD Team, TE Center, DB HiTek</p>
TP-240	<p>Design Strategies for Enhancing ESD Robustness of nLDMOS in BCD Process Se-heon Oh and Young-chul Kim Technology Development, DB HiTek</p>
TP-241	<p>Investigation of Carbon and Nitrogen Co-Doping Effects in Amorphous GeTe Using First-Principles and Machine-Learning Potential Changmin Lim¹ and Jiwon Chang¹ ¹Department of System Semiconductor Engineering, Yonsei University, ²BK21 Graduate Program in Intelligent Semiconductor Technology</p>
TP-242	<p>Improving DC and RF Performance in NanosheetFET via Asymmetric Dual-κ Inner Spacer Sang Min Lee and Hyun Woo Kim Department of Electrical and Electronics Engineering, Konkuk University</p>
TP-243	<p>Hybrid Technique for Extraction of Intrinsic Oxide Trap Density in a-IGZO TFTs with 1/f Noise and Single-Wavelength Light Characterization Donghyeon Lee¹, Jaewook Yoo¹, Hongseung Lee¹, Seongbin Lim¹, Sojin Jung¹, Seohyeon Park¹, Minah Park¹, Dongsun Shin¹, Sieun Lee¹, Soohyun Lim¹, Junhui Park¹, Sangmoon Yoon⁴, TaeWan Kim³, Kiyoung Lee², and Hagyoul Bae¹ ¹Jeonbuk National University, ²Hongik University, ³University of Seoul, ⁴Gachon University</p>
TP-244	<p>Development of a Rare-Earth-Reduced Magnetic Collet based on Halbach Array for Damage-Free Semiconductor Packaging Hyoung Su Kim¹, Young Jun Lee¹, Joon Beom An¹, Dae kyeom Kim², Seung yeon Park², and Myung Suk Song² ¹Research and Development Institute, Magtron Co., Ltd., ²Korea Institute for Rare Metals, KITECH</p>



TP-245	<p>Oxidation Mechanism of HfSe₂ and Principle of Clean Van der Waals Gap between Native Oxide and MoS₂ (WSe₂)</p> <p>Joonho Park and Yong-Hoon Kim School of Electrical Engineering, KAIST</p>
TP-246	<p>Performance Enhancement of 650V E-mode GaN HEMTs through Recess Etched pGaN Gate Structures</p> <p>Min Su Cho, Junhyeok Lee, Nakwon Yu, Jihoun Jung, Wonchae Jeong, Heesub Lee, Jonghyun Lee, Sanggi Lee, and Woochul Jeon DB HiTek</p>
TP-247	<p>In-Situ Characterization of Floating-Body Effects via Low-Frequency Noise Spectroscopy</p> <p>Jaehong Min¹, Youngchan Cho², and Joon-Kyu Han³, and Wonjun Shin²</p> <p>¹Department of Advanced Materials Science and Engineering, Sungkyunkwan University, ²Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³Department of Material Science and Engineering, Seoul National University</p>
TP-248	<p>A New Optimization Method to Extract Off-State Parameters of HR PD-SOI MOSFETs</p> <p>Seunghun Yi¹ and Seonghearn Lee²</p> <p>¹Department of Electronics Engineering, Hankuk University of Foreign Studies, ²Division of Semiconductor and Electronics Engineering, Hankuk University of Foreign Studies</p>
TP-249	<p>Improvement of Heater Temperature Uniformity through Electric Power Supply Control</p> <p>Baek Ju Lee and Hyun Chul Wang Department of Intelligent Semiconductor, Pyeongtaek University</p>
TP-250	<p>Characterizing the Dielectric Properties of Two-Dimensional Electrified Interfaces based on the Electrostatic Embedding Method</p> <p>Ryong-Gyu Lee, Kaptan Rajput, and Yong-Hoon Kim School of Electrical Engineering, KAIST</p>



TP-251	<p>Surface Sulfur Passivation of InP for High Performance MoS₂/InP Heterojunction Optoelectronic Devices</p> <p>Dong Hwi Choi, Min Su Kim, Jae Hyeop Lee, Jong Hwan Park, and Jae Cheol Shin</p> <p>Department of Electronics and Electrical Engineering, Dongguk University</p>
TP-252	<p>Characterization Approach for Spatial and Energetic Analysis of Trap States in Oxide Semiconductor FETs</p> <p>도현서¹, 석규환², 정성웅^{1,2}</p> <p>¹Department of Semiconductor Engineering, POSTECH, ²Department of Electrical and Electronic Engineering, POSTECH</p>
TP-253	<p>전압 조건에 따른 α-Ga₂O₃ MSM X-ray 검출기의 과도 응답 및 오버슛 억제 매커니즘 분석</p> <p>차형주^{1,2}, 김선재², 황완식^{1,2}</p> <p>¹한국항공대학교 스마트항공모빌리티학과, ²한국항공대학교 신소재공학과</p>
P-254	<p>Density Functional-Based Recursive Matrix Green's Function Calculations of Large-Scale Devices</p> <p>Jeongwon Lee, Ryong-Gyu Lee, and Yong-Hoon Kim</p> <p>School of Electrical Engineering, KAIST</p>
TP-255	<p>Bandgap-Engineered 2D/III-V Heterojunction TFETs: Comparative Study of p-InP/n-MoS₂ and p-InGaAs/n-MoS₂ Structures</p> <p>Minsu Kim, Jaehyeop Lee, Jonghwan Park, Dong Hwi Choi, and Jae Cheol Shin</p> <p>Department of Electronics and Electrical Engineering, Dongguk University</p>
TP-256	<p>Analysis of Trap Dynamics in β-Ga₂O₃ SBDs Using a Pulsed Voltage Measurement Technique</p> <p>Thanh Huong Vo^{1,2}, Sunjae Kim^{1,3}, Ji-Hyeon Park³, Dae-Woo Jeon³, Wan Sik Hwang^{1,2}, and Jinyoung Hwang⁴</p> <p>¹Department of Materials Science and Engineering, Korea Aerospace University, ²Department of Smart Air Mobility, Korea Aerospace University, ³KICTEC, ⁴Department of Electrical and Electronic Engineering, Korea Aerospace University</p>



TP-257	<p>Machine Learning-Accelerated Single Event Effect (SEE) Modeling for Nanosheet FETs</p> <p>Joonhan Kim¹, Dongwook Kim¹, and Jongwook Jeon²</p> <p>¹Department of Display Engineering, Sungkyunkwan University, ²Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-258	<p>2D Material-Based Low Height Complementary Field-Effect Transistor</p> <p>Junyeol Lee¹, Hanggyo Jung², Hojin Kim², and Jongwook Jeon¹</p> <p>¹Department of Electrical and Computer Engineering, Sungkyunkwan University, ²Department of Semiconductor Convergence Engineering, Sungkyunkwan University</p>
TP-259	<p>Ge₂Sb₂Te₅ 기반 상변화 메모리와 GeTe 기반 상변화 메모리의 저항 변동성: 확률적 핵 생성을 통합한 다중 물리 시뮬레이션 연구</p> <p>김혜수, 김예찬, 권용우</p> <p>Department of Materials Science and Engineering, Hongik University</p>
TP-260	<p>Physics-Based Compact Modeling of Ferroelectric In-Ga-Zr-O Thin-Film Transistors</p> <p>Jae Woog Jung, Hyung Soo Kim, and Hyunwoo Kim</p> <p>Department of Electrical and Electronics Engineering, Konkuk University</p>
TP-261	<p>SiC 트렌치 MOSFET의 항복 전압 향상을 위한 플로팅 게이트 구조의 TCAD 시뮬레이션 최적화</p> <p>이동석, 김인화, 김기현</p> <p>전북대학교 정보전자공학부</p>
TP-262	<p>상장법을 활용한 폴리 실리콘 박막에서의 고체상 결정화 과정 3차원 시뮬레이션</p> <p>최다은, 이환욱, 권용우</p> <p>홍익대학교 신소재공학과</p>



TP-263	<p>Modeling of Channel and JFET Region Trap Occupancy Behavior under Short-Circuit Stress in 4H-SiC MOSFETs</p> <p>Hyeona Seo¹, Geon Kim¹, Dongyeong Kim¹, Suyeon Kim¹, Jewon Park¹, Shinwook Kim¹, Ryun Kang¹, Chaehyuk Lim¹, Jeonghyeon Yun¹, Sowon Kim¹, Hyerin Lee¹, Juwon Lee¹, Ujin Choi², Eojin Kim², Minwoo Jeong², and Myoungjin Lee²</p> <p>¹Department of Intelligent Electronics and Computer Engineering, Chonnam National University, ²Department of Electronics and Computer Engineering, Chonnam National University</p>
TP-264	<p>Power-Delay Pareto Front Exploration of Ring Oscillator at 1 nm Node Using ML-Based Model</p> <p>정현준, 공정택, 김소영</p> <p>성균관대학교 정보통신대학 전자전기컴퓨터공학과</p>
TP-265	<p>Experimental Demonstration of a CMOS-compatible Diode for 1D-1R RRAM Integration</p> <p>Seung Yeon Kim¹, Sol Ji Park¹, Min Sik Park¹, Won Chul Lee², and Jong Won Lee¹</p> <p>¹Chungnam National University, ²NNFC</p>
TP-266	<p>First-Principles Investigation of Oxygen Vacancy Effects on In_2O_3 (001)/Metal Contacts</p> <p>Seunghyun Yu and Yong-Hoon Kim</p> <p>School of Electrical Engineering, KAIST</p>
TP-267	<p>Comparative Analysis of Analog Performance Between High-Voltage FinFET and HKMG Transistors</p> <p>SoYun Kim, Jeong-Taek Kong, and SoYoung Kim</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-268	<p>Analysis of Hysteresis and PBS Degradation in In_2O_3 and ITO TFTs based on TCAD Simulation</p> <p>Ji-Won Son¹, Su-Min Kim², Kyu-Hwan Seok³, and Sung-Woong Chung^{1,2}</p> <p>¹포항공과대학교 반도체공학과, ²포항공과대학교 반도체대학원, ³포항공과대학교 전자전기공학과</p>



TP-269	<p>3D Simulation-Based Analysis of Geometrical Scaling Effects on GAA-FET Electrical Characteristics Sungsoo Park and Byoungdeog Choi Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-270	<p>차세대 FET 구조별 자가 발열 효과 비교 분석 신동현, 김소영, 공정택 성균관대학교 정보통신대학 전자전기컴퓨터공학과</p>
TP-271	<p>Charge Coupling Effects on the Electrical Characteristics of CMOS Neuron Hybridized With Memristor-Based Synapse Seo Yong Chi^{1,2}, Hi-Deok Lee², and Wan-Gyu Lee¹ ¹NNFC, ²Chungnam National University</p>
TP-272	<p>인쇄 TFT의 실측 비대칭 I-V 데이터 기반 ML Compact Model 개발 유정민¹, 공정택¹, 김소영² ¹성균관대학교 반도체융합공학과, ²성균관대학교 반도체시스템공학과</p>
TP-273	<p>ASM-HEMT 모델을 이용한 p-GaN AlGaN/GaN HEMT의 트랩 노드 기반 전압 의존 모델링 및 물리적 해석 이엘림, 김형탁 홍익대학교 전자전기공학부</p>
TP-274	<p>유전 알고리즘 및 딥러닝 기반 a-IGZO TFT 전류-전압 특성 모델링 김문기^{1,3,4}, 김윤^{2,3,4} ¹서울시립대학교 지능형반도체학과, ²서울시립대학교 전자전기컴퓨터공학부, ³서울시립대학교 반도체 연구센터(UOS-FAB), ⁴주식회사 IM전자</p>
TP-275	<p>High-Efficiency Self-Curing Method for Radiation-Damaged SiC MOSFETs Using Joule Heating and Fowler-Nordheim Tunneling Sang Hyeok Yun¹, Kihyun Kim², Gyuin Jang², Beomjin Kim², Young Jun Yoon², Jong-Ho Bae³, and Jae Hwa Seo⁴ ¹Power SoC Research Center, KERI, ²School of Electronic & Mechanical Engineering, Gyeongkuk National University, ³Department of System Semiconductor Engineering, Yonsei University, ⁴Advanced Semiconductor Research Center, KERI</p>



제 33회 한국반도체학술대회

The 33rd Korean Conference on Semiconductors

2026년 1월 27일(화)-30일(금) | 강원도 하이원리조트 그랜드호텔(컨벤션타워)

A Paradigm Shift in Semiconductors for AI Era

TP-382

In-Ga-O 채널의 전도 활성화 거동의 물리적 해석 및 Self-Consistent 모델링

김희수¹, 김희태¹, 이호석¹, 조성행², 조병진¹

¹한국과학기술원 전기 및 전자공학부, ²한국전자통신연구원 플렉시블 전자소자연구실



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE1 (4층, 로비)

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TP-316	<p>Photospike-Driven True Random Number Generation for Secure Image Processing Juhhyung Seo, Seungme Kang, Taehyun Park, and Hocheon Yoo Department of Electric Engineering, Hanyang University</p>
TP-317	<p>Graphene-Based Physically Unclonable Functions Patterned by Randomly Distributed Microparticles Dong Hyun Lee, Youngmin Han, and Hocheon Yoo Department of Electronic Engineering, Hanyang University</p>
TP-318	<p>Zinc Tin Oxide Nanoparticle-Based Deep UV Photodetector Embedded Circuit for Wireless Flame Detection Junhyung Cho¹, Seungmi Kang², Taehyun Park², and Hocheon Yoo^{1,2} ¹Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²Department of Electronic Engineering, Hanyang University</p>
TP-319	<p>Wavelength-Dependent Negative Photoconductance Effect Enabling Neuromorphic-Photodetector Switching in Low-Power ZnON Transistors Integrated with PCB Systems Won Woo Lee¹, Wangmyung Choi², and Hocheon Yoo^{1,2} ¹Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²Department of Electronic Engineering, Hanyang University</p>
TP-320	<p>Dual Gate ZnON Transistor for Logic Gate Implementation in a Single Device Heebeen Shin¹, Youngmin Han², Minseo Kim¹, and Hocheon Yoo² ¹School of Semiconductor Engineering, Gachon University, ²School of Electronic Engineering, Hanyang University</p>



TP-321	<p>Light-to-Acoustic Entropy Generation in Nanocomposite PUFs</p> <p>Taehyun Park and Hocheon Yoo</p> <p>Department of Electric Engineering, Hanyang University</p>
TP-322	<p>Gaussian Function with Twist Contact Transistor</p> <p>Kyoungsoon Kim¹, Youngmin Han², Eousik Cho³, and Hocheon Yoo²</p> <p>¹Department of Semiconductor Engineering, Gachon University, ²Department of Electronic Engineering, Hanyang University, ³Department of Electronic Engineering, Gachon University</p>
TP-323	<p>Thickness-Dependent Switching between Optoelectronic Memory and Gaussian-Sigmoid Behavior in a Double-Floating-Gate Transistor</p> <p>Seungeun Jeong¹, Seungme Kang², Eousik Cho³, and Hocheon Yoo²</p> <p>¹Department of Semiconductor Engineering, Gachon University, ²Department of Electronic Engineering, Hanyang University, ³Department of Electronic Engineering, Gachon University</p>
TP-324	<p>Schottky-Gated Polyaniline/Reduced Graphene Oxide Channel for Conductivity Modulation on Flexible Substrates</p> <p>Hyeon-Wook Yu, Tae-Hee Kim, Jun-Yeong Jeon, Seung-Woo Lee, Ju-Seong Lee, and Hyun-Seok Kim</p> <p>Division of Electronics and Electrical Engineering, Dongguk University</p>
TP-325	<p>CMOS-Compatible Neuromorphic Photoreceptor with Monolithic Optical-to-Spike Conversion and In-Sensor Convolution</p> <p>Seungyeob Kim¹, Seungmin Cheon¹, Seonjae Park², Taeseung Jung¹, and Sanghun Jeon^{1,2}</p> <p>¹School of Electrical Engineering, KAIST, ²Graduate School of Semiconductor Technology, KAIST</p>
TP-326	<p>CMOS-Compatible Oxide TFT-Based 2T-2C Near-Pixel Analog Compute Cell with ADC/DAC-Free Sensor Interface for Energy-Efficient Feature Extraction</p> <p>Seonjae Park¹, Seungyeob Kim², Seungmin Cheon², Taeseung Jung², and Sanghun Jeon^{1,2}</p> <p>¹Graduate School of Semiconductor Technology, KAIST, ²School of Electrical Engineering, KAIST</p>



TP-327	<p>Bio-Inspired TTFS Coding Architecture Utilizing L-AFeFET and TS Devices for Sub-1 ms Neuromorphic Tactile Systems</p> <p>Taeseung Jung¹, Seonjae Park², Seungyeob Kim¹, and Sanghun Jeon¹</p> <p>¹School of Electrical Engineering, KAIST, ²Graduate School of Semiconductor Technology, KAIST</p>
TP-328	<p>Bioinspired Thermal Perception via IGZO Memristor-Pt Sensor Integration for Artificial Nociceptor Systems</p> <p>Jiseon Moon, Muhammad Naqi, and Sunkook Kim</p> <p>School of Advanced Materials Science and Engineering, Sungkyunkwan University</p>
TP-329	<p>Enhanced Response and Stability of a Low-Power Transparent SnO₂ Gas Sensor with Memristor Integration</p> <p>Taegi Kim and Hee-Dong Kim</p> <p>Department of Electrical Engineering and Convergence Engineering for Intelligent Drone, Sejong University</p>
TP-330	<p>Colorimetric E-Nose Platform Using Fluorescent Nanofiber and IGZO Phototransistor Arrays</p> <p>Siyun Lee and Sunkook Kim</p> <p>Sungkyunkwan University</p>
TP-331	<p>Flexible NO₂ Gas Sensor Using IGZO for Enhanced Mechanical and Electrical Stability</p> <p>Sanggyu Bang and Hee-dong Kim</p> <p>Department of Electrical Engineering and Convergence Engineering for Intelligent Drone, Sejong University</p>
TP-332	<p>Microheater-Centric Design Optimization of TFT-Type Gas Sensors</p> <p>Siwon Jang¹, Hunhee Shin¹, Jong-Ho Lee¹, and Gyuweon Jung^{1,2}</p> <p>¹Department of Electrical and Computer Engineering and Inter-university Semiconductor Research Center, Seoul National University, ²School of Transdisciplinary Innovations, Seoul National University</p>
TP-333	<p>Electrospun P(VDF-TrFE) Nanofibrous Membrane Based High Performing Energy Harvesting Devices</p> <p>Yongbin Han and Hong-Joon Yoon</p> <p>Department of Semiconductor Engineering, Gachon University</p>



TP-334	<p>IZO / CNT TFT를 활용한 상보성 인버터 기반 암모니아 가스센서 길가영¹, 임수빈¹, 백수진², 김봉준², 김민희¹ ¹한밭대학교 창의융합학과, ²숙명여자대학교 첨단소재 · 전자융합공학부 지능형전자시스 템 전공</p>
TP-335	<p>Hydrogen-Treated Pd–Graphene Interface for Enhanced Electrocatalytic Performance in Laser-Induced Graphene Electrodes Yunhoe Koo¹, Ji Min Kim¹, Jueun Park¹, Suok Lee², Young-Woo Lee², Hyoung Woo Yang³, and A-Rang Jang¹ ¹Division of Electrical, Electronic and Control Engineering, Kongju National University, ²Department of Energy Engineering, Soonchunhyang University, ³Firbl</p>
TP-336	<p>NO₂ Gas Sensing Characteristics of Carbon Nanotubes–Ag Nanoparticles Heterostructure-Based Memristor Gas Sensor Mohsin Ali^{1,2,3} and Hee-Dong Kim^{1,2,3} ¹Department of Semiconductor Systems Engineering, Sejong University, ²Department of Electrical Engineering, Sejong University, ³Convergence Engineering for Intelligent Drone, Sejong University</p>
TP-337	<p>Enhancing the Reservoir Characteristics of IGZO Optoelectronic Transistor via Oxygen Vacancy Modulation for In-Sensor RC System Dongjun Min¹, Hyunjun Kim¹, Narim Lee¹, Hyunjoo Hwang², Wonwoo Kho², and Seung-Eon Ahn^{1,2} ¹Department of Nano & Semiconductor Engineering, Tech University of Korea, ²Department of IT · Semiconductor Convergence Engineering, Tech University of Korea</p>
TP-338	<p>Textile-Based ZnO Nanorod In-Sensor Reservoir Computing for Word Recognition Dongjun Min¹, Hyunjun Kim¹, Narim Lee¹, Hyunjoo Hwang², Wonwoo Kho², and Seung-Eon Ahn^{1,2} ¹Department of Nano & Semiconductor Engineering, Tech University of Korea, ²Department of IT · Semiconductor Convergence Engineering, Tech University of Korea</p>



TP-339	<p>Optoelectronic Dynamics of ZnO Nanorods for Wearable In-Sensor Reservoir Computing Hyunjoo Hwang¹, Wonwoo Kho¹, Dongjun Min², Hyunjun Kim², Narim Lee², and Seung-Eon Ahn^{1,2} ¹Department of IT· Semiconductor Convergence Engineering, Tech University of Korea, ²Department of Nano & Semiconductor Engineering, Tech University of Korea</p>
TP-340	<p>Electrochemical Characteristics of Gold/Graphene Hybrid Neural Electrodes for Electrical Stimulation Youngsoo Kim¹, Hyungjun Choi², Dongjun Han³, Rayoung Park ², Sookyeong Kim², and Dong-Wook Park^{1,3} ¹Department of BioHealth and Eco-Up Convergence, University of Seoul, ²Department of Electrical and Computer Engineering Convergence, University of Seoul, ³School of Electrical and Computer Engineering, University of Seoul</p>
TP-341	<p>Interfacial Ion Dynamics in Fluorinated Electrolyte-Gated Transistors Yeonseo Kim¹, Serang Jung¹, Minho Jegal¹, and Youn Sang Kim^{1,2} ¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology, Seoul National University</p>
TP-342	<p>Flat-Top Response Coarse Wavelength-Division Demultiplexer for Mid-Infrared Photonic Applications Inki Kim, Soohyun Kim, and SangHyeon Kim 3D Integrated Opto-Electronic Device Laboratory, KAIST</p>
TP-343	<p>Performance Evaluation of CeO₂/ZnO Memristor-Based Gas Sensor for Carbon Monoxide Detection Ibtisam Ahmad¹, Myungbin Gwak², Yoon-Sik Cho², and Hee-Dong Kim¹ ¹Department of Semiconductor Systems Engineering and Convergence Engineering for Intelligent Drone, Sejong University, ²Department of Artificial Intelligence, Chung-Ang University</p>



TP-344	<p>Improved Resistive Switching Characteristics Observed in Amorphous Boron Nitride-Based RRAM Device via Oxygen Doping: A Study based on Bulk and Interface Traps Analysis</p> <p>Syed Ameer Hamza^{1,2,3} and Hee-Dong Kim^{1,2,3}</p> <p>¹Department of Semiconductor Systems Engineering, Sejong University, ²Department of Electrical Engineering, Sejong University, ³Convergence Engineering for Intelligent Drone, Sejong University</p>
TP-345	<p>Photo-Induced Dual-Mode Memory Transistor with Charge-Storage Bilayer Dielectric for Flexible Optoelectronic Neuromorphic Computing</p> <p>Sunwoo Jeong¹, Gyeongho Lee^{2,3}, Hyeonjung Kim⁴, Seyong Oh⁴, Hocheon Yoo⁵, and Junhwan Choi¹</p> <p>¹Department of Chemical Engineering, Dankook University, ²Semiconductor Total Solution Center, Korea Institute of Ceramic Engineering and Technology, ³Department of Materials Science and Engineering, Korea University, ⁴Division of Electrical Engineering, Hanyang University ERICA, ⁵Department of Electronic Engineering, Hanyang University</p>
TP-346	<p>Role of en-APTAS Membranes in Enhancing the NO₂ Gas-Sensing Characteristics of Carbon Nanotube/ZnO-Based Memristor Gas Sensors</p> <p>Umar Ijaz and Hee-Dong Kim</p> <p>Department of Semiconductor Systems Engineering and Convergence Engineering for Intelligent Drone, Sejong University</p>
TP-347	<p>Flexible CMUT Array for Conformal Wearable Ultrasound Imaging</p> <p>Keun Young Huh^{1,2}, Jin Soo Park¹, Joo Young Pyun¹, Soo Jin Kim², and Byung Chul Lee^{1,3,4}</p> <p>¹Bionics Research Center, KIST, ²Department of Electrical Engineering, Korea University, ³Division of Bio-Medical Science & Technology, KIST School, University of Science & Technology, ⁴KHU-KIST Department of Converging Science and Technology, Kyung Hee University</p>



TP-348	<p>Cyano-Functionalized Copolymeric Memristors Synthesized by iCVD for CNN-Based Compute-in-Memory Applications</p> <p>Minsu So¹, Ji In Kim¹, Taehoon Kim¹, Woo Jin Wang², Yong Goo Shin², Min Ju Kim^{1,3,4}, and Junhwan Choi^{1,5}</p> <p>¹Department of Foundry Engineering, Dankook University, ²Department of Electronics and Information, Korea University, ³Department of Semiconductor Convergence Engineering, Dankook University, ⁴Department of Electronics & Electrical Engineering, Dankook University, ⁵Department of Chemical Engineering, Dankook University</p>
TP-349	<p>Fabrication of Silicon Nano Rods via Single-Ring Metal-Assisted Chemical Etching for Electromechanical Applications</p> <p>Seonghun Cho^{1,2}, Seyeon Yun^{1,3}, Sung Hun Jang^{1,4}, Woo Jin Jung^{1,5}, Dong-Hyun Kang^{1,6}, and Byung Chul Lee^{1,7,8}</p> <p>¹Bionics Research Center, KIST, ²School of Electrical Engineering, KIST, ³Department of Energy Resources Engineering, Inha University, ⁴Department of Electrical and Electronic Engineering, Inha University, ⁵School of Electronic Engineering, Hanyang University, ⁶Department of Mechanical Engineering, Gangneung-Wonju National University, ⁷Division of Bio-Medical Science and Technology, KIST School, University of Science and Technology, ⁸KHU-KIST Department of Converging Science and Technology, Kyung Hee University</p>
TP-350	<p>A Circuit for Microbubble Harmonics in Blood-Brain Barrier (BBB) Opening</p> <p>Yu Been Oh¹, Ho Jun Kim¹, and Jae Myung Lim^{1,2}</p> <p>¹Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²Department of Electronic Engineering, Hanyang University</p>
TP-351	<p>CeO₂ 측매층이 코팅된 산화물 반도체 가스센서를 이용한 휘발성 방향족 탄화수소의 고감도·고선택성 검지</p> <p>윤성영, 서정후, 이상명, 정성용</p> <p>국립공주대학교 신소재공학과</p>
TP-352	<p>다공성 PdO-SnO₂ 산화물 반도체를 활용한 초고속·고감도 H₂ 가스센서</p> <p>서정후, 윤성영, 이상명, 정성용</p> <p>국립공주대학교 신소재공학과</p>



TP-353	<p>Machine Learning Enabled Environmental Adaptation in TEMPO-Functionalized SWCNTs-Based H₂S Sensor</p> <p>Da-Gyo Yoo¹, Ryang Ha Kim¹, Minhyeok Yang², Young Lae Kim², and Myung-Hyun Baek²</p> <p>¹Department of Electronic Engineering, Gangneung-Wonju National University, ²Department of Electronic and Semiconductor Engineering, Gangneung-Wonju National University</p>
TP-354	<p>Hierarchical Triangular MoS₂ for Edge-Engineered Hydrogen Sensing</p> <p>Minsu Lee, Johaug Seo, and Juwon Lee</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-355	<p>Environmental Monitoring Using a Highly Sensitive and Stable Nitrogen Dioxide Gas Sensor</p> <p>Bum Hwan Kim, Jae Eun Kim, Hyung Soo Ahn, Won Bae Cho, and Young Teah Chun</p> <p>Division of Electronics and Electrical Information Engineering, Korea Maritime & Ocean University</p>
TP-356	<p>Programmable Thermo-Responsive Biodegradable Composites for On-Demand Deactivation of Electronic Systems</p> <p>Hyek Kang, Chan-Hwi Eom, and Suk-Won Hwang</p> <p>Korea University</p>
TP-357	<p>Microfluidic 3D Hydrodynamic Focusing Technologies for Advanced Microflow Platform</p> <p>Gyeonghyeon Lee, Noah Jang, Hyunjun Kim, Da Ye Kim, Jinkyung Kim, Jin Park, Kihyun Kim, Dongchan Jung, and Seong Ho Kong</p> <p>School of Semiconductor Convergence Engineering, Kyungpook National University</p>
TP-358	<p>Physical Anchoring Enables Buckling-Guided Assembly of Freestanding 3D Mesostructures</p> <p>Yeonhee Heo¹, Gooyoon Chung¹, Pei Liu², Raudel Avila², and Yoonseok Park¹</p> <p>¹Department of Materials Science and Engineering, Kyung Hee University, ²Department of Mechanical Engineering, Rice University</p>



TP-359	<p>Magnetically Driven Triboelectric Nanogenerator for a Wireless, Versatile Energy Transfer System</p> <p>Junyeop Kim¹, Hong-Joon Yoon², and Yoonseok Park¹</p> <p>¹Department of Materials Science Engineering, Kyung Hee University, ²Department of Semiconductor Engineering, Gachon University</p>
TP-360	<p>Magnet-Based Wearable Sensing System for Real-time Monitoring of Physiological Organ Motion</p> <p>Jihoo Kim and Yoonseok Park</p> <p>Department of Materials Science and Engineering, Kyung Hee University</p>
TP-361	<p>Highly Stretchable and Biodegradable Materials for Soft, Temporary Electronics</p> <p>Hyewon Cho, Won-Bae Han, Gwan-Jin Ko, and Suk-Won Hwang</p> <p>Korea University</p>
TP-362	<p>Multifunctional, Bioresorbable and Elastomeric Polymers for Eco-friendly and Biomedical Electronics</p> <p>Jeong-Uk Kim¹, Jeong-Woong Shin¹, Dong-Je Kim¹, Tae-Min Jang¹, and Suk-Won Hwang^{1,2,3}</p> <p>¹KU-KIST Graduate School of Converging Science and Technology, Korea University, ²Center for Biomaterials, Biomedical Research Institute, KIST, ³Department of Integrative Energy Engineering, Korea University</p>
TP-363	<p>Improved H₂S Detection Through Ligand Modification of Carbon Nanotube Networks</p> <p>Ryang Ha Kim², Kun Choi¹, Kyu Won Lee³, Eunji Lee³, and Young Lae Kim¹</p> <p>¹Department of Electronic and Semiconductor Engineering, Gangneung-Wonju National University, ²Department of Electronic Engineering, Gangneung-Wonju National University, ³Department of Chemistry, Gangneung-Wonju National University</p>
TP-364	<p>Germanium-on-Insulator 중적외선 On-Chip 가스 센서용 광대역 편광 회전 기</p> <p>김수현¹, 김인기², 김상현^{1,2}</p> <p>¹KAIST 반도체공학대학원, ²KAIST 전기및전자공학부</p>



TP-365	<p>Multi-Functional, Fully Biodegradable Bioelectronic System Featuring Shape-Programmable and Bioresorbable Elastomer</p> <p>Li Hyun Kim, Jun Hyeon Lim, Won Bae Han, and Suk-Won Hwang</p> <p>KU-KIST Graduate School of Converging Science and Technology, Korea University</p>
TP-366	<p>Self-Clocking Perovskite TRNG with Polymer-Blended Stochastic Photoresponse for Secure Encryption</p> <p>Minz Lee^{1,2}, Hyun Seok Song¹, and Yusin Pak³</p> <p>¹Nanophotonic Systems Center, KIST, ²Department of Materials Science and Engineering, Korea University, ³Division of Advanced Materials Engineering, Jeonbuk National University</p>
TP-367	<p>Machine Learning-Based Tracking of Cardiac Motion Using Implantable Magnetic Patches</p> <p>Sunjin Lee¹, Eojin Lee¹, Youn-kyoung Baek², Sumin Kim³, Mikyung Shin⁴, and Yoonseok Park¹</p> <p>¹Department of Materials Science and Engineering, Kyung Hee University, ²KIMS, ³Department of Intelligent Precision Healthcare Convergence, Sungkyunkwan University, ⁴Department of Biomedical Engineering, Sungkyunkwan University</p>
TP-368	<p>ZnO/Al₂O₃/IGZO TFT for Room-Temperature Ammonia Sensing</p> <p>Dong Chan Jung¹, Seong Ho Kong¹, Hyunjun kim², Da Ye Kim², Noah Jang², Uihoon Jung², Gyeongyeon Lee², Kihyun Kim², Jinkyung Kim², and Jin Park²</p> <p>¹School of Semiconductor Convergence Engineering, Kyungpook National University, ²School of Electronic and Electric, Kyungpook National University</p>



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE2-1 (5층, 로비)

[TP] 포스터세션

D. Thin Film Process Technology 분과

TP-001	<p>Hot Wire Assisted Area Selective Deposition of Ruthenium Kyeongmin Min and Han-Bo-Ram Lee Department of Materials Science and Engineering, Incheon National University</p>
TP-002	<p>Single-Device Integration of PUFs and TRNGs via Stochastic ZGO Nanoclusters for Secure and Generative Applications Seungme Kang, Wangmyung Choi, and Hocheon Yoo Department of Electronic Engineering, Hanyang University</p>
TP-003	<p>The Effect of Post-Al₂O₃ Passivation Annealing on the Electrical Characteristics of ALD-IGZO Thin-Film Transistors Sahngik Aaron Mun^{1,2}, Donghee Han^{1,2}, Minhyuk Park^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-004	<p>Oxalic Acid-Based Wet Etching of Amorphous Zinc-Tin-Oxide Thin Films Deposited by Atomic Layer Deposition Jaewon Ham^{1,2}, Seo Young Jang^{1,2,3}, Yonghee Lee^{1,2}, Sukin Kang^{1,2}, Sahngik Aaron Mun^{1,2}, Jinheon Choi^{1,2}, Juneseong Choi^{1,2}, Seoryong Park^{1,2}, Hyungjeung Kim^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³SK Hynix Inc.</p>



TP-005	<p>Al-Doped ZnSnO Amorphous Oxide Semiconductor for Highly Reliable Thin-Film Transistors and Capacitorless DRAM Applications</p> <p>Jinheon Choi^{1,2}, Taeyoung Jeong³, Shihyun Kim^{1,2}, Néstor Ghenzi^{1,2}, Byongwoo Park^{1,2}, Gwang sik Jeon^{1,2}, Juneseong Choi^{1,2}, Hyungjeung Kim^{1,2}, Jung-Hae Choi³, Yonghee Lee^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³Electronic and Hybrid Materials Research Center, KIST</p>
TP-006	<p>Ferroelectric Thin-Film Transistors with Indium Gallium Zinc Oxide Channels: Effects of Stack Thickness Scaling</p> <p>Won Kim^{1,2}, Sung Eun Kim^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-007	<p>Minimizing on-Current Drop in HfO₂-Based Ferroelectric Tunnel Junctions through a-IGZO Interlayer Engineering</p> <p>Sanghyup Lee^{1,2} and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-008	<p>Mechanism Study of The Ferroelectric Behavior in Ferroelectric/Field-Induced Ferroelectric Bilayer Doped-HfO₂ Capacitors</p> <p>Joon Young Kang^{1,2}, Han Sol Park^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-009	<p>Ferroelectric Property of Wurtzite AlScN/AlN/AlScN Multilayers</p> <p>Dong Jae Kim^{1,2} and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-010	<p>Enhancing Ferroelectric Properties of Zr-Doped HfO₂ Thin Films Using the Bi-Layer Bottom Electrode Structures</p> <p>Han Sol Park^{1,2}, Joong Chan Shin^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>



TP-011	<p>Memory Window Improvement in Ferroelectric Thin-Film Transistors with Fluorite/Wurtzite Bilayers Grown by Atomic Layer Deposition</p> <p>Hyun Woo Nam^{1,2}, Dong Jae Kim^{1,2}, Kyung Do Kim^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-012	<p>Improved Interfacial Properties of TiO₂ Thin Films on Ru Electrodes through Stepwise Optimization of Ozone-Assisted ALD</p> <p>Haewon Song^{1,2}, Tae Kyun Kim^{1,2}, Heewon Paik^{1,2}, Jonghoon Shin^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-013	<p>Effect of GeO₂ Interfacial Layer on Low-Temperature Crystallization of SrTiO₃ Thin Film Grown via Atomic Layer Deposition</p> <p>Heewon Paik^{1,2}, Tae Kyun Kim^{1,2}, Jonghoon Shin^{1,2}, Haewon Song^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-014	<p>BaTiO₃ Thin Films Grown by Atomic Layer Deposition Using a Seed-Layer Process</p> <p>Chansoo Kwak^{1,2}, Heewon Paik^{1,2}, Sunoo Kim³, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³Next-Generation Semiconductor Convergence and Open Sharing System, Seoul National University</p>
TP-015	<p>Surmounting the Thermionic Restriction via All-ALD-Fabricated Metal-Oxide Negative Capacitance Field-Effect Transistor</p> <p>Chan Lee¹, Minho Jegal¹, and Youn Sang Kim^{1,2}</p> <p>¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology</p>



TP-016	<p>ZnO 채널의 결정 구조에 따른 박막 트랜지스터의 종이온 내방사선 특성 향상 연구 이용수¹, 이해원², 김수진¹, 박정민¹, 이병훈², 강창구¹ ¹한국원자력연구원 첨단방사선연구소, ²포항공과대학교 전자전기공학과 반도체기술융합센터</p>
TP-017	<p>Ruthenium Metallic Compounds for Advanced Interconnect Materials Gyungho Maeng¹, Subeen Lim¹, Bonggeun Shong², and Yeonghun Lee¹ ¹Department of Electronics Engineering, Incheon National University, ²Department of Chemical Engineering, Hongik University</p>
TP-018	<p>Linear Memristor Synapse with Mechanism Elucidation for Online Training in Spiking Neural Networks Yu Lin Zou^{1,2}, Sunwoo Cheong^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-019	<p>Sub-Oxide-Free and Amorphous HfO₂ Films Deposited by ALD Using Liquid Precursor & H₂O/O₃ Hybrid Reactants for HKMG Applications Juyong Lee, Jaejin Han, Hanbee Lee, Wooyoung Park, and Hunhyeong Lim Orbital Development Team, WONIK IPS Co., Ltd.</p>
TP-020	<p>A Study on Ti Thin Film Deposition by the Cyclic Pulse Chemical Vapor Deposition Method to Improve the TiSi_x Resistance of Via Contact Ildong Cho, Min Kyu Bang, and Jinho Jeon Department of Semiconductor and Display, College of Semiconductor, Gachon University</p>
TP-021	<p>Selenium-Assisted Growth of NbSe₂/Bilayer Nb-Doped WSe₂ Heterostructures with Enhanced Performance Seungmin Baek and Woo Jong Yu Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-022	<p>Dynamics of Polarization Switching and Loss in Antiferroelectric/Dielectric Tunnel-Switch Bilayer Seunghyun Choi^{1,2}, Han Sol Park^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>



TP-023	<p>Artificial Retina based on 2D Schottky Solar Cells for Bioinspired Visual Signal Processing Jinwoo Hong and Woo Jong Yu Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-024	<p>Ag-Ion Intercalation-Enhanced h-BN/MoTe₂ Nanochannel Memristors for Neuromorphic Applications Yun Jae Baek and Woo Jong Yu Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-025	<p>Energy-Efficient Neuromorphic Learning Using Gold-Nanoparticle Floating-Gate Memristor Array Sa Been Hong and Woo Jong Yu Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-026	<p>극저온 광개시성 CVD 기반 불소계 고분자 합성 기초기술 및 반도체 응용 가능성 연구 박태원¹, 김형준¹, 한상혁¹, 김건우², 송상호³, 김민주^{1,2} ¹단국대학교 공과대학 전자전기공학과, ²단국대학교 공과대학 융합반도체공학과, ³단국대학교 공과대학 고분자공학과</p>
TP-027	<p>Enhanced Interface Quality of ZrO₂/MoN-Based Capacitors with an Ultra-Thin Nb2O5 Buffer Layer for DRAM Applications Yeon-Ji Jeon and Ji-Hoon Ahn Department of Materials Science & Chemical Engineering, Hanyang University</p>
TP-028	<p>Memory Window Expansion and Synaptic Behavior Enhancement in IGZO MFMIS FeTFTs by HfAlOx Interlayer Engineering Hyeong Seok Choi, Dong Hee Han, Hyun Woo Jeong, Jae Joon Kim, Dong In Han, Joonyong Kim, Suyong Lee, and Min Hyuk Park Department of Material Science and Engineering, Seoul National University</p>



TP-029	<p>BEOL-Compatible Ferroelectric Activation by Wake-Up in As-Deposited Hf_xZr_xO₂/MoO_x Films</p> <p>Moonseok Jeong, Hyun Woo Jeong, Taegyu Kwon, Jaewook Lee, Geun Hyeong Park, Dong Hee Han, Ju Yong Park, Dong In Han, Yong Hyeon Cho, Jeongwang Lee, and Min Hyuk Park</p> <p>Department of Material Science and Engineering, Seoul National University</p>
TP-030	<p>MoS₂ 결합 패시베이션을 위한 초박형 공중합 필름 개발</p> <p>유지호¹, 원종서¹, 이재원¹, 한상혁², 박태원², 김형준², 김건우², 흥웅기^{1,2,3}, 김민주^{1,2,3}</p> <p>¹단국대학교 공과대학 파운드리공학과, ²단국대학교 공과대학 전자전기공학부, ³단국대학교 공과대학 융합반도체공학과</p>
TP-031	<p>Room Temperature Ferromagnetism of Magnetically Intercalated 2D Transition Metal Dichalcogenides</p> <p>Joonmin Woo and Woojong Yu</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-032	<p>불화갈륨 전구체를 사용하여 불소화한 용액 공정 기반 IGZO 박막 트랜지스터의 전기적 특성</p> <p>박설아¹, 정태희², 문국철², 김용상^{1,2}</p> <p>¹성균관대학교 첨단디스플레이공학과, ²성균관대학교 전자전기컴퓨터공학과</p>
TP-033	<p>IGZO:Y TFT의 Yttrium 도핑 농도에 따른 히스테리시스 특성</p> <p>채수현¹, 문국철², 김용상^{1,2}</p> <p>¹성균관대학교 첨단디스플레이공학과, ²성균관대학교 전자전기컴퓨터공학과</p>
TP-034	<p>용매에 UV 처리를 이용한 용액 공정 기반 IGZO 박막 내 산소 공급 조절을 통한 신뢰성 개선</p> <p>소인영¹, 정기용^{2,3}, 문국철⁴, 김용상^{1,2,4}</p> <p>¹성균관대학교 첨단디스플레이공학과, ²성균관대학교 반도체디스플레이공학부, ³삼성전자 공과대학교, ⁴성균관대학교 전자전기컴퓨터공학과</p>
TP-035	<p>Interface Engineering of MFIS Capacitor with Plasma-Enhanced ALD Hafnium Nitride for FeFET Applications</p> <p>Ga Yeon Lee, Yong Hyeon Cho, Geun Hyeong Park, Moonseok Jeong, Ju Yong Park, Dong Hee Han, Da Hyun Kim, Taegyu Kwon, and Min Hyuk Park</p> <p>Department of Material Science and Engineering, Seoul National University</p>



TP-036	<p>Oxygen Vacancy Control via Ar Plasma Treatment for Remnant Polarization Enhancement of (Hf, Zr)O₂ Antiferroelectric Capacitors</p> <p>Heejin Hong, Hyun Woo Jeong, Jeonggwang Lee, Jaewook Lee, Ju Yong Park, Hyojun Choi, Dong In Han, Kwan Hyun Park, and Min Hyuk Park</p> <p>Department of Material Science and Engineering, Seoul National University</p>
TP-037	<p>고투과 및 고내열성 ZnO/Cu/SiO₂ 기반 다층 박막 투명히터의 제작 및 특성 분석</p> <p>강건우, 조재정, 이찬경, 최두호</p> <p>가천대학교 반도체공학과</p>
TP-038	<p>Contact Engineering in In₂O₃ Thin Film Transistors Guided by Contact-Dominated Low Frequency Measurement</p> <p>Minsun Kim^{1,2}, Donghee Kim^{1,2}, Seunghwan Kim^{1,2}, Jong-Ho Lee^{1,2}, and Gyuweon Jung^{1,2,3}</p> <p>¹Department of Electrical and Computer Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³School of Transdisciplinary Innovations, Seoul National University</p>
TP-039	<p>유전율 구배형 게이트 절연막을 통한 SiC MOSFET 신뢰성 향상</p> <p>이승민, 송현교</p> <p>반암 주식회사</p>
TP-040	<p>High-Reliable 1T-1C Cell with Mold Structures Fabricated by Channel-Last Processing of Amorphous Oxide Semiconductor Thin-Film Transistors</p> <p>Subin Moon^{1,2}, Jinheon Choi^{1,2}, Sahngik Aaron Mun^{1,2}, Juneseong Choi^{1,2}, Jaewon Ham^{1,2}, Hyungjeung Kim^{1,2}, Shihyun Kim^{1,2}, Jeongyoon Lee^{1,2}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-041	<p>Impact of HfO₂ Back-Channel Resistive Switching Layer in Scaled IGZO Thin Film Transistors for Parallel 1T-1R Integration</p> <p>Hyun Young Kim^{1,2} and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>



TP-042	<p>Controlled Deposition of HfO_2 Thin Films via Combined Remote and Direct Plasma ALD for Charge-Trap Applications</p> <p>Byung Wook Kim, Ji Won Kim, In Kook Hwang, Young Woon Jang, Hyeon Wu Nam, Min Kyun Kang, and Chang Bun Yoon</p> <p>Department of IT-Semiconductor Convergence Engineering, Tech University of Korea</p>
TP-043	<p>Variations in the Ferroelectric Properties of $\text{Hf}_{0.5}\text{Zr}_{0.5}\text{O}_2$ Depending on the Hafnium Nitride Electrode Conditions</p> <p>Seong Jae Shin^{1,2}, Min Jong Kim^{1,2,3}, and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University, ³R&D Center, SK Hynix Semiconductor</p>
TP-044	<p>Wake-Up-Free Ferroelectricity in Ultrathin (5 nm) $\text{Hf}_{0.5}\text{Zr}_{0.5}\text{O}_2$ With Molybdenum Bottom Electrode</p> <p>Seong Jae Shin^{1,2} and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-045	<p>Memory Window Enhancement with Channel Length Scaling of IGZO Channel-Based Al-Doped HfO_2 Ferroelectric Thin Film Transistors</p> <p>Ho Jin Kim^{1,2} and Cheol Seong Hwang^{1,2}</p> <p>¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-046	<p>Atomic Layer Deposition of MoO_3 Using Noble Molybdenum Precursor for High-k Rutile TiO_2 in DRAM Applications</p> <p>Yoonchul Shin, Yeon-Ji Jeon, Ji Hwan Kim, Chan-Bin Hong, Chang Mo Yoon, and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>
TP-047	<p>Impact of Channel Thickness and Composition on the Performance of Indium Tin Oxide Thin Film Transistors</p> <p>Jae Joon Kim, Hyeong Seok Choi, Dong Hee Han, Hyun Woo Jeong, Joonyong Kim, Suyong Lee, and Min Hyuk Park</p> <p>Department of Material Science and Engineering, Seoul National University</p>



TP-048	<p>Demonstration of Low Temperature Processed P-Type Tellurium Thin Film Transistors for Next-Generation CMOS Application</p> <p>Suyong Lee, Dong Hee Han, Hyun Woo Jeong, Hyeong Seok Choi, Jaejoon Kim, Joonyong Kim, and Min Hyuk Park</p> <p>Department of Material Science and Engineering, Seoul National University</p>
TP-049	<p>Atomic Layer Deposition of MoC_x as a Next-Generation Electrode Using a Liquid Precursor</p> <p>Eun-Su Chung¹, Min-Ji Ha¹, Na-Gyeong Kang¹, Jin-Sik Kim², Myeong-Ho Kim², Yun-Gyeong Yi², and Ji-Hoon Ahn¹</p> <p>¹Hanyang University, ²UP Chemical</p>
TP-050	<p>Low-Temperature Heterogeneous Integration of Complementary Thin-Film Transistors for Advanced Micro-LED Backplanes</p> <p>Junho Lee, Joo-On Oh, Yuseong Lee, and Sunkook Kim</p> <p>Sungkyunkwan University</p>
TP-051	<p>Low-Temperature Plasma Treatment for High-Performance p-Type Tellurium Field-Effect Transistors</p> <p>Minhyuk Jeon¹, Uisik Jeong², and Sunkook Kim¹</p> <p>¹School of Advanced Materials and Science Engineering, Sungkyunkwan University, ²DRAM Process Architecture Team, Samsung Electronics Co., Ltd.</p>
TP-052	<p>Mechanistic Transformation Pathway to Continuous and Impurity-Free Tellurium Films</p> <p>Seung Ho Ryu^{1,2}, Seungsu Kim^{1,2}, Jihoon Jeon^{1,2}, Gwang Min Park^{1,2}, and Seong Keun Kim^{1,2}</p> <p>¹Electronic and Hybrid Materials Research Center, KIST, ²KU-KIST Graduate School of Converging Science and Technology, Korea University</p>
TP-053	<p>Top-Gate Stack Seed Layer Engineering for Stable Operation of MoS₂ Transistors</p> <p>Dahyeon Kim and Myungsoo Kim</p> <p>Department of Electrical Engineering, UNIST</p>



TP-054	<p>Process Integration of Al:IZTO Oxide Semiconductor Channel into NanoSheet Gate–All–Around Architecture</p> <p>Bada Kim^{1,2}, Chihun Sung², Chaerin Yu^{1,2}, Jeho Na², Junghwan Kim¹, and Sung Haeng Cho²</p> <p>¹UNIST, ²ETRI</p>
TP-055	<p>Theoretical Analysis on the Effect of HBr in TiN ALD Process</p> <p>Seungmin Noh¹, Jae Min Jang¹, Kyeong Hyeon Choi², Se Eun Kim², Sang Woon Lee², and Bonggeun Shong¹</p> <p>¹Hongik University, ²Ajou University</p>
TP-056	<p>Oxidation Behavior of Pt–Ru Bimetallic Thin Films by Atomic Layer Deposition</p> <p>Young Wan Kim¹, Jaehyeong Kim¹, Yu Ri Han¹, and Woo Jae Lee²</p> <p>¹Department of Nanotechnology Engineering, Pukyong National University, ²Division of Nanotechnology and Semiconductor Engineering, Pukyong National University</p>
TP-057	<p>Theoretical Analysis on the ALD Surface Chemistry of Elemental Antimony</p> <p>Seoyun Heo and Bonggeun Shong</p> <p>Hongik University</p>
TP-058	<p>Computational Study on ALD Surface Chemistry of III–V Compounds</p> <p>Ho Hyeon Kim and Bonggeun Shong</p> <p>Hongik University</p>
TP-059	<p>Computational Study on Surface Chemistry during Atomic Layer Deposition of GeSbTe</p> <p>Il Hyeok Seo and Bonggeun Shong</p> <p>Hongik University</p>
TP-060	<p>P-Type Doping in MoS₂ Field–Effect Transistors via Radical Encapsulation</p> <p>Jaewon Lee, Mingyu Shin, Jiho Yu, Junyeon Hwang, Jungyoon Hur, Jongseo Won, Minju Kim, and Woonggi Hong</p> <p>Department of Foundry Engineering, Dankook University</p>



TP-061	<p>Atomic Layer Growth of Pt films Using Dimethyl(N,N-Dimethyl-3-Butene-1-Amine-N)Platinum and O₃</p> <p>Minseok Kim^{1,2}, Taeseok Kim^{1,2}, Jihoon Jeon^{1,2}, Gwang Min Park^{1,2}, Sung-Chul Kim³, Sung Ok Won³, Ryosuke Harada⁴, and Seong Keun Kim^{1,2}</p> <p>¹KU-KIST Graduate School of Converging Science and Technology, Korea University, ²Electronic and Hybrid Materials Resea</p>
TP-062	<p>Vertically Integrated Floating-Gate Memristor Enabling Compact and Energy-Efficient Two-Terminal Neuromorphic Architectures</p> <p>So Hyeon Park and Woo Jong Yu</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-063	<p>Investigation of Dielectric Properties and Interface on the 2D Channel Thin-Film Transistor Performance</p> <p>Soeun Ahn^{1,2}, Seongmo Kang³, Minchul Kim³, Yonghun Kim³, Seokwoo Jeon², and Seung-heon Chris Baek¹</p> <p>¹Center for Semiconductor Technology, KIST, ²Department of Materials Science and Engineering, Korea University, ³Department of Energy and Environment Materials, KIMS</p>
TP-064	<p>Designing Cryo-Compatible Ferroelectric Hafnia Devices through Electrode and Interface Control</p> <p>Chaehyeong Ha¹, Jeongmin Han², Jeonghwan Han², Taehwan Moon³, and Yoon Jang Chung¹</p> <p>¹Department of Chemical and Biological Engineering, Korea University, ²Department of Materials Science and Engineering, Seoul National University of Science & Technology, ³Department of Intelligence Semiconductor Engineering, Ajou University</p>
TP-065	<p>A HZO/ZnO-Based Ferroelectric Synaptic Transistor for Nonvolatile Neuromorphic Systems</p> <p>Yoonseon An¹ and Woo Jong Yu²</p> <p>¹Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ²Department of Electrical and Computer Engineering, Sungkyunkwan University</p>



TP-066	<p>Non-Volatile Memory Characteristics and Artificial Synaptic Behaviors in Two-Dimensional Materials via Defect Engineering</p> <p>Huiyeong Lee^{1,2}, Hyeonmin Bong^{1,2}, Jinsik Choe¹, Shinyoung Moon^{1,2}, M. -H. Cho², and Sungjin Park¹</p> <p>¹Icheon Branch, KICET, ²Department of Physics, Yonsei University</p>
TP-067	<p>Low-Resistivity TiN Thin Film Grown by Hydrazine-Enhanced Thermal ALD at Temperature Below 430°C</p> <p>Kihye Kim, Ilhyeong Cho, Yooseong Kim, and Dongwon Seo</p> <p>Hanwha Semitech, Process Development Team</p>
TP-068	<p>Ferroelectric HfZrO₂ and MoTe₂ Heterostructures for Synaptic Memristor Applications</p> <p>Yong Ha Shin and Woo Jong Yu</p> <p>Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-069	<p>Positional Doping in Al:HfO₂ Enables Strain-Engineered Texture and 1.2 V Operating Ferroelectricity at 7 nm</p> <p>Dong In Han¹, Jaewook Lee¹, Hyojun Choi¹, Ju Yong Park¹, Hyun Woo Jeong¹, Younghwan Lee², and Min Hyuk Park¹</p> <p>¹Department of Material Science and Engineering, Seoul National University, ²School of Materials Science and Engineering, Chonnam National University</p>
TP-070	<p>Ru Area-Selective Deposition by Aldehyde Inhibitor on SiO₂/Si₃N₄ Patterns</p> <p>이병찬¹, Chi Thang Nguyen¹, Ngoc Le Trinh¹, 민경민¹, 조은형², 이한보람¹</p> <p>¹Department of Materials Science and Engineering, Incheon National University, ²2D Device TU, Samsung Advanced Institute of Technology</p>
TP-071	<p>Improved Ferroelectricity and Tunneling Electroresistance in Metal-Ferroelectric-Insulator-Ferroelectric-Metal Structure Ferroelectric Tunnel Junctions via Nb₂O₅ Interlayer Insertion</p> <p>Hyungseok Lee, Hyo-Bae Kim, Gunho Kim, Hye-Won Cho, and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>



TP-072	<p>Surface Oxidation-Dependent Growth Mechanism of ZnO Thin Films in Ozone-Diethylzinc ALD Junhee Han and Yoon Jang Chung Department of Chemical and Biological Engineering, Korea University</p>
TP-073	<p>Area-Selective Atomic Layer Deposition of Ge-Sb-Te for Vertical-Recessed Phase-Change Memory Sanghyeon Kim, Heejae Kim, Myung Jun Yu, Chaeyeon Jeong, Kyungwhan Koh, and Chanyoung Yoo Department of Materials Science and Engineering, Hongik University</p>
TP-074	<p>Atomic Layer Deposition of Ge-rich GeSe for Selector-Only Memory Sungjin Kim^{1,2}, Jeong Woo Jeon^{1,2}, Wonho Choi^{1,2}, Byongwoo Park^{1,2}, Gwangsik Jeon^{1,2}, Sangmin Jeon^{1,2}, Junwoo Park^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-075	<p>ALD-Grown VO₂ Thin Films for Selector-Only Memory Syeon Song and Hongsub Lee Department of Materials Science and Engineering, Kyung Hee University</p>
TP-076	<p>Atomic Layer Deposition and Doping Strategies for High-Performance Arsenic-Free Ovonic Threshold Switch Devices Byongwoo Park^{1,2}, Jeong Woo Jeon^{1,2}, Wonho Choi^{1,2}, Gwangsik Jeon^{1,2}, Sangmin Jeon^{1,2}, Sungjin Kim^{1,2}, Junwoo Park^{1,2}, and Cheol Seong Hwang^{1,2} ¹Department of Materials Science and Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-077	<p>ZnO Thin Films with Stable, Tunable Properties Deposited by Atomic Layer Deposition Using Et₂Zn:NEtMe₂ Precursor Dokyun Kim¹, Ye Won Shin¹, and Woo Jae Lee² ¹Department of Nanotechnology Engineering, Pukyong National University, ²Division of Nanotechnology and Semiconductor Engineering, Pukyong National University</p>



TP-078	<p>새로운 플라즈마 소스를 활용한 실리콘 질화막 증착 연구 진광선, 신기원, 김유덕, 조승희, 왕야즈, 이태완, 노원태, 김훈 원익아이피에스 반도체연구소 선행기술개발본부 소재개발팀</p>
TP-079	<p>High Step Coverage Barrier/Seed Cu Metallization for AR >10:1 TSV Using Twin Magnet Cathode Sputtering Kazuki Hattori¹, Hyo Jun Bae², Tae Jung Kwon², and Byeong Hwa Jeong² ¹ULVAC Japan Co. Ltd, ²ULVAC Korea Co. Ltd</p>
TP-080	<p>Dynamic Thermal Control via Intensive Pulsed Light in ALD: Interfacial Layer Suppression & Crystallization of ZrO₂/TiN Stacked Capacitors Jongseo Kim^{1,2}, Jihoon Jeon^{1,2}, Seungwan Ye^{1,2}, and Seong Keun Kim^{1,2} ¹KU-KIST Graduate School of Converging Science and Technology, Korea University, ²Electronic and Hybrid Materials Research Center, KIST</p>
TP-081	<p>Area-Selective Atomic Layer Deposition of ZnO Using Acetylacetone as a Small Molecule Inhibitor Ye lim Lee and Yoon Jang Chung Department of Chemical and Biological Engineering, Korea University</p>
TP-082	<p>Influence of Gallium Cycling on the Electrical Performance of In-Ga-O Thin-film Transistors Fabricated by Thermal Atomic Layer Deposition Kang Choi, Dong-Geun Kim, Su-Jin Sim, and Ji-Hoon Ahn Department of Materials Science and Chemical Engineering, Hanyang University</p>
TP-083	<p>Characterization of Ultra Low-k Amorphous Boron Nitride Deposited by Using PEALD Chang Dong Lee, Dong Hak Kim, Ji Hyeon Kim, Gang Ill Seo, So Young Park, So Young Park, and Young Chul Choi WONIK IPS Co., Ltd.</p>
TP-084	<p>DC Magnetron Sputtered Ta/TaN Barriers and Seed-Cu Integration for Enhanced Metallization in BEOL Interconnects Hyo Jun Bae, In Sik Joo, Tae Jung Kwon, So Hyeon Lee, and Byeong Hwa Jeong ULVAC Korea Co., Ltd.</p>



TP-085	<p>Optical Modulation of Charge Transport and Negative Differential Transconductance in Hybrid Heterojunction Transistor for Reconfigurable Multi-Valued Logic</p> <p>Jong Chan Shin¹, Serang Jung¹, and Youn Sang Kim^{1,2}</p> <p>¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology</p>
TP-086	<p>Development of ON STACK Warpage Control Technique for Enhanced Integration Density in 3D NAND Flash Memory Device</p> <p>Taehun Lim, Gunhee Jo, Chanyeop Kim, Gangjin Cheon, and Jaegab Lim</p> <p>WONIK IPS Co., Ltd.</p>
TP-087	<p>Machine Learning-Guided Optimization of He Plasma-Assisted ALD for In₂O₃ Thin-Film Transistors</p> <p>Sung Hoon Lee^{1,2} and Youn Sang Kim^{1,2}</p> <p>¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology, ³Samsung Institute of Technology, Samsung Electronics Co., Ltd.</p>
TP-088	<p>All-Oxide Double-Peak Anti-Ambipolar Transistors for Quaternary Logic Inverter through Composition Modulation</p> <p>Jun Ah Jeon¹, Jong Chan Shin¹, Jiho Lee¹, and Youn Sang Kim^{1,2}</p> <p>¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology</p>
TP-089	<p>Accelerated Compositional Optimization of InZnHfO Thin-Film Transistors Using Machine Learning</p> <p>Jiho Lee¹, Serang Jung¹, Minho Jekal¹, and Youn Sang Kim^{1,2}</p> <p>¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology</p>
TP-090	<p>Non-Filamentary Self-Rectifying Resistive Switching in Sb₂Te₃-Based Chalcogenide RRAM</p> <p>Sang Hyun Park and Yoon Jang Chung</p> <p>Department of Chemical and Biological Engineering, Korea University</p>



TP-091	<p>차세대 Vertical NAND 패터닝을 위한 SiON 하드마스크 공정 Soo Namgoong, Woongbin Yim, Sung Ryong Moon, Sangmin Lee, and Young Chul Choi Department of CVD Development Team, WONIK IPS Co., Ltd.</p>
TP-092	<p>Field-Effect Mobility Boosting via Ar-Plasma Post-Treated TiO_{2-x} Interlayer for Specific Contact Resistivity Reduction in In_2O_3 Transistors Seungsu Kim^{1,2}, Taikyu Kim², Seung Ho Ryu^{1,2}, Bo Kyoung Kim², Jongseo Kim^{1,2}, Seungwan Ye^{1,2}, and Seong Keun Kim^{1,2} ¹KU-KIST Graduate School of Converging Science and Technology, Korea University, ²Electronic and Hybrid Materials Research Center, KIST</p>
TP-093	<p>Seed-Layer-Mediated Nucleation for Enhanced Atomic Layer Deposition of Platinum Thin Films Yoohyeon Jung, Dahui Jeon, and In-Hwan Baek Department of Chemical Engineering and Program in Semiconductor Convergence, Inha University</p>
TP-094	<p>In-Cycle Plasma-Assisted Atomic Layer Deposition for High-Performance Oxide Thin-Film Transistors Taeyun Kim^{1,3}, Serang Jung¹, Minho Jekal¹, and Youn Sang Kim^{1,2} ¹Department of Chemical and Biological Engineering, College of Engineering, Seoul National University, ²Advanced Institute of Convergence Technology, ³Samsung Institute of Technology, Samsung Electronics Co., Ltd.</p>
TP-095	<p>Multifunctional Hybrid Superlattices: Achieving Ultra-High Conductivity, Optical Transparency, and Superior Gas-Barrier Performance in Flexible Thin Films Quang Khanh Nguyen¹, Soojin Park², Junseong Hur², and Myung Mo Sung¹ ¹Department of Chemistry, Hanyang University, ²Department of Semiconductor Engineering, Hanyang University</p>
TP-096	<p>Tailoring Mechanical Properties and Surface Energy of Fluorinated Copolymer Thin Films via iCVD for Interfacial Stabilization of Zinc Metal Anodes 오명석¹, 이진홍², 정규남², 장봉준¹, 임성갑³ ¹한국화학연구원, ²한국에너지기술원, ³KAIST 생명화학공학과</p>



TP-097	<p>Morphable and Sustainable Thin-Film Electronics Enabled by Shape-Reconfigurable and Recyclable Covalent Adaptable Network Substrates</p> <p>Jihun Noh¹, Jae Hyuk Lee², Jaehoon Lee^{3,4}, Seongmo Kang^{3,4}, Chae Bin Kim^{2,5,6}, and Boseok Kang^{1,3,4,7}</p> <p>¹Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ²School of Chemical Engineering, Pusan National University, ³SKKU Advanced Institute of Nano Technology, Sungkyunkwan University, ⁴Department of Nano Science and Technology, Sungkyunkwan University, ⁵Department of Polymer Science and Engineering, Pusan National University, ⁶Research Institute for Convergence of Biomedical Science and Technology, Pusan National University Yangsan Hospital, ⁷Department of Nano Engineering, Sungkyunkwan University</p>
TP-098	<p>Indium 삽입층을 통한 IGZO 박막 트랜지스터의 소스/드레인 접촉 특성 향상 및 열 적 안정성 확보</p> <p>이호석¹, 김희태¹, 김희수¹, 조성행², 조병진¹</p> <p>¹한국과학기술원 전기 및 전자공학부, ²한국전자통신연구원 플렉시블 전자소자연구실</p>
TP-099	<p>Enhanced Electrical Stability of Al₂O₃/ZnO Thin-Film Transistors through a Ga₂O₃ Interfacial Layer</p> <p>AhYoung Kim¹, Hyunmin Dang¹, Taehyun Yoon¹, Minje Lee¹, Jiyea Nam¹, and Hyungtak Seo^{1,2}</p> <p>¹Department of Energy Systems Research, Ajou University, ²Department of Materials Science and Engineering, Ajou University</p>
TP-100	<p>Discrete Feeding Method(DFM)을 이용한 HfO₂/Al₂O₃ 구조의 다이폴 제어 및 신뢰성 향상 연구</p> <p>박수연¹, 서형탁^{1,2}</p> <p>¹아주대학교 에너지시스템학과, ²아주대학교 첨단신소재공학과</p>
TP-101	<p>O₂ In-Situ 어닐 기반 계면 결합 개선: 히스테리시스-프리 IAZO TFT for PIM</p> <p>윤태현¹, 당현민¹, 김아영¹, 남지예¹, 서형탁^{1,2}</p> <p>¹아주대학교 에너지시스템학과, ²아주대학교 첨단신소재공학과</p>
TP-102	<p>Crystallinity-Driven Enhancement of Charge-to-Spin Conversion in Sb₂Te₃ Topological Insulator Films</p> <p>Woo Hyun Park and Yoon Jang Chung</p> <p>Department of Chemical and Biological Engineering, Korea University</p>



TP-103	<p>Exploration of Metal Doped HfO₂ Ferroelectric Films via PEALD for Negative-Capacitance Field-Effect Transistors</p> <p>Hye Won Lee^{1,2}, Young Bin Yoon¹, So Jeong Yun^{1,2}, and Jung Wook Lim^{1,2}</p> <p>¹Next-Generation Semiconductor Device Research Section, ETRI, ²Department of Semiconductor and Advanced Device Engineering, University of Science and Technology</p>
TP-104	<p>Properties of Strontium Oxide Films Deposited by Atomic Layer Deposition for Strontium Compounds</p> <p>Ji Hwan Kim and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>
TP-105	<p>Research on Phase Transition via Ar Plamsa and UV/Ozone Treatment</p> <p>Joon Soo Byeon, Ha Yeon Choi, Ju Yong Shin, Seung Ri Jeong, Jong Mun Park, Shivam Kumar, Gauatam, and Hi-DeokLee</p> <p>Department of Electronics Engineering, Chungnam National University</p>
TP-106	<p>산소 유량 제어를 통한 Li-Doped NiO 박막의 구조적·전기적·광학적 특성 변화</p> <p>정혜린, 김지형, 김해찬, 윤승복, 홍정수</p> <p>가천대학교 IT융합대학 전기공학과</p>
TP-107	<p>Deposition of Ru-Doped ZnO Thin Films by ZnO/Ru Super-Cycle Atomic Layer Deposition</p> <p>Jaewoo Ku, YongJu Kim, Chanmin Park, and Minsu Kim</p> <p>Department of Advanced Materials Science and Engineering, Kyonggi University</p>
TP-108	<p>Improved Switching Uniformity of HfO₂-Based RRAM through Controlled TiN Deposition and Interfacial TiO_xN_y Engineering</p> <p>Seokho Cho, Juan Hong, Soonbin Kwon, and Woongkyu Lee</p> <p>Department of Materials Science and Engineering, Soongsil University</p>
TP-109	<p>Formation and Electrical Properties of High-k SrTiO₃ Thin Films via ALD Using Sr(dmts)(hfac)₂ and (CpMe₅)Ti(OMe)₃ Precursors</p> <p>Sangyeon Jeong, Jaejun Lee, Juhyeong Kim, and Woongkyu Lee</p> <p>Department of Materials Science and Engineering, Soongsil University</p>



TP-110	<p>Improving Electrical Properties of Indium Oxide Thin-Film Transistors through Niobium Ion Doping</p> <p>Su-Jin Sim, Dong Geun Kim, Kang Choi, and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>
TP-111	<p>Bottom-Electrode Engineering for Enhanced Performance of Morphotropic Phase Boundary $Hf_{1-x}Zr_xO_2$ DRAM Capacitors</p> <p>Donghyeon Bae¹ and Hyungtak Seo^{1,2}</p> <p>¹Department of Energy Systems Research, Ajou University, ²Department of Materials Science and Engineering, Ajou University</p>
TP-112	<p>Abrupt Switching and Ultralow-Leakage Operation in an IGZO FET Integrated with a Filamentary Threshold Switch</p> <p>Anupom Devnath^{1,2}, Seungwoo Moon^{1,2}, Junseong Bae^{1,2}, Batyrbek Alimkhanuly^{1,2}, Gisung Lee^{1,2}, Minwoo Lee^{1,2}, Jinsu Choi^{1,2}, Taemin Sim^{1,2}, Yongjun Kim^{1,2}, and Seunghyun Lee^{1,2}</p> <p>¹Department of Electronic Engineering, Kyung Hee University, ²Department of Semiconductor Engineering, Kyung Hee University</p>
TP-113	<p>Achieving p-Type SnO_x via Sn(IV)-Based Thermal ALD: Oxygen-Deficient Growth and Mid-Film Al_2O_3 Interlayers</p> <p>Suhyeon Park, Huiseung Kim, Dawon Lee, Jiseop Byeon, Jeongin Seo, and Roy Byung Kyu Chung</p> <p>Department of Advanced Materials Science and Engineering, Kyungpook National University</p>
TP-114	<p>계면층 삽입을 통한 산화알루미늄-산화물반도체 계면 품질과 산화물 InGaZnO 박막 트랜지스터의 성능 조절</p> <p>김건하, 이진영, 정재욱</p> <p>충북대학교 정보통신공학부</p>
TP-115	<p>Area-Selective Deposition of SnS_2 Using Self-Assembled Monolayer as Bottom-Up Patterning</p> <p>Dong Geun Kim, Kang Choi, Su-Jin Sim, and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>



TP-116	<p>Tuning Properties of Novel Amorphous p-Type Oxide Semiconductor via Thermal Evaporation Rate</p> <p>Seongmo Kang¹, Jaehoon Lee¹, Jihun Noh², and Boseok Kang^{1,2,3}</p> <p>¹SKKU Advanced Institute of Nanotechnology (SAINT) and Department of Nano Science and Technology, Sungkyunkwan University, ²Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ³Department of Nano Engineering, Sungkyunkwan University</p>
TP-117	<p>Multilevel Resistive Switching in Perovskite Memristors Enabled by Ion Gel Layers</p> <p>Seong Min Lee, Jung-Ju Bae, and Min Sup Choi</p> <p>Department of Materials Science & Engineering, Chungnam National University</p>
TP-118	<p>누설전류 억제를 위한 원자층 증착 공정 기반 Al_2O_3/HfO_2 Nanolaminate 유전체 박막 트랜지스터의 구현</p> <p>김민성¹, 이세형², 김성재¹, 윤혜지¹, 지현정¹, 장형록¹, 이문석¹</p> <p>¹부산대학교 전기전자공학과, ²부산대학교 반도체특성화대학사업단</p>
TP-119	<p>Zero-Bias-Operated $Ti_3C_2T_x$ MXene/Lignin Composite Sensor for High-Sensitivity Chemiresistive Detection of CO_2 and NO_2</p> <p>I Ketut Gary Devara and Jun Hong Park</p> <p>Department of Materials Engineering and Convergence Technology, Gyeongsang National University</p>
TP-120	<p>Molecularly Triggered of Oxidized MXene Chemimemristor for Mimicking Biological Olfactory System</p> <p>Windy Ayu Lestari and Jun Hong Park</p> <p>Department of Materials Engineering and Convergence Technology, Gyeongsang National University</p>



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE2-2 (5층, 로비)

[TP] 포스터세션

D. Thin Film Process Technology 분과

TP-121	<p>Investigation of Double-Gate In_2O_3 TFTs Kyusun Han¹ and Hongseok Oh² ¹Department of Intelligent Semiconductor, Soongsil University, ²Department of Physics, Soongsil University</p>
TP-122	<p>Schottky Barrier-Engineered Graphene Heterojunction Biosensor for Ultrasensitive DNA Detection Sung Hyun Kim and Woo Jong Yu Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
TP-123	<p>van der Waals Heterostructure-Integrated Fe-MST Memory Leveraging Ferroelectric Phase-Transition Behavior Dokyoeng Yun and WooJong Yu Sungkyunkwan University</p>
TP-124	<p>Study of Interfacial Property between Electrode and Superconducting NbN Thin Film at Room Temperature and Superconducting Transition Temperature Soyun Woo^{1,2}, Hyesoo Jin^{1,3}, Byeong-Kwon Ju², and Donghee Park¹ ¹KIST, ²School of Electrical Engineering, Korea University, ³Department of Materials Science and Engineering, Seoul National University</p>
TP-125	<p>1k Active-Matrix Thermal Sensor Array Using IGZO Thin Film Transistors Hyunsoo Kim¹, Jaegoo Lee², and Hongseok Oh¹ ¹Department of Intelligent Semiconductor, Soongsil University, ²Department of Physics, Soongsil University</p>



TP-126	<p>Effect of Precursor Molecular Structure on the Growth Behavior and Properties of SiNx Films via Very High-Frequency Atomic Layer Deposition</p> <p>Yea-Ji Kim¹, Kim-Hue Thi Dinh¹, Young-Jin Lim¹, Min-Jeong Rhee¹, Nhat-Le Bui Dang¹, Ngoc Le Trinh², Bonwook Gu², Youngho Kang², Jae Hack Jeong³, Han-Bo-Ram Lee², and Il-Kwon Oh^{1,4}</p> <p>¹School of Intelligence Semiconductor Engineering, Ajou University, ²School of Materials Science and Engineering, Incheon National University, ³CN1 Co., Ltd., ⁴School of Electric and Computer Engineering, Ajou University</p>
TP-127	<p>Discrete Feeding in Atomic Layer Modulation of Dy-Doped HfO₂ Films</p> <p>Bui Huu Phi¹, Byung-Ha Kwak¹, and Il-Kwon Oh^{1,2}</p> <p>¹Department of Intelligence Semiconductor Engineering, Ajou University, ²Department of Electrical and Computer Engineering, Ajou University</p>
TP-128	<p>ZnO/Te 반양극성 스위치를 이용한 Standard Ternary Inverter 연구</p> <p>신현석^{1,3}, 최준혁^{1,4}, 김민재², 전재현², 이병훈^{1,2,3,4}</p> <p>¹Center for Semiconductor Technology Convergence, POSTECH, ²National Institute for Nanomaterials Technology, POSTECH, ³Department of Electrical Engineering, POSTECH, ⁴Graduate school of semiconductor technology, POSTECH</p>
TP-129	<p>저온/고압 수소 열처리 공정을 활용한 IGZO/Te CFET 성능 개선 연구</p> <p>최준혁^{1,3}, 신현석^{1,3}, 김기영^{1,2}, 이병훈^{1,2,3}</p> <p>¹Center for Semiconductor Technology Convergence, POSTECH, ²Department of Electrical Engineering, POSTECH, ³Graduate school of semiconductor technology, POSTECH</p>
TP-130	<p>SiO₂ Interlayer와 고압수소 열처리를 활용한 Te TFT 소자의 Hysteresis 개선에 관한 연구</p> <p>조중훈¹, 김기영^{2,3}, 이병훈^{1,2,3}</p> <p>¹Graduate School of Semiconductor Technology, POSTECH, ²Department of Electrical Engineering, POSTECH, ³Center for Semiconductor Technology Convergence, POSTECH</p>



TP-131	<p>Multimode Operation of Light-Gated Transistors based on Millimeter-Scale Transition-Metal Dichalcogenide Grown by Chemical Vapor Deposition Wonbeom Kim¹, Somnath S. Kundale³, Hyeongtae Kim¹, Su-Yeon Cho¹, Mi Ji Kwon¹, Soobin Shim¹, and Jun Hong Park^{1,2} ¹Department of Materials Engineering and Convergence Technology, Gyeongsang National University, ²School of Materials Science and Engineering, Gyeongsang National University, ³Research Institute for Green Energy Convergence Technology, Gyeongsang National University</p>
TP-132	<p>열 안정화 Cp-Hf 전구체를 이용한 HfO₂ 박막의 ALD 증착 및 전기적 특성 평가: H₂O와 O₃산화제의 비교 연구 정현우^{1,2}, 김형민¹, 정재학¹, 허성우², 이희철² ¹씨엔원, ²한국공학대학교 신소재공학과</p>
TP-133	<p>Comparative Analysis of Lithography-Process-Induced Chemical and Potential Changes in InGaZnO Thin Films via XPS(X-ray Photoelectron Spectroscopy) and KPFM(Kelvin Probe Force Microscopy) Hojoon Jeong and Bo Sung Kim Division of Display and Semiconductor Physics, Korea University</p>
TP-134	<p>Multi-Level Switching and Threshold Engineering in Te/Al:IZTO Heterojunction TFTs Jung Hoon Han^{1,2}, MiRiNae Lee³, Chahwan Yang³, Byeong-Kwon Ju², and Sooji Nam^{1,3} ¹Flexible Electronic Device Research Division, ETRI, ²Department of Micro/Nano System, Korea University, ³Semiconductor and Advanced Device Engineering, University of Science and Technology</p>



TP-135	<p>Threshold Switching Behavior and Oxygen–Vacancy–Driven Mechanisms in Pure SiO_2–Based Selectors with Tunable V_{th}</p> <p>Hye Rim Kim¹, Tae Jung Ha², Jeong Hwan Song², Su Yong Park¹, Jun Seok Moon¹, Min–Yeong Choi³, Jae Hyeon Lee⁴, Jeong Hwan Han⁴, Byung Joon Choi⁴, Jae Hyuck Jang^{3,5}, Soo Gil Kim², and Tae Joo Park¹</p> <p>¹Department of Materials Science and Chemical Engineering, Hanyang University, ²SK hynix Inc., ³AI enhanced Electron Microscopy Research Group, Korea Basic Science Institute, ⁴Department of Materials Science and Engineering, Seoul National University of Science & Technology, ⁵Graduate School of Analytical Science and Technology, Chungnam National University</p>
TP-136	<p>Superconducting Transition Characteristics of NbN Films Applied to Nanowire Photon Detectors</p> <p>Hyesoo Jin^{1,2}, Soyun Woo^{1,3}, Howon Jang², and Donghee Park¹</p> <p>¹Center for Quantum Technology, KIST, ²Department of Materials Science and Engineering, Seoul National University, ³School of Electrical Engineering, Korea University</p>
TP-137	<p>Capacitance–Voltage Characteristics of PMMA–Passivated Solution–Processed IGZO TFTs</p> <p>Chaeyeon Jeong, Ayeong Cho, and Jaewook Jeong</p> <p>School of Information and Communication Engineering, Chungbuk National University</p>
TP-138	<p>Study on In–O Based Interfacial Layer as a Diffusion Barrier In–Te Alloy and W Electrode for Vertical Selector–Only Memory Applications</p> <p>Beom Joo Kim^{2,3}, Myeong Hwan You², and Dae Hwan Kang^{1,2}</p> <p>¹Department of Semiconductor Engineering, POSTECH, ²Graduate School of Semiconductor Technology, POSTECH, ³Department of Materials Science and Engineering, Chonnam National University</p>
TP-139	<p>높은 전자이동도를 가지는 N– 도핑 ALD SnO_2 전하수송층 형성 방법</p> <p>한재연¹, 김형민¹, 배다은¹, 이성민¹, 최재호¹, 이원종², 임종철², 정재학¹</p> <p>¹(주)씨엔원 기업부설연구소, ²충남대학교 에너지과학기술대학원</p>



TP-140	<p>Low-Temperature Solution-Processed Indium-Tungsten Oxide Thin-Film Transistor via Hybrid Microwave Annealing Seong-Hwan Lim and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University</p>
TP-141	<p>Enhanced Leakage Suppression and Interfacial Stability in ZrO_2-Based MIM Capacitor with In_2O_3 Electrodes for DRAM Applications Hoonseok Son and Woojin Jeon Department of Materials Science and Engineering, Kyung Hee University</p>
TP-142	<p>$Ag/Al_2O_3/Ag$ Multilayer 기반의 FP UV Pass Filter 제작 및 Ag 두께에 따른 빛의 투과율 평가 정한비, 김지형, 김해찬, 윤승복, 흥정수 가천대학교 전기공학과</p>
TP-143	<p>Effect of H_2/N_2 Ratio on the Electrical and Structural Properties of Cobalt Films Prepared by Plasma-Enhanced Atomic Layer Deposition Seungchae Lee¹, Hyunjin Lim¹, Yehbeen Im¹, Youngseo Na², Kangbaek Seo², Donguk Kim², Sangtae Park¹, Kanghyeok Lee², and Changhwan Choi^{1,2} ¹Division of Materials Science & Engineering, Hanyang University, ²Department of Semiconductor Engineering, Hanyang University</p>
TP-144	<p>Modulating Synaptic Plasticity of Analogue Memristor based on Oxidized MXene Composited with ZrO_2 Quantum Dots Prabana Jetty¹, Fernando Ordóñez Morales¹, Seongchan Kim², and Jun Hong Park¹ ¹Department of Materials Engineering and Convergence Technology, Gyeongsang National University, ²Division of Materials Engineering and Science, Hanyang University</p>
TP-145	<p>Optoelectronic Synapse Behavior of HfS_2 Grown via Molten Salt Flux Method Karybayeva Kamila, Mi Ji Kwon, and Jun Hong Park Department of Material Engineering and Convergence Technology, Gyeongsang National University</p>
TP-146	<p>Surfactant-Driven Morphology Evolution of Hydrothermal Ceria Abrasives Su-Bin Kim, Pil-Su Kim, Min-Uk Jeon, Se-Hui Lee, Hye-Min Lee, Jin-Sub Park, and Jea-Gun Park Hanyang University</p>



TP-147	<p>Enhanced NiSi Formation via Template Effect and Suppression of Ni Channeling by Employing MoN Interlayer</p> <p>Jiwan Hong, Seoeun Yoon, and Woojin Jeon</p> <p>Department of Materials Science and Engineering, Kyung Hee University</p>
TP-148	<p>Improving Dielectric Properties of TiO₂ Dielectric Films by Employing ZrO₂ Buffer Layer for Next-Generation DRAM Capacitor Applications</p> <p>Kyungmo Yang, Chaeyeong Hwang, and Woojin Jeon</p> <p>Department of Materials Science and Engineering, Kyung Hee University</p>
TP-149	<p>MoSi_x의 에피택셜 효과에 의한 C54 상 TiSi₂ 형성 온도 감소</p> <p>윤서은, 흥지완, 전우진</p> <p>경희대학교 신소재공학과</p>
TP-150	<p>Leakage Suppression and Memory Window Optimization via Gd-Doped HfO₂ Charge-Trap Layers in 3D NAND</p> <p>Jonghyeok Lee¹, Jihun Nam¹, Hansol Oh², Hanbyul Kim², Yongjoo Park², and Woojin Jeon¹</p> <p>¹Department of Materials Science and Engineering, Kyung Hee University, ²Advanced Research Development Team, SK Trichem Co. Ltd</p>
TP-151	<p>Conformality-Controlled Atomic Layer Deposition with High Mass Noble Gas Plasma</p> <p>Sangwon Lee¹, Jeongmin Oh¹, Jaeyun Lim¹, Donghyun Kim², and Jihwan An^{1,2}</p> <p>¹Graduate School of Semiconductor Technology, POSTECH, ²Department of Mechanical Engineering, POSTECH</p>
TP-152	<p>Enhancement of Electrical Characteristics of ALD-HfO₂ Thin Films Using a Modulated Split Feeding Approach (SFA) with TEMA-Hf and O₃</p> <p>Soyoung Park¹, Sangkuk Han², Wonjae Choi¹, Kyungwook Park², Jaewon Chung¹, Haesoo Jang¹, Sangmyun Lim¹, Sangwoo Jeong², and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science & Engineering, Hanyang University</p>



TP-153	<p>Enhanced Performance in Polymer Transistors via Chain Alignment for Biosensor Applications</p> <p>Jin Seok Yoon, Nak Hee Kang, Won Bae Cho, Hyung Soo Ahn, and Young Tea Chun</p> <p>Division of Electronics and Electrical Information Engineering, Korea Maritime & Ocean University</p>
TP-154	<p>Enhancement of Electric Properties in MoO₂ Deposited by ALD Using Ethanol as a Reactant</p> <p>Soo Min Yoo, Chaeyeong Hwang, and Woojin Jeon</p> <p>Department of Materials Science and Engineering, Kyung Hee University</p>
TP-155	<p>Effects of VO_x Interlayer on TiN Bottom Electrode on the Electrical Properties of Hf_{0.5}Zr_{0.5}O₂ Ferroelectric Thin Films</p> <p>Iksun Kwon¹, Seungwoo Lee¹, Jaehyeon Yun¹, Hyunseok Oh², Donghun Shin², Yongjoo Park², and Woojin Jeon¹</p> <p>¹Department of Materials Science and Engineering, Kyung Hee University, ²Advanced Research Development Team, SK Trichem Co., Ltd.</p>
TP-156	<p>Atomic Layer Infiltration of Al₂O₃ for Organic-Inorganic Hybrid Encapsulation of Flexible OLED Display</p> <p>Si Eun Jung¹, Ye Jin Jung¹, Su Min Eun¹, Ji Ho Jeon¹, Kwan hyuck Yoon², Woo Yong Sung², Jeong Hwan Han¹, and Byung Joon Choi¹</p> <p>¹Seoul National University of Science & Technology, ²Display Research Center, Samsung Display Co., Ltd.</p>
TP-157	<p>Subthreshold-Swing Control in Oxide-Semiconductor TFTs via Sn-Doped IGO (IGTO) Channels</p> <p>Yeonghun Kim¹, Yena Kim², and Jae Kyeong Jeong^{1,2}</p> <p>¹Department of Nano Semiconductor Engineering, Hanyang University, ²Department of Electronic Engineering, Hanyang University</p>
TP-158	<p>Effect of Crystallinity-Controlled ZrO₂ Films on the Resistive Switching Behavior of RRAM Devices</p> <p>Juyoung Park, Gyeongil Son, Suraj B. Madake, and Minjae Kim</p> <p>School of Materials Science & Engineering, Yeungnam University</p>



TP-159	<p>Deposition of ALD HfO₂ Films at Various Temperatures and Evaluation of Device Characteristics</p> <p>Muheon Kim, Hyun-June Park, Subin Lee, and Sung-Woong Chung Graduate School of Semiconductor Technology, POSTECH</p>
TP-160	<p>Area-Selective Atomic Layer Deposition of Molybdenum Carbide Enabled by Acetylacetone as a Dual-Function Inhibitor and Counter-Reactant</p> <p>Min Seok Kim, Ji Sang Ahn, and Jeong Hwan Han Department of Materials Science and Engineering, Seoul National University of Science & Technology</p>
TP-161	<p>Atomic-Layer-Doped TaO_x Memristor With Enhanced Self-Rectification and Analog Switching for Neuromorphic Crossbar Array</p> <p>Gyeongil Son, Juyoung Park, Suraj B. Madake, and Minjae Kim School of Materials Science & Engineering, Yeungnam University</p>
TP-162	<p>Study on Compact Modeling of Selector-Only Memory Devices</p> <p>Ju Hwan Park, Hyun Wook Km, Se Hwan Jeon, and Byung Joon Choi Seoul National University of Science & Technology</p>
TP-163	<p>Impact of Crystallization on Performance and Reliability of ALD-Derived IGO Thin-Film Transistors for High-Thermal-Budget DRAM Application</p> <p>Nahyun Kim¹, Jeong Eun Oh², Daeyoung Kim¹, and Jae Kyeong Jeong² ¹Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²Department of Electronics Engineering, Hanyang University</p>
TP-164	<p>Atomic Layer Etching of ZrO₂ Using SF₆ Plasma and TiCl₄ for DRAM Capacitors</p> <p>Juhyeong Kim, Sangyeon Jeong, Hyeongjun Kim, and Woongkyu Lee Department of Materials Science and Engineering, Soongsil University</p>
TP-165	<p>Threshold Voltage Modulation in nMOS Devices via Rare-Earth Oxide-Based Dipole Engineering Using ALD</p> <p>Gaeul Kim¹, Seungwoo Lee¹, Hanbyul Kim², Hansol Oh², Yongjoo Park², and Woojin Jeon¹ ¹Department of Materials Science and Engineering, Kyung Hee University, ²Advanced Research Development Team, SK Trichem Co., Ltd.</p>



TP-166	<p>Memory Window Enhancement in Ferroelectric Field-Effect Transistors via Oxygen Content Control in IGZO</p> <p>Su Min Jeong¹, He Young Kang², Seung Hee Cha², and Jae Kyeong Jeong^{1,2}</p> <p>¹Department of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²Department of Electronic Engineering, Hanyang University</p>
TP-167	<p>Bandgap-Engineered Te-Based Alloy Films for High-Performance p-Channel Thin-Film Transistors</p> <p>Seok Hyun Hwang¹, Chanwoo Jung², and Jae Kyeong Jeong^{1,2}</p> <p>¹Department of Electronic Engineering, Hanyang University, ²Department of Display Science and Engineering, Hanyang University</p>
TP-168	<p>Self-Oxidized Metal Gate-Driven GI Formation for Gate-All-Around Structures toward Process-Simplified 3D DRAM Integration</p> <p>Minsong Kim¹, Jaehyun Park², and Jae Kyeong Jeong^{1,2}</p> <p>¹Department of Electronic Engineering, Hanyang University, ²Department of Artificial Intelligence Semiconductor Engineering, Hanyang University</p>
TP-169	<p>Development of Ru ALD and ALE Processes for Continuous Ultrathin Low-Resistivity Films</p> <p>Eun Ji Ju, Jae Hyeon Lee, and Jeong Hwan Han</p> <p>Department of Materials Science and Engineering, Seoul National University of Science & Technology</p>
TP-170	<p>Switching-Kinetics-Engineered Selector-Less Single-Capacitor FeRAM: First Experimental Demonstration</p> <p>Yong Hyeon Cho¹, Dong Hyun Lee², Geun Hyeong Park¹, Hyeong Seok Choi¹, Dong Hee Han¹, Ga Yeon Lee¹, and Min Hyuk Park¹</p> <p>¹Department of Material Science and Engineering, Seoul National University, ²Electronic Nanomaterials, Center for Functional Nanomaterials, Brookhaven National Laboratory</p>
TP-171	<p>Atomic Layer Deposition of Molybdenum Oxide Thin Films and Electrical Properties under Various Annealing Conditions</p> <p>Seungmin Jo, Hyeongjun Kim, Jiwon Ahn, and Woongkyu Lee</p> <p>Department of Materials Science and Engineering, Soongsil University</p>



TP-172	<p>Electrical Voltage-Induced Optical Property Changes in Te-Based Ovonic Threshold Switch Sangyeop Kim^{1,2}, Siwon Park^{1,2}, Jihun Kang^{1,2}, Young-Min Kim^{1,2}, Seungwhan Moon^{1,2}, and Jong-Souk Yeo^{1,2} ¹School of Integrated Technology, College of Computing, Yonsei University, ²BK21 Graduate Program in Intelligent Semiconductor Technology, Yonsei University</p>
TP-173	<p>Low-Temperature Metal-Induced Crystallization of α-IGZO: High-Performance Thin Films for 3D DRAM Applications Hyun Wee Cho, Jeong Woo Seo, Sae Gyoung Song, Dong Il Kim, Byeong Chan Sim, Min Ju Kim, and Jin Pyo Hong Department of Physics, Hanyang University</p>
TP-174	<p>Parametric Study on the Growth of MoTe₂ Using MOCVD Method: Analysis of Phase-Controlled Synthesis Sanghwa Lee^{1,2}, Hyunwoo Kim^{1,2}, Junghyun Lee^{1,2}, Bongjoong Kim², and Jun Oh Kim¹ ¹KRISS, ²Hongik University</p>
TP-175	<p>TiO₂/IZO Hybrid-Channel Phototransistor Enabling Enhanced In-Sensor Neuromorphic Vision Computing Yu-Jung Cha¹, Akash Bharat More², Sung Woon Cho², and Joon Seop Kwak¹ ¹Department of Energy Technology, KENTECH, ²Department of Advanced Components and Materials Engineering, Sunchon National University</p>
TP-176	<p>Comparative Study of Ga, Sn, and Zn δ-Doped In₂O₃ FETs Fabricated by PEALD for 3D DRAM Integration Gwansun Choi¹, Soojin Park², and Jae Kyeong Jeong^{1,2} ¹Department of Electronic Engineering, Hanyang University, ²Department of Artificial Intelligence Semiconductor Engineering, Hanyang University</p>
TP-177	<p>Advanced Atomic Level Patterning Process by Area Selective Atomic Layer Deposition Integrating Atomic Layer Etching Seo-Hyun Lee¹, Jieun Oh¹, and Woo-Hee Kim² ¹Department of Materials Science and Chemical Engineering, Hanyang University, ²Advanced Materials and Semiconductor Engineering, Hanyang University</p>



TP-178	<p>Effect of Plasma Frequency on the Electrical Properties of Low-k Films Deposited by PECVD with a Dimethoxydimethylsilane Precursor</p> <p>Minwoo Park^{1,2}, Jaeyeon Kim¹, Kyubeom Bae¹, Young Chul Choi², Dong Hak Kim², Younghyun Kim², Jeesoo Lee², and Donggeun Jung¹</p> <p>¹Department of Physics, Sungkyunkwan University, ²WONIK IPS Co., Ltd.</p>
TP-179	<p>Enhancement of Electrical Properties of Molybdenum Oxide Thin Films via Post-Deposition Annealing and Plasma Treatment</p> <p>Jiwon Ahn, Seungmin Jo, Hyeongjun Kim, and Woongkyu Lee</p> <p>Department of Materials Science and Engineering, Soongsil University</p>
TP-180	<p>Suppression of Wake-Up Effect in Morphotropic Phase Boundary Stabilized $Hf_{1-x}Zr_xO_2$ by Yttrium Doping</p> <p>Chan-Bin Hong, Yoonchul Shin, Yeon-Ji Jeon, Ji Hwan Kim, Seo Young Choi, and Ji-Hoon Ahn</p> <p>Department of Materials Science and Chemical Engineering, Hanyang University</p>
TP-181	<p>Chemical Vapor Deposition of Indium-Based Metal Organic Films as an All-Dry Resist for Next-Generation EUV Lithography</p> <p>Hyekyung Kim and Woo-Hee Kim</p> <p>College of Advanced Technology and Convergence, Hanyang University</p>
TP-182	<p>Study of High-k Al_2O_3 Dielectric under High Thermal Budget for FEOL Compatible Top Gate IGZO TFT</p> <p>Sung Ha Lee¹, Joo Hee Jeong², and Jae Kyeong Jeong^{1,2}</p> <p>¹School of Artificial Intelligence Semiconductor Engineering, Hanyang University, ²School of Display Science and Engineering, Hanyang University</p>
TP-183	<p>Sol-Gel Derived Ga_2O_3/ZnO Heterojunction Thin Films for Betavoltaic Energy Conversion</p> <p>장성우, 이윤진, 송민석, 정이현, 신해주, 심인보</p> <p>국민대학교 전자공학부</p>



TP-184	<p>Improved Electrical Performance of HKMG pMOS Device Using Plasma-Based Hydrogen Treatment</p> <p>Wonjae Choi¹, Sangkuk Han², Haesoo Jang¹, Jaewon Chung¹, Sangmyun Lim¹, Kyungwook Park^{2,3}, Sangwoo Jeong², Soyoung Park¹, and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science and Engineering, Hanyang University, ³DRAM Process Development Team, Samsung Electronics Co., Ltd.</p>
TP-185	<p>Atomic Layer Deposition of Ga₂O₃ Using (N,N'-di-tert-butylacetimidamido)dimethylgallium Precursor</p> <p>Jun Yong Lee^{1,2}, Inhong Hwang^{1,2}, Dahui Jeon^{1,2}, Changbong Yeon³, Jaesun Jung³, and In-Hwan Baek^{1,2}</p> <p>¹Department of Chemical Engineering, Inha University, ²Program in Semiconductor Convergence, Inha University, ³Thin Film Materials Development Team, Soulbrain Co., Ltd.</p>
TP-186	<p>Ultra-thin Molybdenum Nitride (MoN) Using a Plasma-Enhanced ALD as a Next-Generation Copper Diffusion Barrier for Scaled Interconnects</p> <p>Donguk Kim¹, Hyunjin Yim², Yehbeen Im², Youngseo Na¹, Kangbaek Seo¹, Seungchae Lee², Kanghyeok Lee¹, Sangtae Park², and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science and Engineering, Hanyang University</p>
TP-187	<p>Oxygen Reservoir Layer-Enabled Performance Enhancement in HZO Ferroelectrics: Dependence on Temperature and Electrode Oxygen Ratio</p> <p>Taesuk Kim¹, Kyungsoo Park¹, Hyeoncheol Jung¹, Yeonwoo Choi², Sangmyun Lim², Jihoon Choi², Jinyeong Lee², and Changhwan Choi^{1,2}</p> <p>¹Division of Materials Science and Engineering, Hanyang University, ²Department of Semiconductor Engineering, Hanyang University</p>
TP-188	<p>Ultra-Thin ALD-MoN Interlayer for Low-Resistance and Reliable Contacts to In-Based Oxide Channels</p> <p>Kyoungyeon Min¹, JinYeong Lee¹, YeonWoo Choi¹, SunBum Kim², and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science and Engineering, Hanyang University</p>



TP-189	<p>Interfacial Oxygen Engineering for Rutile TiO₂ Growth Using Composition-Controlled RuO_x Electrodes</p> <p>Youngseo Na¹, Sangkuk Han², Soyoung Park¹, and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science and Engineering, Hanyang University</p>
TP-190	<p>Strain and Scavenging Effects of Metal Electrodes on Ferroelectric Properties of Hf_xZr_{1-x}O₂ Thin Films</p> <p>Yoonseok Lee¹, Kyung-Soo Park ², Sang-Myun Lim¹, Ji-Hoon Choi¹, Yeon-Woo Choi¹, Jin-Yeong Lee¹, Hyeon-Cheol Jeong¹, Young-Seo Na¹, Chulwon Chung³, Tae-Suk Kim², and Changhwan Choi^{1,2}</p> <p>¹Department of Semiconductor Engineering, Hanyang University, ²Division of Materials Science and Engineering, Hanyang University, ³Research Institute of Industrial Materials Technology, Hanyang University</p>
TP-191	<p>Switching Mechanism and Device Structure Optimization of Al-Doped HfO₂-Based Self-Rectifying Memory for Uniform Switching Performance</p> <p>Yura Oh and Hae Jin Kim</p> <p>Department of Materials Science and Engineering, Myongji University</p>
TP-192	<p>Cobalt Nitride (CoN) as for Alternative P-type Work-Function Metal Gate</p> <p>Haesoo Jang¹, Sangkuk Han², Wonjae Choi¹, Jaewon Chung¹, Sangmyun Lim¹, Kyungwook Park², Sangwoo Jeong², Soyoung Park¹, and Changhwan Choi^{1,2}</p> <p>¹Division of Semiconductor Engineering, Hanyang University, ²Department of Materials Engineering, Hanyang University</p>



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE2-2 (5층, 로비)

[TP] 포스터세션

O. System LSI Design 분과

TP-369	Fuzzy-State-Machine Energy Management System for a 30-kW PEMFC Hybrid System in Green Marine Applications Md. Rubel Sarkar, Jeong-Min Woo, and Hyunwoo Son School of Electronic Engineering, Gyeongsang National University
TP-370	사전 명령어 분석 기반 저전력 RISC-V 프로세서 우정민, Md. Rubel Sarkar, 손현우 경상국립대학교 전자공학부
TP-371	A Low-Power Asynchronous AES-128 Design So Jeong Ok and Woong Choi School of Electronics Engineering, Sookmyung Women's University
TP-372	A 2-4GHz Digital-PLL-Based Octa Clock Generator With Fast Phase Error Calibration for Clock Distribution in HBM Interfaces Si Heon Lee and Younghyun Lim Department of Semiconductor Engineering, Kyung Hee University
TP-373	Residue Number System을 이용한 FPGA 기반 고속신호 전처리 파이프라인 김현진 ¹ , 박태인 ^{1,2} , 최문영 ^{1,2} ¹ 단국대학교 전자전기공학과, ² LIG넥스원(주) 해양연구소
TP-374	오픈-소스 설계 도구 활용 “내 칩 제작 서비스” MPW 검증 사례 국일호 ¹ , 노태문 ¹ , 박건식 ¹ , 정동윤 ¹ , 고상준 ¹ , 김규태 ² , 김용신 ² , 심규현 ³ ¹ 한국전자통신연구원, ² 고려대학교 전기전자공학부, ³ 한성대학교 컴퓨터공학과
TP-375	LPDDR6의 신뢰성을 위한 On-Die ECC 공영배 ¹ , 양준성 ^{1,2,3} ¹ 연세대학교 전기전자공학과, ² 연세대학교 시스템반도체공학과, ³ 연세대학교 지능형반도체 IT융합전공



TP-376	<p>A Novel ECC for Reliability and Power in LPDDR6 Jongwoo Jeon¹ and Joon-Sung Yang^{1,2,3} ¹Department of Electrical and Electronic Engineering, Yonsei University, ²Department of Systems Semiconductor Engineering, Yonsei University, ³BK21 Graduate Program in Intelligent Semiconductor Technology, Yonsei University</p>
TP-377	<p>Xilinx FPGA 기반 FFT 가속 구조를 적용한 GPS 수신기 이현우¹, 라승환¹, 양희훈¹, 유호영¹, 권석영², 양상진², 길창현², 이영빈² ¹충남대학교 전자공학과, ²아이오테크아이</p>



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE3 (6층, 로비)

[TP] 포스터세션

H. Display and Imaging Technologies 분과

TP-276	<p>Mg-Doped NiO Hole Transport Layers with Ultrathin ALD-MgO Diffusion Barriers for All-Inorganic Quantum Dot Light-Emitting Diodes Min Seok Kim^{1,2}, Minsu Kim³, and Seong-Yong Cho^{1,2} ¹Department of Photonics and Nanoelectronics, Hanyang University, ²BK21 FOUR ERICA-ACE Center, Hanyang University, ³Department of Advanced Materials Engineering, Kyonggi University</p>
TP-277	<p>Enhanced Detectivity in PbS QD Photodetectors through P-Type Ink and ALD Integration Hong Gu Kang¹, Ju Young Woo^{1,2,3}, and Seong-Yong Cho^{1,4,5} ¹Department of HYU-KITECH Joint Graduate School, Hanyang University, ²Department of Autonomous Manufacturing & Process R&D, KITECH, ³School of Integrative Engineering, Chung-Ang University, ⁴Department of Photonics and Nanoelectronics, Hanyang University, ⁵BK21 FOUR ERICA-ACE Center, Hanyang University</p>
TP-278	<p>고성능 투명 히터와 융합을 통한 전기변색소자 성능 한계 돌파 연구 이찬경, 강건우, 조재정, 최두호 가천대학교 반도체공학과</p>
TP-279	<p>Ar 플라즈마 기반 초평탄 SnO_x/Ag 계면 구현 기술로 균적외선 차폐, 계면 안정성, 광학적 및 전기적 특성의 동시 향상 조재정, 강건우, 이찬경, 최두호 가천대학교 반도체공학과</p>
TP-280	<p>Enhancing the Stability and Lifetime of Quantum-Dot LEDs via Butylated Hydroxytoluene (BHT) Treatment Jaesung Lee, Byoungho Kang, Saewan Kim, Sanghoon Jung, Sehyuk Yeom, and Wanghoon Lee GERI</p>



TP-281	<p>Synthesis of Green Quantum Dots with High Efficiency and FWHM by Steric Hindrance</p> <p>Byoungho Kang, Jaesung Lee, Saewan Kim, Sanghoon Jung, Sehyuk Yeom, and Wanghoon Lee</p> <p>Semiconductor Defense Industrial Research Center, GERI</p>
TP-282	<p>IGZO TFT의 온도 변화에 따른 접촉저항 거동 분석</p> <p>여준호, 문국철, 김용상</p> <p>성균관대학교 전자전기컴퓨터공학과</p>
TP-283	<p>Large-Area Bilayer MoS₂ Transistor-Based Active Pixel Array for High-Sensitivity Image Sensing</p> <p>Minseo Kim and Sunkook Kim</p> <p>Department of Advanced Materials Science & Engineering, Sungkyunkwan University</p>
TP-284	<p>Universal Selective Transfer Printing via Micro-Vacuum Force for Flexible Optoelectronics</p> <p>Ki Yun Nam¹, Sang Hyun Park², and Keon Jae Lee^{1,2}</p> <p>¹Graduate School of Semiconductor Technology, KAIST, ²Department of Materials Science and Engineering, KAIST</p>
TP-285	<p>Phase Engineering of Two-Dimensional MoS₂ Monolayers for Tuning Optical Properties</p> <p>Ga-Ram Park^{1,2}, Jae-Yeon Kim¹, Mi-Jeong Kim³, Ick-Joon Park³, and Tae In Kim^{1,2}</p> <p>¹School of Electrical and Electronic Engineering, Inha University, ²Program in Semiconductor Convergence, Inha University, ³Department of Electrical and Electronic Engineering, Joongbu University</p>
TP-286	<p>Two-Dimensional MoS₂/h-BN Heterostructures for NAND Flash Memory Device</p> <p>Mi-Jeong Kim¹, Ha-Eun Dang^{2,3}, Ga-Ram Park^{2,3}, Tae In Kim^{2,3}, and Ick-Joon Park¹</p> <p>¹Department of Electrical and Electronic Engineering, Joongbu University, ²School of Electrical and Electronic Engineering, Inha University, ³Program in Semiconductor Convergence, Inha University</p>



TP-287	<p>Enhanced Electrical Performance of WSe₂ Transistors with Defect-Healed Graphene Electrodes</p> <p>Min Seok Kim¹, Janggeun Shin¹, Young-Roh Shin^{2,3}, Tae In Kim^{2,3}, and Ick-Joon Park¹</p> <p>¹Department of Electrical and Electronic Engineering, Joongbu University, ²School of Electrical and Electronic Engineering, Inha University, ³Program in Semiconductor Convergence, Inha University</p>
TP-288	<p>Vertically Stacked MoS₂/Graphene Oxide Van der Waals Heterointerface</p> <p>Su-Yeon Ye¹, Min Seok Kim², Ha-Eun Dang^{1,3}, Ick-Joon Park², and Tae In Kim^{1,3}</p> <p>¹School of Electrical and Electronic Engineering, Inha University, ²Department of Electrical and Electronic Engineering, Joongbu University, ³Program in Semiconductor Convergence, Inha University</p>
TP-289	<p>Electro-Thermal Analysis of Quantum Dot Light-Emitting Diode as Pixel Areas Using Thermoreflectance Microscopy</p> <p>Yongjin Shin¹, Min Seok Kim¹, Daehong Kim¹, Jaekyun Kim¹, Seong-Yong Cho¹, SangHoon Shin², and Younghyun Kim¹</p> <p>¹Department of Photonics and Nanoelectronics, BK21 FOUR ERICA-ACE Center, Hanyang University ERICA, ²School of Electrical Engineering, Hanyang University ERICA</p>
TP-290	<p>Development of Al₂O₃/HfO₂/Oxide-Based ARC Structure for Enhancing Quantum Efficiency in the UV Wavelength Range (200–400 nm)</p> <p>Joo Young Jeong, Hong Boem Park, Sang Won Yun, Man Lyun Ha, and Sang Gi Lee</p> <p>CIS Process Development Team, DB HiTek</p>
TP-291	<p>압전-발광 동시 거동을 갖는 PVDF 그라프트 발광 공중합체의 합성</p> <p>정예원¹, 이규영¹, 임병진¹, 장지혜⁴, 진유진¹, 신상민¹, 김가영¹, 이진균^{1,2}, 김동환³, 김은경³, 박철민⁴</p> <p>¹인하대학교 고분자환경융합공학전공, ²인하대학교 고분자공학과, ³연세대학교 화공생명공학과, ⁴연세대학교 신소재공학과</p>



TP-292	<p>Interfacial Electronic Structure of ALD-Grown $\text{Al}_2\text{O}_3/\text{CuI}$-Based Heterojunction Janggeun Shin¹, Seo Yeon Kim^{2,3}, Su-Yeon Ye², Hyuck-In Kwon⁴, Tae In Kim^{2,3}, and Ick-Joon Park¹ ¹Department of Electrical and Electronic Engineering, Joongbu University, ²School of Electrical and Electronic Engineering, Inha University, ³Program in Semiconductor Convergence, Inha University, ⁴School of Electrical and Electronics Engineering, Chung-Ang University</p>
TP-293	<p>Development of $\text{CuI}/\text{In}_2\text{Se}_3$ pn Heterojunction for Optoelectronic Applications Seo Yeon Kim^{1,2}, Ga-Ram Park^{1,2}, Jae-Yeon Kim¹, Ick-Joon Park³, and Tae In Kim^{1,2} ¹School of Electrical and Electronic Engineering, Inha University, ²Program in Semiconductor Convergence, Inha University, ³Department of Electrical and Electronic Engineering, Joongbu University</p>
TP-294	<p>Strain-Insensitive Capacitive Pressure Sensor for Stretchable Displays Geonoh Choe and Yei Hwan Jung Department of Electronic Engineering, Hanyang University</p>
TP-295	<p>Super Sampling 기반의 Pixel-Modulation Transfer Function(MTF) 시뮬레이션 방법론 성현준¹, 박문수¹, 김윤경^{1,2} ¹동아대학교 ICT융합해양스마트시티공학과, ²동아대학교 전자공학과</p>
TP-296	<p>MicroLED Integration with Single-Crystalline MOSFETs on a Releasable Substrate Ji-Woong Cho and Yei-Hwan Jung Department of Electronic Engineering, Hanyang University</p>
TP-297	<p>광대역 및 고안정성 $\text{ZnO}/\text{Te}/\text{Al}_2\text{O}_3$ 구조 기반 광트랜지스터 강민서^{1,2}, 이경수^{1,3}, 이병훈^{1,2,3} ¹Center for Semiconductor Technology Convergence, POSTECH, ²Department of Electrical Engineering, POSTECH, ³Graduate School of Semiconductor, POSTECH</p>



TP-298	<p>Sub-1-Volt, Reconfigurable Gires-Tournois Resonators for Enhanced Chromaticity in Reflective Monopixel Array</p> <p>Hyo Eun Jeong and Young Min Song School of Electrical Engineering, KAIST</p>
TP-299	<p>Stress Analysis of a-IGZO TFTs with Various Device Architectures for Stretchable Backplanes Using Rigid-Island Structures</p> <p>Seong Eun Kim^{1,2}, Ji-Ho Lee^{1,2}, Dong Hyeon Lee^{1,2}, Soobin An^{1,2}, and Soo-Yeon Lee^{1,2}</p> <p>¹Department of Electrical and Computer Engineering, Seoul National University, ²Inter-university Semiconductor Research Center, Seoul National University</p>
TP-300	<p>Study on the Degradation Mechanism in AlGaN_xP-Based Red Micro-LEDs under Electrical Stress</p> <p>Yeonbin Kang, T. Lee, and Dae-Myeong Geum Department of Electrical and Computer Engineering, Inha University</p>
TP-301	<p>Crystallization of In₂O₃ Channels for the Self-Aligned Coplanar Thin Film Transistor Applications by Controlling the Channel Thickness</p> <p>A-yeong Lee, Se-Hyun Chun, and Sung-Min Yoon Kyung Hee University</p>
TP-302	<p>Ultra-Thin ($\leq 0.5 \mu\text{m}$) InGaAs Broadband (400–1700 nm) Photodetectors With High External Quantum Efficiency ($>70\%$)</p> <p>Hosang Hwang¹, Junho Jang², Dae-Myeong Geum³, and SangHyeon Kim^{1,2}</p> <p>¹Graduate school of semiconductor technology, KAIST, ²School of Electrical Engineering, KAIST, ³Department of Electrical and Electronic Engineering, Inha University</p>
TP-303	<p>Impact of Gate-Edge Fields and Self-Heating on Self-Aligned Top-Gate a-IGZO TFTs</p> <p>Hyeonseo Lee, Seunghyun Oh, and Junhong Na Department of Semiconductor Physics, Kangwon National University</p>



TP-304	<p>Light-Driven Reconfigurable Logic in a Monolithic Perovskite Device via Nonlinear Photoresponse Switching</p> <p>Dante Ahn^{1,2}, Hyun Seock Song^{1,2}, and Yusin Pak³</p> <p>¹Nano-Photonics System Research Center, KIST, ²KU-KIST Graduate School of Converging Science and Technology, Korea University, ³Division of Advanced Materials Engineering, Jeonbuk National University</p>
TP-305	<p>OLEDoS Pixel Transistor Optimization through the AI-Based TCAD Optimization Method(ATOM) Framework</p> <p>Suyeong Hwang¹, Jaekyoung Son², Sueyeon Kim², Wook Lee³, and Jongwook Jeon²</p> <p>¹Department of Display Engineering, Sungkyunkwan University, ²Department of Electrical and Computer Engineering, Sungkyunkwan University, ³Department of Semiconductor Convergence Engineering, Sungkyunkwan University</p>
TP-306	<p>Solution-Processable Polymer/ZnO Hybrid UV Detector with Tunable Doping for Performance Optimization</p> <p>Sanghoon Jung, Byoungho Kang, Jaesung Lee, Saewan Kim, Sehyuk Yeom, and Wanghoon Lee</p> <p>Semiconductor-Defense Industrial Research Center</p>
TP-307	<p>Enhanced Electrical Performance of Cul Transistors through Hydrogen-induced Buried Interface Engineering</p> <p>Hyeong-Min Kim¹, Hyo-Won Jang¹, Tae-In Kim², Ick-Joon Park³, and Hyuck-In Kwon¹</p> <p>¹Chung-Ang University, ²Inha University, ³Joongbu University</p>
TP-308	<p>Vacancy-Engineered Cul/IGTO Vertical Heterojunction for High-Performance Self-Powered Ultraviolet Photodetectors</p> <p>Go-Eun Kim¹, Tae-In Kim², Ick-Joon Park³, and Hyuck-In Kwon¹</p> <p>¹Chung-Ang University, ²Inha University, ³Joongbu University</p>
TP-309	<p>Next Generation Cold Electron Beam Microscopy Using a Carbon Nanotube Cold Cathode</p> <p>Aniket Karande and Kyu Chang Park</p> <p>Kyung Hee University</p>



TP-310	<p>Single-Island Cold Cathode Electron Beam (C-Beam) for High Resolution X-ray Semiconductors Inspection Ravindra Patil, and Kyu Chang Park Kyung Hee University</p>
TP-311	<p>Light-Driven Polarity Control in Single Walled Carbon Nanotube/Te Transistors for Convolution Neural Networks Jae Jun Lee¹, Yun Sung Lee², Han Min Kim², Hyeyon Bin Jo¹, Soo Heon Chae¹, and Sung Hun Jin¹ ¹Department of Information Display, Kyung Hee University, ²Department of Intelligent Semiconductor Engineering, Incheon National University</p>
TP-312	<p>Solution Processed Cs₂SnI₆ RRAMs for Security Application Ji Sang Kim¹, Hyeyon Bin Jo¹, Han Min Kim², Yun Sung Lee², and Sung Hun Jin¹ ¹Department of Information Display, Kyung Hee University, ²Department of Intelligent Semiconductor Engineering, Incheon National University</p>
TP-313	<p>Statistical Analysis and Origin of Photodiode Full-Well Capacity Variation in CMOS Image Sensors 강전웅, 박지원, 이지원 포항공과대학교 반도체공학과</p>
TP-314	<p>In-Situ STEM-DPC Biasing을 이용한 OLED 소자 내 전기장 분포 이미징 윤진하¹, 이기용¹, 김종일¹, 신미향², 이용희², 한석규², 오상호¹ ¹한국에너지공과대학교 에너지신소재트랙, ²삼성디스플레이</p>
TP-315	<p>Trap-Mediated Negative Feedback Enables Self-Regulated Switching in Polymer Inserted CuI RRAMs Yun Tae Won¹, Ji Min Kang¹, Dhananjay Mishura¹, Hyeyon Bin Jo¹, Han Min Kim², Yun Sung Lee², and Sung Hun Jin¹ ¹Department of Information Display, Kyung Hee University, ²Department of Intelligent Semiconductor Engineering, Incheon National University</p>



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE3 (6층, 로비)

[TP] 포스터세션

R. Semiconductor Software 분과

TP-378	LLM 추론 시나리오별 스토리지 I/O 특성 분석 정준혁 ¹ , 최문석 ² , 김재호 ² ¹ 경상국립대학교 항공우주및소프트웨어공학부, ² 경상국립대학교 AI융합공학과
TP-379	FEMU 기반 FDP SSD 애뮬레이터 구현 이슈와 해결 방안 Nakyeong Kim and Jongmoo Choi Department of AI-based Convergence, Dankook University
TP-380	WAL-aware한 KVSSD 설계 및 구현 Kwanghee Lee and Jongmoo Choi Department of Computer Science, Dankook University
TP-381	eBPF 기반 실시간 메모리 접근 패턴 탐지 및 페이지 교체 정책 최적화 시스템 안승현 ¹ , 이동영 ² , 강동현 ¹ ¹ 동국대학교 컴퓨터·AI학부, ² 동국대학교 일반대학원 컴퓨터·AI학과



2026-01-29(목), 10:00-19:00

(공식발표시간: 17:40-19:00)

ZONE3 (6층, 로비)

[TP] 포스터세션

K. Memory (Design & Process Technology) 분과

TP-383

Optimizing Al-Doped HfO₂ Thin Films via PEALD for Ferroelectric Tunnel

Junction (FTJ) Applications

Gangmin Lee, Sang Hyun Sung, and Keon Jae Lee

Department of Materials Sciences and Engineering, KAIST