



# 제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

## Future Normal in Semiconductor

2025-02-13(목), 15:50-17:20

좌장: 추후업데이트 예정

L. Analog Design 분과

### [TM3-L] Analog Circuits

<p><b>초청</b> TM3-L-1 15:50-16:20</p>	<p><b>Low-power Voltage References</b> Youngwoo Ji Hanbat National University</p>
<p>TM3-L-2 16:20-16:35</p>	<p><b>A 2x48 Gb/s Single-Ended PAM-3 Transmitter with Crosstalk Cancellation Technique Separated from Feed-Forward Equalizer</b> Junsu Park, Dongwoo Kang, and Kwanso Park Yonsei University</p>
<p>TM3-L-3 16:35-16:50</p>	<p><b>A Low-Jitter Injection-Locked CDR with Injection Rate Control</b> Junhak Kim, Youngwook Kim, and Kwanso Park Yonsei University</p>
<p>TM3-L-4 16:50-17:05</p>	<p><b>A 32-Gb/s 0.52-pJ/b Sub-Baud-Rate Receiver with 1-tap DFE</b> Ho-Joon Shin, Suil Kang, Yoojin Jung, and Kwanso Park Yonsei University</p>
<p>TM3-L-5 17:05-17:20</p>	<p><b>A 12-Bit 500-MS/S Ring Amp-Based Fully-Differential Pipelined SAR ADC With Dynamic Body Biasing</b> Jisu Kim<sup>1</sup>, Jun-Eun Park<sup>1</sup>, and Taeho Lee<sup>2</sup> <sup>1</sup>Department of Electrical and Computer Engineering, Sungkyunkwan University, <sup>2</sup>Department of Semiconductor Convergence Engineering, Sungkyunkwan University</p>