

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025-02-13(목), 09:00-10:45

좌장: 추후업데이트 예정

G. Device & Process Modeling, Simulation and Reliability 분과

[TJ1-G] TCAD Simulation

초청 TJ1-G-1 09:00-09:30	Impact of Random Discrete Dopants on 6F² DRAM Cell Transistors: A Simulation Study Jaehyun Lee School of Electrical and Electronics Engineering, Pusan National University
TJ1-G-2 09:30-09:45	Effect of Metal-Capping Layer on Electrical Performances in a-IGZO TFTs: Experiment and TCAD Simulation Seongbin Lim ¹ , Soohyun Lim ¹ , Jaewook Yoo ¹ , Hyeonjun Song ¹ , Soyeon Kim ¹ , Hongseung Lee ¹ , Minah Park ¹ , Seohyeon Park ¹ , Sojin Jung ¹ , Jin-Ha Hwang ² , Kiyoung Lee ² , Sangmoon Yoon ³ , and Hagyoul Bae ¹ Jeonbuk National University, ² Hongik University, and ³ Gachon University
TJ1-G-3 09:45-10:00	Random Phase Distribution Effects of Ferroelectric Tunneling Field-Effect-Transistors (FeTFETs) Jiwon Park and Woo Young Choi Department Electrical and Computer Engineering and ISRC, Seoul National University
TJ1-G-4 10:00-10:15	Assessment of Logic Circuit Characteristics with Contact Resistance in 2D Material-Based Complementary-FETs (CFETs) Hanggyo Jung ¹ and Jongwook Jeon ² Kyungpook National University
TJ1-G-5 10:15-10:30	Comprehensive Analysis of Random Variability in Forksheet FETs Compared to Nanosheet FETs Minchan Kim, Seunghwan Lee, Junjong Lee, Sanguk Lee, Yonghwan Ahn, Gunryeol Cho, and Rock-Hyun Baek Department of Electrical Engineering, POSTECH
TJ1-G-6 10:30-10:45	A Novel Simulation Method of Harmonic Distortion of RF Substrates Using Ramo-Shockley Theorem Nakwon Yu, Jongmin Kim, Youngchul Kim, and Hyunchul Nah TE Team, DB HiTek