2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025-02-13(목), 09:00-10:45

좌장: 추후업데이트 예정

D. Thin Film Process Technology 분과

[TC1-D] Emerging Devices - I

초청 TC1-D-1 09:00-09:30	Atomic Layer Deposition of Chalcogenide Materials for Futuristic Memory Devices Chanyoung Yoo Hongik University
TC1-D-2 09:30-09:45	Self-rectifying RRAM with CMOS Compatible Materials So Jung Yoo and Gun Hwan Kim Department of System Semiconductor Engineering, Yonsei University
TC1-D-3 09:45-10:00	Self-rectifying Resistive Memory in 4K Passive Crossbar Arrays Ik Joon Seo and Gun Hwan Kim Department of System Semiconductor Engineering, Yonsei University
TC1-D-4 10:00-10:15	Thermal Perspective on the Impact of Operating Current in Selector-Only Memory Devices: A Study of Joule Heating Effect Ju Hwan Park ¹ , Hyun Wook Km ¹ , Se Hwan Jeon ¹ , Tae Jun Yang ¹ , Byung Joon Choi ¹ , Guesuk Lee ² , Sungsoon Choi ² , and Tae Hee Jung ² ¹ Seoul National University of Science and Technology, ² KETI
TC1-D-5 10:15-10:30	Selector-Only Memory with Switchable Threshold Voltage Behavior Hyun Kyu Seo, Jae Jun Lee, Min Kyung Lee, Se Yeon Jeong, and Min Kyu Yang Sahmyook University
TC1-D-6 10:30-10:45	The Effects of Indium Doping on the Structural and Electrical Properties of the Selector-Only-Memory (SOM) Device Won Hee Jeong and Gun Hwan Kim Department of System Semiconductor Engineering, Yonsei University