



## Future Normal in Semiconductor

2025-02-14(금), 09:00-10:45

좌장: 추후업데이트 예정

### H. Display and Imaging Technologies 분과

#### [FN1-H] Display and Imaging Technologies V

<p>초청 FN1-H-1 09:00-09:30</p>	<p>IT OLED향 채널 결정화를 통한 고성능, 고신뢰성 산화물 TFT 소자 개발 Development of High Performance, High Reliability Oxide TFTs for IT OLED 정재경 Department of Electronics Engineering, Hanyang University</p>
<p>FN1-H-2 09:30-09:45</p>	<p>A Novel Analysis Method and Mitigation Strategy for Hot Carrier Degradation in a-IGZO Transistor Changeon Jin<sup>1</sup>, Taewon Seo<sup>1</sup>, and Yoonyoung Chung<sup>1,2,3</sup> <sup>1</sup>Department of Electrical Engineering, POSTECH, <sup>2</sup>Department of Semiconductor Engineering, POSTECH, <sup>3</sup>Center for Semiconductor Technology Convergence, POSTECH</p>
<p>FN1-H-3 09:45-10:00</p>	<p>Contact Engineering Utilizing Ultrathin Dielectric Interlayer for Nanoscale IGZO Transistor without Short-Channel Effects Juyoung Yun<sup>1</sup>, Dowan Kang<sup>1</sup>, and Yoonyoung Chung<sup>1,2,3</sup> <sup>1</sup>Department of Electrical Engineering, POSTECH, <sup>2</sup>Department of Semiconductor Engineering, POSTECH, <sup>3</sup>Center for Semiconductor Technology Convergence, POSTECH</p>
<p>FN1-H-4 10:00-10:15</p>	<p>Reconfigurable Logic Gate enabled by Dual-Gating of Silicon Nanomembrane Field-Effect Transistors Changsoon Choi<sup>1</sup>, Joonha Hwang<sup>1,2</sup>, and Jong Ik Kwon<sup>1,3</sup> <sup>1</sup>KIST, <sup>2</sup>Korea University, <sup>3</sup>UNIST</p>
<p>FN1-H-5 10:15-10:30</p>	<p>Study on pixel design optimization for indirect time-of-flight sensor with high demodulation contrast and good depth accuracy Jaehyung Jang, Hoon-moo Choi, Jongchae Kim, Minseok Shin, Kyung-do Kim, Hoon-sang Oh, and Chang-rock Song CIS Development, SK hynix Inc</p>
<p>FN1-H-6 10:30-10:45</p>	<p>Demonstration of vertically stacked full-color micro-LED using CMOS compatible oxide wafer-bonding technology Hyun Soo Kim<sup>1</sup>, Juhyuk Park<sup>1</sup>, Woo Jin Baek<sup>1</sup>, Dae-Myeong Geum<sup>2</sup>, and SangHyeon Kim<sup>1</sup></p>



# 제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

## *Future Normal in Semiconductor*

---

<sup>1</sup>School of Electrical Engineering, KAIST, <sup>2</sup>School of Electronics Engineering, Inha University

---