



Future Normal in Semiconductor

2025-02-14(금), 15:10-17:10

좌장: 추후업데이트 예정

T. AI 분과

[FM3-T] Artificial Intelligence

<p>FM3-T-2 15:40-15:55</p>	<p>Analog Matrix-Vector Multiplication Accelerator using Capacitive Coupling-based Compute-In Memory Technology Jung Nam Kim¹, Yong Woo Kim¹, Minsuk Koo^{2,3}, and Yoon Kim^{1,3} ¹Department of Electrical and Computer Engineering, University of Seoul, ²School of Advanced Fusion Studies and AI Semiconductor, University of Seoul, ³IM Electronics Co., Ltd.</p>
<p>FM3-T-3 15:55-16:10</p>	<p>DRAM 기반 스토리지를 활용한 RAG 기반 LLM 추론 가속화 연구 KiHyun Kim¹, Jongman Kim², and Youngjae Kim¹ ¹Sogang University, ²Soteria Inc.</p>
<p>초청 FM3-T-4 16:10-16:40</p>	<p>Hardware-Algorithm Co-Design for Low-Power Deep Learning Training Processors Jeongwoo Park Department of Semiconductor Systems Engineering, Sungkyunkwan University</p>
<p>FM3-T-5 16:40-16:55</p>	<p>Analysis of Numeric Formats in Artificial Intelligence Balancing Accuracy and Resource Usage in Depth-Wise Convolutions Dayoung Lee, Jaeseong Kim, Chaebin Lee, Joungmin Park, Raehyeong Kim, and Seung Eun Lee Department of Electronic Engineering, Seoul National University of Science and Technology</p>
<p>FM3-T-6 16:55-17:10</p>	<p>Data-driven deep neural operators for solution of gas dynamic conservation equations in non-equilibrium plasma reactors. Sangjun Ahn, Jinkyu Bae, Suyoung Yoo, and Sang Ki Nam Core Technology R&D Team, Samsung Electronics Co., Ltd.</p>