제 32회 한국반도체학술대회
The 32nd Korean Conference on Semiconductors

2025년 **2월 12일(수)-14일(금)** | 강원도 하이원리조트

Future Normal in Semiconductor

2025-02-14(금), 15:10-17:10

좌장: 추후업데이트 예정

O. System LSI Design 분과

[FK3-O] System LSI Design

초청 FK3-O-1 15:10-15:40	Neural Network Data Compression Method Using Huffman Coding and High-Throughput Decompression Hardware Architecture Injae Yoo Pusan National University
FK3-O-2 15:40-15:55	FPGA 기반 초기 지연 가변 TDC 전압 센서 구현 노윤진, 엄유진, 양희훈, 유호영 충남대학교 전자공학과
FK3-O-3 15:55-16:10	Xilinx FPGA를 활용한 FPGA-PC Ethernet 시스템 구현 박지호, 손정훈, 유호영 충남대학교 전자공학과
초청 FK3-O-4 16:10-16:40	Recent Advances in VLSI Architectures for IDMA Systems Byeong Yong Kong Kongju National University
FK3-O-5 16:40-16:55	Design of a High-Speed Face Detection Circuit for Always-on Image Sensors Yuchan Yun, Soo Youn Kim Department of System Semiconductor, Dongguk University
FK3-O-6 16:55-17:10	Delta Rule-based Weight Calibration Method for Low Power SNN System Seungjoon Lee and Soo Youn Kim Department of System Semiconductor, Dongguk University