제 32회 한국반도체학술대회
The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025-02-14(금), 10:55-12:40
좌장: 추후업데이트 예정

N. VLSI CAD 분과

[FI2-N] Pioneering VLSI CAD: from AI to Quantum

초청 FI2-N-1 10:55-11:25	**Standard Cells, Simple yet Powerful Design Enablements** Suwan Kim Samsung Advanced Institute of Technology
FI2-N-2 11:25-11:40	**Machine Learning Platform for a Complete LC-VCO Design with Physical Layout** Hyunsoo Lee[1], Sungjin Kim[2], Taigon Song[1], and Heein Yoon [1]Kyungpook National University, [2]UNIST
FI2-N-3 11:40-11:55	**회로 유사도 기반 논리 합성 툴 파라미터 최저화** 이준빈, 안진일, 현대준 Sejong University
FI2-N-4 11:55-12:10	**Realistic Test Case Generation for Power State Coverage** Yunseok Jung, Hyeonwoo Park, and Seokhyeong Kang Department of Electrical Engineering, POSTECH
FI2-N-5 12:10-12:25	**Design Method for Multi-Digit Converters Between Binary and Ternary Logic** Seunghyun Son[1], Seunghan Baek[2], and Sunmean Kim[1] [1]School of Electronic and Electrical Engineering, Kyungpook National University, [2]Department of Electrical Engineering, POSTECH
FI2-N-6 12:25-12:40	**효율적인 Surface Code 통신을 위한 타일 아키텍처** 강영중[1,3], 양준성[1,2] [1]연세대학교 시스템반도체공학과, [2]연세대학교 전기전자공학과, [3]연세대학교 지능형반도체 IT 융합전공