



Future Normal in Semiconductor

2025-02-14(금), 15:10-17:10

좌장: 추후업데이트 예정

D. Thin Film Process Technology 분과

[FE3-D] TFT/Memory

FE3-D-1 15:10-15:25	Composition-Dependent Threshold Voltage Variation in GeSeTe-Based Selector-Only Memory Device Young Yun Mun and Gun Hwan Kim Department of system semiconductor engineering, Yonsei University
FE3-D-2 15:25-15:40	Exploring TiO₂ Interlayer Impact on H_xZ_{1-x}O₂ Ferroelectric Stability and Performance in Cryogenic Memory Applications So-Yeong Park, Woon-San Ko, Do-Yeon Lee, So-Yeon Kwon, Hye-Ri Hong, Seong-Jo Jo, and Ga-Won Lee Department of Electronics Engineering, Chungnam National University
FE3-D-3 15:40-15:55	A New Structure for High-Performance Operation of Oxide and Organic Semiconductor Heterojunction Transistors: Spatially Separating Layer Sandwiched Anti-Ambipolar Transistor Youngmin Han ¹ and Hocheon Yoo ^{1,2} ¹ Department of Semiconductor Engineering, Gachon University, ² Department of Electronic Engineering, Gachon University
FE3-D-4 15:55-16:10	Study on the Enhancement of HfO₂ Dielectric Properties using SiCN Capping Layer Hye-Ri Hong, Woon-San Ko, Do-Yeon Lee, So-Yeon Kwon, So-Yeong Park, Seong-Jo Jo, and Ga-Won Lee Chungnam National University
FE3-D-5 16:10-16:25	Determination of Subgap DOS over the Wide Energy Range using Multi-Wavelength Light in PEALD Cu₂O TFTs Soyeon Kim ¹ , Jaewook Yoo ¹ , Seongbin Lim ¹ , Hyeonjun Song ¹ , Hongseung Lee ¹ , Seohyeon Park ¹ , Minah Park ¹ , Sojin Jeong ¹ , Peide D. Ye ² , and Hagyoul Bae ¹ ¹ Jeonbuk National University, ² Purdue University
FE3-D-6 16:25-16:40	Multimode Operation of Light-Gated Transistors Based on Millimeter-Scale Transition-Metal Dichalcogenide Grown by Chemical Vapor Deposition Wonbeom Kim ¹ , Somnath S. Kundale ³ , Hyeongtae Kim ¹ , Su-Yeon Cho ¹ , Mi Ji Kwon ¹ , Soobin Shim ¹ , and Jun Hong Park ^{1,2}



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<p>FE3-D-7 16:40-16:55</p>	<p>Defect-selective Platinum Nanoparticle Functionalization of 2D WS₂ by Atomic Layer Deposition for Enhancing Gas Sensing Properties</p> <p>Minji Kim, Inkyu Sohn, Dain Shin, Jaehyeok Kim, Tatsuya Nakazawa, Seung-min Chung and Hyungjun Kim</p> <p>School of Electrical and Electronic Engineering, Yonsei University</p>
<p>FE3-D-8 16:55-17:10</p>	<p>High-Performance p-Type Tin Halide Perovskite Transistor with Non-Volatile Methylammonium Chloride</p> <p>Hansol Park^{1,2}, Jongmin Lee^{1,2}, Cheong Beom Lee³, Kyeounghak Kim⁴, and Hui Joon Park^{1,2,5}</p> <p>¹Department of Organic and Nano Engineering, Hanyang University, ²Human-Tech Convergence Program, Hanyang University, ³Department of Chemistry, Hanyang University, ⁴Department of Chemical Engineering, Hanyang University, ⁵Department of Semiconductor Engineering, Hanyang University</p>