



제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025년 2월 13일(목), 15:50-17:20

Room M(다이아몬드 I), 6층

L. Analog Design 분과

039_[TM3-L] Analog Circuits

좌장: 엄지용 교수(금오공과대학교), 정영호 교수(대구대학교)

초청 TM3-L-1 15:50-16:20	Low-Power Voltage References Youngwoo Ji Hanbat National University
TM3-L-2 16:20-16:35	A 2×48 Gb/s Single-Ended PAM-3 Transmitter with Crosstalk Cancellation Technique Separated from Feed-Forward Equalizer Junsu Park, Dongwoo Kang, and Kwanso Park Yonsei University
TM3-L-3 16:35-16:50	A Low-Jitter Injection-Locked CDR with Injection Rate Control Junhak Kim, Youngwook Kim, and Kwanso Park Yonsei University
TM3-L-4 16:50-17:05	A 32-Gb/s 0.52-pJ/b Sub-Baud-Rate Receiver with 1-tap DFE Ho-Joon Shin, Suil Kang, Yoojin Jung, and Kwanso Park Yonsei University
TM3-L-5 17:05-17:20	A 12-Bit 500-MS/S Ring amp-Based Fully-Differential Pipelined SAR ADC With Dynamic Body Biasing Jisu Kim ¹ , Taeho Lee ² , and Jun-Eun Park ¹ ¹ Department of Electrical and Computer Engineering Sungkyunkwan University, ² Department of Semiconductor Convergence Engineering Sungkyunkwan University