



Future Normal in Semiconductor

2025년 2월 13일(목), 15:50-17:20

Room K(하트 I), 6층

J. Nano-Science & Technology 분과

037_[TK3-J] Emerging Nanomaterials & Applications

좌장: 김철주 교수(POSTECH), 강기훈 교수(서울대학교)

<p>TK3-J-1 15:50-16:05</p>	<p>Mitigating Substrate Effects of van der Waals Semiconductors using Perfluoropolyether Self-Assembled Monolayers Dae Young Park¹, Hyeong Chan Suh¹, Seungho Bang¹, Ju Chan Lee², Jaekak Yoo¹, Hayoung Ko², Soo Ho Choi², Ki Kang Kim², Seung Mi Lee³, Seong Chu Lim², Tschang Uh Nahm¹, and Mun Seok Jeong¹ ¹Department of Physics, Hanyang University, ²Department of Energy Science, Sungkyunkwan University, ³KRISS</p>
<p>TK3-J-2 16:05-16:20</p>	<p>Facile Repairing Process of Sulfur Vacancy on MoS₂ Surface with Fluorinated Benzenethiol Mingyu Kim¹, Haksoon Jung¹, Yongwoo Lee², Taoyu Zou¹, Jimin Kwon², and Yong-Young Noh¹ ¹Department of Chemical Engineering, POSTECH, ²Department of Electrical Engineering, UNIST</p>
<p>TK3-J-3 16:20-16:35</p>	<p>Exploring the Piezoresistive Pressure Sensing Range in MOCVD-Grown Hierarchical Three-Dimensional MoS₂ Jeongin Song^{1,2}, Se Min Hwang^{1,3}, Min Sup Choi³, Sang Woo Kang^{1,4}, Taesung Kim², and Jihun Mun¹ ¹KRISS, ²Sungkyunkwan University, ³Chungnam National University, ⁴University of Science and Technology</p>
<p>TK3-J-4 16:35-16:50</p>	<p>Influence of Grain size on Amplified Spontaneous Emission in Metal Halide Perovskite Hyeree Kim¹, Seung-Je Woo², Kyung Yeon Jang¹, and Tae-Woo Lee^{1,3} ¹Department of Material Science and Engineering, Seoul National University, ²Cavendish Laboratory, University of Cambridge, UK, ³Research Institute of Advanced Materials, Institute of Engineering Research, Soft Foundary, Interdisciplinary Program in Bioengineering, SN Display Co. Ltd, Seoul National University</p>



제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

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<p>TK3-J-5 16:50-17:05</p>	<p>Enhanced Gating Efficiency in Vertical Mixed Molecular Transistors with Deep Orbital Level Donguk Kim, Hyemin Lee, Minwoo Song, Jongwoo Nam, and Takhee Lee Department of Physics and Astronomy, Seoul National University</p>
<p>TK3-J-6 17:05-17:20</p>	<p>Development of CMOS-Integrated TiO_x Memristor Array for In-Memory Computing Yeon Seo An¹, Dowon Kim³, Young Ran Park¹, Jung Sun Eo¹, Mingyu Kim¹, Donghyeok Kim¹, Hyeon Bin Kim¹, Byungeun Lee³, and Gunuk Wang^{1,2} ¹KU-KIST Graduate School of Converging Science and Technology, ²Department of Integrative Energy Engineering, Korea University, ³GIST</p>