



Future Normal in Semiconductor

2025년 2월 13일(목), 10:55-12:40

Room K(하트 I), 6층

K. Memory (Design & Process Technology) 분과

023_[TK2-K] Embedded Memory and Computing-in-Memory Application

좌장: 권용우 교수(홍익대학교), 김수길 연구위원 (SK하이닉스)

<p>초청 TK2-K-1 10:55-11:25</p>	<p>State-of-the-Art of High-Density Cross-Point STT-MRAM Technology Soo Man Seo, Soo Gil Kim, Jae Yun Yi, and Seon Yong Cha RnD division, SK hynix Inc.</p>
<p>TK2-K-2 11:25-11:40</p>	<p>Embedded RRAM Technology: An Extremely Cost-Effective eNVM Solution with Full CMOS Logic Compatibility Sooan Kim, Jaehun Lee, Nayan Chandra Das, Yongseok Chung, Sungchan Lim, Hwanho Ma, Youngdong Kim, Daeyun Kang, Kyongsik Yeom, Changmin Jeon, and Kangho Lee Foundry Business, Samsung Electronics Co., Ltd.</p>
<p>TK2-K-3 11:40-11:55</p>	<p>Revealing the Switching Mechanism of ECRAM through an Independently-Contacted WOx Double Layer Seungmin Han, Hyunjeong Kwak, Jinho Byun, Jeonghoon Son, Seungkun Kim, and Seyoung Kim Department of Materials Science and Engineering, POSTECH</p>
<p>TK2-K-4 11:55-12:10</p>	<p>In-Memory Parallel Computing with Variation Tolerant Memristive Majority Logic in a Crossbar Array Moon Gu Choi and Kyung Min Kim Department of Materials Science and Engineering, KAIST</p>
<p>TK2-K-5 12:10-12:25</p>	<p>Experimental Implementation of Programmable Threshold Logic in a Memristor Crossbar Array Sangwook Youn, Jinwoo Park, and Hyungjin Kim Division of Materials Science and Engineering and Department of Semiconductor Engineering, Hanyang University</p>
<p>TK2-K-6 12:25-12:40</p>	<p>Demonstration of Convolution Kernel Operation using Memristor Crossbar Array with Quantized Weights and Binary Activation Jinwoo Park, Sangwook Youn, and Hyungjin Kim Division of Materials Science and Engineering and Department of Semiconductor Engineering, Hanyang University</p>