2025년 2월 13일(목), 09:00-10:45
Room K(하트 I), 6층

G. Device & Process Modeling, Simulation and Reliability 분과

O11_[TK1-G] Device Characterization & Modeling 1

좌장: 이재우 교수(고려대학교), 김세영 교수(포항공과대학교)

초청 TK1-G-1 09:00-09:30	**Multiscale Investigation for Semiconductor Process Design with Computational Science and Artificial Intelligence** Byungjo Kim UNIST
TK1-G-2 09:30-09:45	**Ballistic Transport in State-of-the Art $In_{0.65}Ga_{0.35}As/In_{0.52}Al_{0.48}As$ Quantum-Well HEMTs at Room and Cryogenic Temperatures** Seung-Woo Son, In-Geun Lee, Min-Seo Yu, Su-Min Choi, Yong-Soo Jeon, Sang-Pyeong Son, Ji-Hoon Yoo, Sang-Ki Yun, Jae-Hak Lee, and Dae-Hyun Kim School of Electronic and Electrical Engineering Kyungpook National University
TK1-G-3 09:45-10:00	**Design Optimization of Capacitor-Based Synaptic Cells for Efficient Analog Neural Network Training** Byoungwoo Lee, Wonjae Ji, Hyejin Kim, Seungmin Han, Junyoung Choi, and Seyoung Kim Department of Material Science and Engineering, POSTECH
TK1-G-4 10:00-10:15	**Exploring the Channel Thickness Effect on Carrier Transport Mechanism of Schottky Contacts in Ultrathin a-IGZO TFTs** Hongseung Lee[1], Jaewook Yoo[1], YuJun Roh[1], Hyeonjun Song[1], Soyeon Kim[1], Seongbin Lim[1], Seohyeon Park[1], Minah Park[1], Sojin Jung[1], Jin-Ha Hwang[2], Kiyoung Lee[2], and Hagyoul Bae[1] [1]Jeonbuk National University, [2]Hongik University
TK1-G-5 10:15-10:30	**Exploring the Deuterium Annealing Effect on Persistent Photoconductivity Related to Subgap DOS in IGZO TFTs** Hyeonjun Song[1], Jaewook Yoo[1], Soyeon Kim[1], Hongseung Lee[1], Seongbin Lim[1], Minah Park[1], Seohyeon Park[1], Sojin Jung[1], Jun-Young Park[2], Yoon Kyeung Lee[1], Kiyoung Lee[3], and Hagyoul Bae[1] [1]Jeonbuk National University, [2]Chungbuk National University, [3]Hongik University

TK1-G-6 10:30-10:45	**Analysis on Effect of Proton Irradiation on Schottky-Barrier a-IGZO TFTs using TCAD Simulation** Eunchong Kim[1], Hyunwook Jeong[1], Yubin Choi[1], Junseong Park[1], Haesung Kim[1], Hyojin Yang[1], Sung-Jin Choi[1], Dae Hwan Kim[1], Dong Myong Kim[2], Sung Yun Woo[3], and Jong-Ho Bae[1] [1]School of the Electronic Engineering, Kookmin University, [2]Department of Advanced Technology, DGIST