

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025년 2월 13일(목), 10:55-12:40

Room J(스페이드 II+III), 6층

K. Memory (Design & Process Technology) 분과

O22_[TJ2-K] Neuromorphic Application

좌장: 정연주 박사(KIST), 곽준영 교수(이화여대)

초청 TJ2-K-1 10:55-11:25	Opportunities and Challenges for Analog Computation in Memory in the AI Era 박상수, 최혜정, 김수길, 이재연, 차선용 RnD division, SK hynix Inc.
TJ2-K-2 11:25-11:40	Development of the Neural Network SPICE Integration Platform (NSIP) Using FeFET-Based Crossbar Arrays Huijun Kim ¹ , Juhwan Park ² , Changho Ra ² , and Jongwook Jeon ² ¹ Department of Semiconductor Convergence Engineering, Sungkyunkwan University, ² Department of Electrical and Computer Engineering, Sungkyunkwan University
TJ2-K-3 11:40-11:55	Novel Data-Driven Compact Modeling for RRAM Using Deep Neural Networks Ye Sle Cha ¹ , Premkumar Vincent ¹ , Juhwan Park ² , Hyunseok Whang ¹ , Jongwook Jeon ² , and Hyunbo Cho ¹ ¹ Research & Development Center, Alsemey Inc., ² Sungkyunkwan University
TJ2-K-4 11:55-12:10	Solving Max-Cut Problem Using Spiking Boltzmann Machine Based on Neuromorphic Hardware with Phase Change Memory Yu Gyeong Kang, Jaeweon Park, and Sangbum Kim Seoul National University
TJ2-K-5 12:10-12:25	IGZO and Pt-based Floating Gate Memory for Synaptic Device with Implementing Various Voltage Schemes Heerak Wi ¹ , Eunpyo Park ² , Ria Choi ¹ , Dae Kyu Lee ² , Min Jee Kim ¹ , and Joon Young Kwak ¹ ¹ Ewha Womans University, ² KIST
TJ2-K-6 12:25-12:40	Reconfigurable Neuromorphic Device by 2D Semiconductor-Metal Interfacial Engineering for AI Hardware Hyunho Seok ¹ , Sihoon Son ¹ , Dongyoung Lee ¹ , Hyunbin Choi ¹ , Jinhyoung Lee ¹ , Seyong Oh ² , Jin-Hong Park ¹ , and Taesung Kim ¹ ¹ Sungkyunkwan University, ² Hanyang University