



제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025년 2월 13일(목), 10:55-12:40

Room G(사파이어 II+III), 5층

J. Nano-Science & Technology 분과

019_[TG2-J] Emerging Nanomaterial Electronics

좌장: 김태욱 교수(전북대학교), 이관형 교수(서울대학교)

<p>초청 TG2-J-1 10:55-11:25</p>	<p>Single Element, Multiple Functions: Te-based Nanoelectronics Joonki Suh Department of Materials Science and Engineering & Graduate School of Semiconductor Materials and Devices Engineering, UNIST</p>
<p>TG2-J-2 11:25-11:40</p>	<p>A Ternary Inverter based on 2D Tellurium/IGZO Heterojunction FET with Mxene H. J. Park¹, J. Jang², H. Kim³, J. P. Hong¹, H. Oh³, and M. S. Jeong¹ ¹Department of Physics, Hanyang University, ²Department Of Energy Science, Sungkyunkwan University, ³Department of Physics, Soongsil University</p>
<p>TG2-J-3 11:40-11:55</p>	<p>Negative Differential Transconductance in CMOS-Integrable 2D Ambipolar Tellurene Transistor for Multi-Valued Logic Computing Jihoon Huh^{1,2}, Bolim You^{1,2}, Yuna Kim¹, Mino Yang³, Unjeong Kim⁴, Min-Kyu Joo^{5,6}, Myung Gwan Hahm^{1,7}, and Moonsang Lee^{1,2} ¹Inha University, ²Program in Semiconductor Convergence, Inha University, ³KBSI, ⁴Department of physics, Dongguk university, ⁵Department of Applied Physics, Sookmyung Women's University, ⁶Institute of Advanced Materials and Systems, Sookmyung Women's University, ⁷Institute for Bio-Medical and Translational Health Care, Inha University Hospital</p>
<p>TG2-J-4 11:55-12:10</p>	<p>2-Dimensional InSe Ambipolar Transistors and Their Conversion to Reconfigurable Electronics Dongju Yeom¹, Minsu Kim¹, Yong wook Seok¹, Jungi Song¹, Hanbyeol Jang², YiTaek Choi², Yeonghyeon Ko¹, and Kayoung Lee¹ ¹School of Electrical Engineering, KAIST, ²School of Materials Science and Engineering, GIST</p>
<p>TG2-J-5 12:10-12:25</p>	<p>Bundle-Free Aligned Semiconductor Carbon Nanotubes for Field-Effect Transistors Yehyun Shin¹, Sungmin Eum¹, Haksoon Jung², and Jimin Kwon^{1,2} ¹Graduate School of Semiconductor Materials and Devices Engineering, UNIST, ²Department of Electrical Engineering, UNIST</p>



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TG2-J-6 12:25-12:40	<p>Fabrication of Nanoscale Underlap Carbon Nanotube Field-Effect Transistors on 8-inch Wafer with Enhanced Electrical Performance</p> <p>Jun-Ho Jang¹, Hanbin Lee¹, Gyeongsu Min¹, Hyo-In Yang¹, So-Jeong Park¹, Jeong Yeon Im¹, Ji Won Park¹, Seonghyeon Jeong¹, Dae Hwan Kim¹, Jong-Ho Bae¹, Min-Ho Kang², Dong Myong Kim³, and Sung-Jin Choi¹</p> <p>¹School of Electrical Engineering, Kookmin University, ²Department of Nano-process, National Nanofab Center (NNFC), ³Department of Advanced Technology, DGIST</p>
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