2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025년 2월 14일(금), 09:00-10:45 Room N(다이아몬드 II), 6층

H. Display and Imaging Technologies 분과

O54_[FN1-H] Display and Imaging Technologies V

좌장: 최창순 박사(KIST), 김혁 교수(서울시립대)

초청 FN1-H-1 09:00-09:30	IT OLED향 채널 결정화를 통한 고성능, 고신뢰성 산화물 TFT 소자 개발 Development of High Performance, High Reliability Oxide TFTs for IT OLED 정재경 Department of Electronics Engineering, Hanyang University
FN1-H-2 09:30-09:45	A Novel Analysis Method and Mitigation Strategy for Hot Carrier Degradation in a-IGZO Transistor Changeon Jin ¹ , Taewon Seo ¹ , and Yoonyoung Chung ^{1,2,3} ¹Department of Electrical Engineering, POSTECH, ²Department of Semiconductor Engineering, POSTECH, ³Center for Semiconductor Technology Convergence, POSTECH
FN1-H-3 09:45-10:00	Contact Engineering Utilizing Ultrathin Dielectric Interlayer for Nanoscale IGZO Transistor without Short-Channel Effects Juyoung Yun ¹ , Dowan Kang ¹ , and Yoonyoung Chung ^{1,2,3} ¹Department of Electrical Engineering, POSTECH, ²Department of Semiconductor Engineering, POSTECH, ³Center for Semiconductor Technology Convergence, POSTECH
FN1-H-4 10:00-10:15	Reconfigurable Logic Gate Enabled by Dual-Gating of Silicon Nanomembrane Field-Effect Transistors Changsoon Choi ¹ , Joonha Hwang ^{1,2} , and Jong Ik Kwon ^{1,3} ¹ KIST, ² Korea University, ³ UNIST
FN1-H-5 10:15-10:30	Study on Pixel Design Optimization for Indirect Time-of-Flight Sensor with High Demodulation Contrast and Good Depth Accuracy Jaehyung Jang, Hoon-moo Choi, Jongchae Kim, Minseok Shin, Kyung-do Kim, Hoonsang Oh, and Chang-rock Song CIS Development, SK hynix Inc.

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FN1-H-6 10:30-10:45	Demonstration of Vertically Stacked Full-Color Micro-LED Using CMOS
	Compatible Oxide Wafer-bonding Technology
	Hyun Soo Kim ¹ , Juhyuk Park ¹ , Woo Jin Baek ¹ , Dae-Myeong Geum ² , and SangHyeon
	Kim ¹
	¹ School of Electrical Engineering, KAIST, ² School of Electronics Engineering, Inha University