



# 제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

## Future Normal in Semiconductor

2025년 2월 13일(목), 09:00-10:45

Room L(하트II+III), 6층

### E. Compound Semiconductors 분과

#### 052\_[FL1-E] III-V Semiconductor

좌장: 신승헌 교수(순천향대학교), 심재필 박사(한국나노기술원)

<p><b>초청</b> FL1-E-1 09:00-09:30</p>	<p><b>Accurate Measurement of Wide Bandgap Power Devices' Switching</b> Hyemin Kang Department of Energy Engineering, KENTECH</p>
<p>FL1-E-2 09:30-09:45</p>	<p><b><math>L_g = 50</math> nm <math>\text{In}_{0.65}\text{Ga}_{0.35}\text{As}</math> HEMTs with Record Noise-Figure Performance</b> Min-Seo Yu<sup>1</sup>, Seung-Woo Son<sup>1</sup>, Sang-Ki Yun<sup>1</sup>, Sang-Pyeong Son<sup>1</sup>, Ji-Hoon Yoo<sup>1</sup>, Kyounghoon Yang<sup>2</sup>, Jae-Hak Lee<sup>1</sup>, and Dae-Hyun Kim<sup>1</sup> <sup>1</sup>School of Electronic and Electrical Engineering, Kyungpook National University, <sup>2</sup>KAIST</p>
<p>FL1-E-3 09:45-10:00</p>	<p><b>M3D InGaAs MOSHEMT on Si Substrate with Low Gate Leakage Current</b> Yoon-Je Suh, Jaeyong Jeong, Nahyun Rheem, Chan Jik Lee, and Sanghyeon Kim KAIST</p>
<p>FL1-E-4 10:00-10:15</p>	<p><b><math>L_g = 50</math> nm Gate-All-Around <math>\text{In}_{0.53}\text{Ga}_{0.47}\text{As}</math> Nanosheet MOSFETs</b> J.-H. Yoo<sup>1</sup>, H.-B. Jo<sup>1, 2</sup>, I.-G. Lee<sup>1</sup>, S.-M. Choi<sup>1</sup>, H.-J. Kim<sup>1</sup>, W.-S. Park<sup>1</sup>, H. Jang<sup>3</sup>, C.-S. Shin<sup>3</sup>, K.-S. Seo<sup>3</sup>, SK. Kim<sup>4</sup>, JG. Kim<sup>4</sup>, J. Yun<sup>4</sup>, T. Kim<sup>4</sup>, J.-H. Lee<sup>1</sup>, and D.-H. Kim<sup>1</sup> <sup>1</sup>Kyungpook National University, <sup>2</sup>KETI, <sup>3</sup>KANC, <sup>4</sup>QSI</p>
<p>FL1-E-5 10:15-10:30</p>	<p><b>Study of 1.65 eV <math>\text{Al}_{0.18}\text{Ga}_{0.82}\text{As}</math> Tunnel Junction with Hybrid Delta-Doped n-GaAs Quantum Well for Monolithic III-V/Si Tandem Solar Cells</b> Eunkyo Ju<sup>1,2</sup>, May Angelu Madarang<sup>1,3</sup>, Yeonhwa Kim<sup>1,2</sup>, Rafael Jumar Chu<sup>1,3</sup>, Tsimafei Laryn<sup>1,3</sup>, In-Hwan Lee<sup>2</sup>, Won Jun Choi<sup>1</sup>, and Daehwan Jung<sup>1,3</sup> <sup>1</sup>Center for Quantum Technology, KIST, <sup>2</sup>Department of Materials Science and Engineering, Korea University, <sup>3</sup>Division of Nanoscience and Technology, University of Science and Technology</p>



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FL1-E-6 10:30-10:45	Integration of InGaAs/InP Heterojunction Phototransistor on Si CMOS Platform by Wafer Bonding Kyunghwan Kim, Dae-Hwan Ahn, and Jae-Hoon Han Center for Quantum Technology, KIST
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