2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

Future Normal in Semiconductor

2025년 2월 14일(금), 09:00-10:45 Room K(하트 I), 6층

G. Device & Process Modeling, Simulation and Reliability 분과 O51_[FK1-G] Reliability & Packaging Simulation

좌장: 신홍식 수석(DB하이텍), 우지용 교수(경북대학교)

	Insertion of Protective Layer for Enhancing Effect of N ₂ O Plasma
FK1-G-1 09:00-09:15	Treatment on NBS Stability in IGZO TFTs
	Seungyoon Shin, Jinkyu Lee, Hyunjin Choi, Seong-In Cho, and Soo-Yeon Lee
	Department of Electrical and Computer Engineering, Inter-university Semiconductor
	Research Center, Seoul National University
	Degradation Modeling of Positive Bias Stress Reliability in a-IGZO TFTs
FK1-G-2 09:15-09:30	Considering the Effects of Oxygen and Hydrogen
	Do Hun Kim ¹ , Seung Joo Myoung ¹ , Dong Hyeop Shin ¹ , Jung Rae Cho ¹ , Donguk Kim ¹ ,
	Changwook Kim ¹ , Narae Han ² , Jee-Eun Yang ² , Younjin Jang ² , Sangwook Kim ² , and Dae Hwan Kim ¹
	¹ School of Electrical Engineering, Kookmin University, ² Samsung Advanced Institute of
	Technology (SAIT), Samsung Electronics Co., Ltd.
FK1-G-3 09:30-09:45	Comprehensive Analysis of Proton Collision Effects in SOI MOSFETs using
	Transient and Steady-State Responses
	Hwan Jin Kim ¹ , Haesung Kim ¹ , Hyojin Yang ¹ , Yubin Choi ¹ , Sujong Kim ¹ , Hyunwook
	Jeong ¹ , Sung-Jin Choi ¹ , Dae Hwan Kim ¹ , Dong Myong Kim ² , Sung Yun Woo ³ , and Jong-Ho Bae ¹
	¹ School of the Electronic Engineering, Kookmin University, ² Department of Advanced
	Technology, DGIST, ³ School of Electronic and Electrical Engineering, Kyungpook National
	University
	Uncovering Novel Time Exponent Variations in PBTI of a-IGZO Transistors
	via a 1 µs Ultrafast On-the-Fly Technique
FK1-G-4	Sangwook Jung ¹ , Taewon Seo ² , Changeon Jin ² , and Yoonyoung Chung ^{1,2,3,4}
09:45-10:00	¹ Graduate School of Semiconductor Technology, POSTECH, ² Department of Electrical
	Engineering, POSTECH, ³ Department of Semiconductor Engineering, POSTECH, ⁴ Center
	for Semiconductor Technology Convergence, POSTECH



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	Analysis of Proton-Induced Electrical Degradation in a-IGZO TFTs under
	Aerospace Environments
51/4 O 5	Yubin Choi ¹ , Haesung Kim ¹ , Hyojin Yang ¹ , Junseong Park ¹ , Hyunwook Jeong ¹ , Sujong
FK1-G-5	Kim ¹ , Hwan Jin Kim ¹ , Sung-Jin Choi ¹ , Dae Hwan Kim ¹ , Dong Myong Kim ² , Sung Yun
10:00-10:15	Woo ³ , and Jong-Ho Bae ¹
	¹ School of the Electronic Engineering, Kookmin University, ² Department of Advanced
	Technology, DGIST, ³ School of Electronic and Electrical Engineering, Kyungpook National
	University
	Data-driven Method for Predicting Thermo-mechanical Property Maps of
FK1-G-6	Patterned Semiconductor Packages Using Machine Learning
10:15-10:30	Jeong-Hyeon Park, Sung Jun Kang, and Eun-Ho Lee
	Sungkyunkwan University
	Development of an In-House Finite Element Method Simulator for Static
FK1-G-7	Structural Analysis of a Warped Wafer
10:30-10:45	Sung-Min Hong
	School of Electrical Engineering and Computer Science, GIST