



## Future Normal in Semiconductor

2025년 2월 14일(금), 15:10-17:10

Room H(루비 I), 5층

Q. Metrology, Inspection, Analysis, and Yield Enhancement 분과

### 074\_[FH3-Q] Metrology, Inspection, Analysis, and Yield Enhancement II

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<p>초청 FH3-Q-1 15:10-15:40</p>	<p><b>A Study on Improving Accuracy of Optical 3-D Profile Measurement Using in-Wafer Gradient of Dimensionally Reduced Ellipsometry Spectrum</b> Hyeong-Jun Jeong<sup>1</sup>, Minkyu Kim<sup>2</sup>, Sungyoon Ryu<sup>1</sup>, Taejin Kim<sup>1</sup>, Gwanghun Jung<sup>1</sup>, Young-Seok Kim<sup>1</sup>, and Younghoon Sohn<sup>1</sup> <sup>1</sup>Metrology &amp; Inspection Technology Team, Samsung Electronics, Ltd., <sup>2</sup>CSE Team, Samsung Electronics Ltd</p>
<p>초청 FH3-Q-2 15:40-16:10</p>	<p><b>Matching Technology of Metrology Tools for FAB Process</b> Yong Woo Jung, Yu Seong Gim, and Hee Ju Park SK hynix</p>
<p>초청 FH3-Q-3 16:10-16:40</p>	<p><b>Development of Various EUV Sources for Application in Actinic Tools for EUV Masks</b> Dong Gun Lee ESOL. Inc.</p>
<p>FH3-Q-4 16:40-16:55</p>	<p><b>Development of Stack Die MI Measurement Methodology for the Unrivaled HBM Memory</b> Hye Yun Seong, Young Hoon Lee, and Sung Hyun Yoon SK hynix Inc.</p>
<p>FH3-Q-5 16:55-17:10</p>	<p><b>The Industry's First Study on Inline Mass Measurement Methods for Chip-Level Warpage to Yield Improvement of HBM</b> Sang-Yeop Lee<sup>1</sup>, Sang yup Lee<sup>2</sup>, In Tae Whoang<sup>1</sup>, Jung Hwi Kim<sup>1</sup>, Min Yeop Lee<sup>1</sup>, and Young Hoon Lee<sup>1</sup> <sup>1</sup>SK Hynix Inc., <sup>2</sup>Georgia Institute of Technology</p>