



Future Normal in Semiconductor

2025년 2월 14일(금), 10:55-12:40

Room F(사파이어 I), 5층

C. Material Growth & Characterization 분과

O58_[FF2-C] Advanced Devices in Oxide Heterostructures

좌장: 이정우 교수(홍익대학교), 엄기태 교수(가천대학교)

<p>초청 FF2-C-1 10:55-11:25</p>	<p>Spintronic Security Devices based on Magnetic Random -Access Memory Soogil Lee¹, Jaimin Kang², and Byong-Guk Park² ¹Department of Semiconductor Engineering, Gachon University, ²Department of Materials Science and Engineering, KAIST</p>
<p>FF2-C-2 11:25-11:40</p>	<p>Depletion-mode and Enhancement-mode Diamond MOSFETs Simultaneously Fabricated on Heteroepitaxial Diamond Substrates Taemyung Kwak¹, Yoonsek Nam¹, Yeonghwa Kwon¹, Geunho Yoo¹, Seong-woo Kim², and Okhyun Nam¹ ¹Tech University of Korea, ²Orbray Co., Ltd, Japan</p>
<p>FF2-C-3 11:40-11:55</p>	<p>Monolithic Integration of Quantum Dot Light Emitting Diodes on In_{0.1}Ga_{0.9}As/AlAs Distributed Bragg Reflectors on Si Tsimafei Laryn^{1,2}, Yeonhwa Kim^{1,3}, Eunkyo Ju^{1,3}, Won Jun Choi¹, and Daehwan Jung¹ ¹Center for Quantum Technology, KIST, ²Division of Nanoscience and Technology, KIST School at University of Science and Technology, ³Department of Materials Science and Engineering, Korea University</p>
<p>FF2-C-4 11:55-12:10</p>	<p>Monolithic Integration of Freestanding Ferroelectric Oxide Membranes for Electrically Tunable Energy Storage Performance Min-Seok Kim^{1,2}, Ho Won Jang², Tae Heon Kim¹, and Seung-Hyub Baek¹ ¹KIST, ²Seoul National University</p>
<p>FF2-C-5 12:10-12:25</p>	<p>Wafer-scale Single-crystal Hexagonal Boron Nitride Integrated Hybrid Dielectric for Two-dimensional Transistor Arrays Jaewon Wang¹, Hyeonwoo Lee¹, Jaemin Kim^{1,2}, Haeng Un Yeo¹, Cheol Hwan Yoon¹, Min Seok Yoo³, Junseop Noh¹, Kitae Park¹, Joonki Suh¹, Tae-Sik Yoon¹, Minsu Seol³, Chanyong Hwang⁴, Hyung-Joon Shin^{1,2}, Zonghoon Lee^{1,2}, Changwook Jeong¹, and Soon-Yong Kwon¹ ¹UNIST, ²CMCM, IBS, ³2D Device Laboratory, SAIT, ⁴Quantum Technology Institute, KRISS</p>



제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

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FF2-C-6 12:25-12:40	High-performance Flash Memory with 1T-MoS2 Floating Gate Hyelim Shin ¹ , Gunhoo Woo ² , and Taesung Kim ^{1,2,3} ¹ School of Semiconductor Convergence Engineering, Sungkyunkwan University, ² SKKU Advanced Institute of Nanotechnology, Sungkyunkwan University, ³ School of Mechanical Engineering, Sungkyunkwan University
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