



Future Normal in Semiconductor

2025년 2월 14일(금), 15:10-17:10

Room B(그랜드볼룸III), 4층

G. Device & Process Modeling, Simulation and Reliability 분과

068_[FB3-G] Carrier Transport & Quantum Simulation

좌장: 홍성민 교수(GIST), 신흥식 수석(DB하이텍)

<p>초청 FB3-G-1 15:10-15:40</p>	<p>Advances in Understanding Chemical Bonding in Amorphous and Crystalline Chalcogenide Semiconductors Tae Hoon Lee¹ and Stephen R. Elliott² ¹Kyungpook National University, ²University of Oxford, UK</p>
<p>FB3-G-2 15:40-15:55</p>	<p>Tackling Multi-dimensional Poisson Equations with Mixed Boundary Conditions Using Variational Quantum Algorithm Minjin Choi¹ and Hoon Ryu² ¹Center for Quantum Information R&D, KISTI, ²Department of Artificial Intelligence Engineering, Kumoh National Institute of Technology</p>
<p>FB3-G-3 15:55-16:10</p>	<p>First-Principles Modeling of Non-Equilibrium Behavior in Oxide Ferroelectric Tunnel Junctions Kaptan Rajput, Ryong Gyu Lee, Tae Hyung Kim, Hyeonwoo Yeo, Juho Lee, and Yong-Hoon Kim School of Electrical Engineering, KAIST</p>
<p>FB3-G-4 16:10-16:25</p>	<p>Quantum Transport Simulation for the Nanosheet FET Including Surface Roughness Scattering Phil-Hun Ahn and Sung-Min Hong School of Electrical Engineering and Computer Science, GIST</p>
<p>FB3-G-5 16:25-16:40</p>	<p>Evaluation of the Effect of Metal Contact Geometry on S/D Junction Resistance in Si/InGaAs FinFETs Using Semi-Classical Monte Carlo Carrier Transport Simulation Jae Yeon Kim^{1,3}, Dong Hyeok Lee², Suk Hyeong Youn², and Jiwon Chang^{1,3} ¹Department of System Semiconductor Engineering, Yonsei University, ²Department of Materials Science and Engineering, Yonsei University, ³BK21 Graduate Program in Intelligent Semiconductor Technology, Yonsei University</p>



제 32회 한국반도체학술대회

The 32nd Korean Conference on Semiconductors

2025년 2월 12일(수)-14일(금) | 강원도 하이원리조트

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FB3-G-6 16:40-16:55	Four-coupled Multi-physics Simulations of Multi-layer Ferroelectric Field Effect Transistors with Polar Topological States Yangjin Jung, Seokwon Lee, and Mincheol Shin Department of Electrical Engineering, KAIST
FB3-G-7 16:55-17:10	Extremely Low-Power Topological Transistors Yosep Park ¹ , Yungyeong Park ² , Subeen Lim ² , and Yeonghun Lee ^{1,2} ¹ Department of Intelligent Semiconductor Engineering, Incheon National University, ² Department of Electronics Engineering, Incheon National University