



현대모비스(주) 반도체사업 / 전무 박철홍

Head of Semiconductor Business
Senior Vice President

■ Personal Information

생년월일 1968년 5월
Date of Birth May 1968
출생지 대한민국 부산시
Place of Birth Busan, Korea

■ Educational Background

2008년 Univ California, San Diego
전자컴퓨터공학 박사
Ph.D., Electrical Computer
Engineering, UC San Diego

2005년 UC, San Diego 전자컴퓨터공학 석사
M.S., ECE, UC San Diego

1994년 경희 대학교, 수학과 석사
M.S., Mathematics, Kyung-Hee
University, Seoul

1992년 경희 대학교, 수학과 학사
B.S., Mathematics, KHU, Seoul

■ Professional Experience

2023년 3월~ 현대 모비스 반도체 사업 / 전무

2017년 1월~ 삼성전자 반도체 연구소, Logic TD / 상무

1994년 12월~ 삼성전자 반도체 연구소 입사

■ Technical Experience

Chul-Hong Park is a senior vice president of semiconductor business at Hyundai MOBIS. Currently, he leads all of divisions which include business development for the forthcoming mobility generation, system semiconductor, power device design, and power-module manufacturing. He is an executive in the technology with extensive background in design architecture, semiconductor manufacturing, yield management, VLSI CAD, process & device simulation, etc. He has developed design and process architecture of logic devices from 14nm to 2nm node in Samsung Semiconductor. He is the holder of over 30 patents and has published over 50 journal and conference papers. His research interests mainly include VLSI SOC design, automotive logic design, and automotive power-device design and manufacturing, as well as SiP, 2D and 3D packages for emerging technologies.