2024년 1월 25일(목) 09:00-17:55 저자 Q&A 세션: 17:15-17:55

D. Thin Film Process Technology 분과

	Mimicking IR Visionary System via 0D-2D Heterojunction of InAs QD/WSe ₂ Artificial Synapse
TP1-001	Soobin Shim ¹ , Hyeongtae Kim ¹ , Seongchan Kim ² , Nuri Oh ² , and Jun Hong Park ¹
	¹ School of Materials Science & Engineering, Gyeongsang National University, ² Division of Materials Science & Engineering, Hanyang University
	Charge Trap Engineering and Synaptic Behavior of Transition Metal Dichalcogenides Transistor, via Molecular Dynamics
TP1-002	MiJi Kwon ¹ , Hyeongtae Kim ¹ , Suyeon Cho ² , and Junhong Park ¹
	¹ School of Materials Science & Engineering, Gyeongsang National University, ² Department of Materials Engineering and Convergence Technology, Gyeongsang National University
	Van Der Waals Interface Engineering for Enhancement of Semiconductor Device Performance
TP1-003	Su-yeon Cho ¹ , Do-Hyeon Lee ² , and Jun Hong Park ¹
	¹ School of Materials Science & Engineering, Gyeongsang National University, ² Department of Materials Engineering and Convergence Technology, Gyeongsang National University
	Centimeter Scaled Growth and Electric Characteristics of Layered NiTe2
	Wonbeom Kim ¹ , Hyeongtae Kim ² , Yeonjin Je ³ , Eunjeung Kim ⁴ , and Junhong Park ²
TP1-004	¹ School of Materials Science & Engineering, Gyeongsang National University, ² Materials Engineering and Convergence Technology, Gyeongsang National University, ³ Ceramic Engineering, Gyeongsang National University, ⁴ LLNL
	Investigation of Annealing Effect on Indium-Zinc-Oxide (IZO) Thin Film Transistor (TFT) by
TD4 005	Gamma-ray Radiation
TP1-005	Do-Kywn Kim ¹ , Dong-Seok Kim ² , Min-Ju Kim ¹ , Tae-Eon Kim ¹ , and Seung Heon Shin ³
	¹ Department of Semiconductor Materials and Applications, Korea Polytechnics, ² Korea Multi-Purpose Accelerator Complex, KAERI, ³ Department of Semiconductor Process Equipment, Korea Polytechnics
	듀얼 게이트 a-ITGZO 박막 트랜지스터의 채널 구조에 따른 전기적 특성 연구
TP1-006	설민혁 ¹ , 조경아 ¹ , 강민구 ¹ , 김상섭 ² , 김상식 ¹
	¹ 고려대학교 전기전자공학과, ² Samsung Display Co., Ltd.
	a-ITGZO 박막트랜지스터의 성능 향상을 위한 Al ₂ O ₃ /SiO ₂ 게이트 절연막의 적용
TP1-007	강민구 ¹ , 조경아 ¹ , 김상섭 ² , 김상식 ¹
	¹ 고려대학교 전기전자공학과, ² Samsung Display Co., Ltd.
	HfO ₂ /InGaZnO Double-layered Transistor with Low-powered Switching Enabled by Quasi- two-dimensional Electron Channel
TP1-008	Seyoung Oh ^{1,2} and Byungjin Cho ^{1,2}
111 000	¹ Department of Urban, Energy, and Environmental Engineering, Chungbuk National University, ² Department of Advanced Material Engineering, Chungbuk National University
	Influence of TiO ₂ Thin Film Grown by Atomic Layer Deposition on N-Type Bi ₂ Te ₃ Se _{0.3} Thermoelectric
	Powders
TP1-009	Su Min Eun ¹ , Ji Hyeon Hwang ² , and Byung Joon Choi ¹
	¹ Department of Material Science and Engineering, Seoul National University of Science and Technology, ² Department of Optometry, Seoul National University of Science and Technology

TP1-010	The Enhancement of the Electrical Properties of a-ZTO Thin-Film Transistors through Metal Capping Process
	Jin Woo Lee, Se-Hyeong Lee, So-Young Bak, Dongki Baek, Chan-Yeong Park, Hyeongrok Jang, and Moonsuk Yi
	Pusan National University
TP1-011	Optimization for Enhanced Electrical Properties of ZrO ₂ /HfO ₂ Laminated Structure for Metal- Insulator-Metal Capacitors
	Yoonchul Shin, Seung Won Lee, and Ji-Hoon Ahn
	Department of Materials Science and Chemical Engineering, Hanyang University
	Wafer-scale Thin Film Grown WSe ₂ via Molten Salt Method and Device Applications
TP1-012	Sojeong Park ¹ , Hyeongtae Kim ² , Soobin Shim ² , and Jun Hong Park ²
	¹ Department of Energy and Mechanical Engineering, Gyeongsang National University, ² School of Materials Science and Engineering, Gyeongsang National University
	Two Step Surface Engineering of Transition Metal Dichalcogenide Heterojunction with Metal Oxide-formation and Reduction Processes
TP1-013	Mingu Kang ¹ and Jun Hong Park ^{1,2}
	¹ School of Materials Science and Engineering, Gyeongsang National University, ² Department of Materials Engineering and Convergence Technology, Gyeongsang National University
	Optically Simulated Synaptic Behaviors of HfS2 Grown via Molten Salt Flux Method
TD1_014	Seunghee Kim ¹ , Mi Ji Kwon ² , Jung Young Cho ³ , and Jun Hong Park ^{1,2}
TP1-014	¹ School of Materials Science and Engineering, Gyeongsang National University, ² Department of Materials Engineering and Convergence Technology, Gyeongsang National University, ³ Nano Convergence Materials Center, KICET
	Low Temperature Processed, Highly Stable CMOS Inverter by integrating Zn–ON and Tellurium Thin–Film Transistors : Journal of Information Display
TP1-015	Taeung Kim, Muhammad Naqi, Yongin Cho, and Sunkook Kim
	¹ Multifunctional Nano Bio Electronics Lab, Sungkyunkwan University, ² School of Advanced Materials Science and Engineering, Sungkyunkwan University
	Intense Pulsed Light을 이용한 선택적 어닐링을 통한 Top-gate Self-aligned 구조의 IGZO TFT 성능 개선
TP1-016	김희태 ¹ , 박서학 ¹ , 정재중 ¹ , 박영근 ¹ , 김동빈 ¹ , 조성행 ² , 최성율 ¹ , 조병진 ¹
	¹ 한국과학기술원 전기 및 전자공학부, ² 한국전자통신연구원 플렉시블전자소자연구실
	Unlocking the Functionality of Multi-phase Tungsten Disulfide for Negative Differential Resistance and Random-access Memory Devices
TP1-017	Jinill Cho ¹ , Gunhoo Woo ² , Jinhyeong Lee ¹ , and Taesung Kim ^{1,2}
	¹ School of Mechanical Engineering, Sungkyunkwan University, ² SKKU Advanaced Institute of
	Nanotechnology, Sungkyunkwan University
	Multi-stack Ferroelectric Capacitor based on Fluorite Structure Materials for Neuromorphic Computing
TP1-018	Hyo-Bae Kim and Ji-Hoon Ahn
	Department of Materials Science and Chemical Engineering, Hanyang University
TD1 010	Multilevel Block Copolymers and Polymer Colloids Composites for Sensitive Gas Sensor
TP1-019	Dong Won You, Geon Gug Yang, and Sang Ouk Kim Department of Material Science & Engineering, KAIST
	Department of Material Science & Lingmeening, NAIST
	Tungsten Diselenide (WSe) Ambipolar Transistor with Al ₂ O ₂ Passivation Supported by Ultrathin
TD4 000	Tungsten Diselenide (WSe ₂) Ambipolar Transistor with Al ₂ O ₃ Passivation Supported by Ultrathin Al layer for High-stability Logic Device
TP1-020	

TP1-021	Composition-controllable Growth of GeTe Thin Films by Hollow Cathode Plasma-assisted Atomic Layer Deposition
	Min Gyoo Cho, Ju Hwan Park, Si Eun Jung, and Byung Joon Choi
	Department of Material Science and Engineering, Seoul National University of Science and Technology
	High-performance of Hydrogenated Spinel Phase InZnSnO Thin-Film Transistors
TP1-022	Gwang-Bok Kim and Jae Kyeong Jeong
	Department of Electronic Engineering, Hanyang University
	Hydrogen Behavior in Oxide TFTs with Gate Insulator Variation by High-pressure Hydrogen Annealing
TP1-023	Jin Won Bak and Jae Kyeong Jeong
	Department of Electronic Engineering, Hanyang University
	Hydrogen Doped a-IGZTO TFTs with Excellent Reliability and High Field-effect Mobility
TP1-024	Sang Won Chung and Jae Kyeong Jeong
	Department of Electronic Engineering, Hanyang University
	Effect of Ti Interlayer on Ferroelectric HZO Thin Film
TP1-025	Jaeyoung Joo, Ju-young Jeong, Yoogeun Han, and Hyunchul Sohn
	Department of Materials Science and Engineering, Yonsei University
	Optimization of Al-doped HfO ₂ /ZrO ₂ Layered Structure for Improving Electrical Characteristics
TP1-026	Yeon-Ji Jeon, Seung Won Lee, and Ji-Hoon Ahn
	Department of Materials Science and Chemical Engineering, Hanyang University
	Investigation of IWO TFT for Enhanced Electrical Performance and Long-term Stability Compared to IGZO TFT
TP1-027	Hyun-Sik Choi, Ki-Ju Park, and Won-Ju Cho
	Department of Electronic Materials Engineering, Kwangwoon University
	Inherent Area-selective Atomic Layer Deposition of SiO2
TP1-028	Quang Khanh Nguyen, Juyeong Lee, and Myung Mo Sung
	Department of Chemistry, Hanyang University
	Inducing the Tetragonal-phase HfO ₂ in ZrO ₂ /HfO ₂ Stack by Introducing the Controlled Interfacial Layer
TP1-029	Woo Young Park ^{1,2} , In kyu Lee ^{1,2} , Young Uk Ryu ^{1,2} , and Woojin Jeon ^{1,2}
	¹ Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University,
	² Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University
	Hybrid Reactant of HfO ₂ Atomic Layer Deposition Process for Metal-insulator-metal Capacitor Applications
TP1-030	In Gyu Lee ^{1,2} , Woo Young Park ^{1,2} , Young Uk Ryu ^{1,2} , and Woojin Jeon ^{1,2}
	¹ Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University, ² Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University
	Effects of Electrode Configuration on the Electrical Properties of PEALD HZO Ferroelectric
TP1-032	Capacitors
	Ha Jeong Kim, Won Ji Park, and Hee Chul Lee
	Department of Advanced Materials Engineering, Tech University of Korea
	Characterization of HZO Films Prepared by Co-Plasma Atomic Layer Deposition for Ferroelectric Memory Application
TP1-033	Won Ji Park, Jae Hoon Yu, and Hee Chul Lee

TP1-034	Study of Reactant Gas Characteristics of Silicon Nitride Thin Film Deposited Using 27.12 MHz Frequency
	B.J. Lee ^{1,2} , H.C. Cho ^{1,2} , M.H. Cheon ^{1,2} , H.S. Ru ^{1,2} , H.C. Moon ^{1,2} , R. Woo ^{1,2} , D.W. Seo ^{1,2} , and J.W. Choi ^{1,2}
	¹ Vacuum Equipment R&D Division, Hanwha Corporation, ² Semiconductor Research Center, Hanwha Corporation
	Failure Analysis of Ovonic Threshold Switch from a Thermal Perspective
TP1-035	Ju Hwan Park ¹ , Myeong Jun Jung ¹ , Hyun Wook Kim ¹ , Su Yeon Lee ² , Jae Hyuck Jang ³ , Gun Hwan Kim ⁴ , Min Kyu Yang ² , and Byung Joon Choi ¹
	¹ Department of Materials Science and Engineering, Seoul National University of Science and Technology, ² Division of AI Convergence Engineering, Sahmyook University, ³ Electron Microscopy and Spectroscopy Team, KBSI, ⁴ Department of Materials Science and Engineering, Yonsei University
	Superior Infrared Reflectance and Sheet Resistance of ITO/Ag/ITO/Ag/ITO(IAIAI) Structure as Electrode of Transparent Photovoltaics
TP1-036	Chanhyuk Choi, JungHyun Lee, and Joondong Kim
	Department of Electrical Engineering, Incheon National University
	Functional Design of Optically Transparent Windows by Using Macleod Simulation
TP1-037	JungHyun Lee, ChanHyuk Choi, and Joondong Kim
	Incheon National University
	Implementation of Integrate-and-Fire (IF) Characteristics Using Oxide-based 1T-Neuron
TP1-038	Jaehee Lee ^{1,2} , Jieun Kim ^{1,2} , and Jung Wook Lim ^{1,2}
	¹ ETRI, ² UST
	Development of Visible–NIR Responsive Nanoporous Morphology on Large–scale IGZO and Realization of High–performance Image Sensor
TP1-039	Jaeseong Kim, Anamika Sen, Chaeyoung Im, and Sunkook Kim
	Department of Advanced Materials Science Engineering, Sungkyunkwan University
	TiO ₂ Interlayer를 이용한 AI/ZnO 접촉저항 개선
TP1-040	윤성빈 ^{1,2} , 김기영 ^{1,2} , 이해원 ^{1,2} , 김민재 ^{1,2} , 황현준 ^{1,2} , 이병훈 ^{1,2}
	100TO DOCTECH 2D DOCTECH
	¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH
	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating
TP1-041	
TP1-041	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives
TP1-041	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park
TP1-041 TP1-042	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University
	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University 극박막 상복합 ZnO 영미분전도소자의 1/f 노이즈 특성 연구
	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University 극박막 상복합 ZnO 영미분전도소자의 1/f 노이즈 특성 연구 이해원 ^{1,2} , 김승모 ^{1,2} , 이용수 ^{1,2} , 김민재 ^{1,2} , 전재현 ^{1,2} , 황현준 ^{1,2} , 이병훈 ^{1,2} ¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH Performance Improvement of Indium Oxide TFTs with Tungsten Doping
	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University 국박막 상복합 ZnO 영미분전도소자의 1/f 노이즈 특성 연구 이해원 ^{1,2} , 김승모 ^{1,2} , 이용수 ^{1,2} , 김민재 ^{1,2} , 전재현 ^{1,2} , 황현준 ^{1,2} , 이병훈 ^{1,2} ¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH Performance Improvement of Indium Oxide TFTs with Tungsten Doping Juwon Kim ¹ , Hyun-Sik Choi ² , Won-Ju Cho ² , and Hamin Park ¹
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TP1-042 TP1-043	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University 국박막 상복합 ZnO 영미분전도소자의 1/f 노이즈 특성 연구 이해원 ^{1,2} , 김승모 ^{1,2} , 이용수 ^{1,2} , 김민재 ^{1,2} , 전재현 ^{1,2} , 항현준 ^{1,2} , 이병훈 ^{1,2} ¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH Performance Improvement of Indium Oxide TFTs with Tungsten Doping Juwon Kim ¹ , Hyun–Sik Choi ² , Won–Ju Cho ² , and Hamin Park ¹ ¹ Department of Electronic Engineering, Kwangwoon University, ² Department of Electronic Materials Engineering, Kwangwoon University
TP1-042 TP1-043 TP1-044	Enhanced Performance and Stability of 2D Sn Halide Perovskite Transistor by Incorporating Alkylammonium Halide Additives Hakjun Kim, Bum Ho Jeong, Jongmin Lee, and Hui Joon Park Department of Organic and Nano Engineering, Hanyang University 국박막 상복합 ZnO 영미분전도소자의 1/f 노이즈 특성 연구 이해원 ^{1,2} , 김승모 ^{1,2} , 이용수 ^{1,2} , 김민제 ^{1,2} , 전재현 ^{1,2} , 황현준 ^{1,2} , 이병훈 ^{1,2} ¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH Performance Improvement of Indium Oxide TFTs with Tungsten Doping Juwon Kim ¹ , Hyun-Sik Choi ² , Won-Ju Cho ² , and Hamin Park ¹ ¹ Department of Electronic Engineering, Kwangwoon University, ² Department of Electronic Materials Engineering, Kwangwoon University 플라즈마 처리를 이용한 Tellurium FET 의 히스테리시스 개선 김규헌 ^{1,2} , 김민제 ^{1,2} , 이용수 ^{1,2} , 김승모 ^{1,2} , 이해원 ^{1,2} , 황현준 ^{1,2} , 이병훈 ^{1,2} ¹ CSTC, POSTECH, ² Department of Electrical Engineering, POSTECH
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TP1-046	Stable Ferroelectric Properties of Sub-5 nm Hafnium-Zirconium-Oxide Thin Films Deposited via Atomic Layer Deposition
	Gunho Kim ¹ , Hyo-Bae Kim ¹ , Wonwoo Kho ² , Yoomi Kang ² , Seung-Eon Ahn ³ , and Ji-Hoon Ahn ¹
	¹ Department of Materials Science and Chemical Engineering, Hanyang University, ² Department of IT · Semiconductor Convergence Engineering, Tech University of Korea, ³ Department of Nano & Semiconductor Engineering, Tech University of Korea
	Fabrication of TiO ₂ Dispersion Strengthened Nb-based Alloy by Atomic Layer Deposition
TP1-047	Ji Young Park, Ji Hyeon Jeon, Sumin Eun, Eui Seon Lee, Eunho Ma, Min Seob Jeong, Byoungchul Hwang, Jongmin Byun, Sung-Tag Oh, and Byung Joon Choi
	Department of Materials Science and Engineering, Seoul National University of Science and Technology
	Thermal ALD Novel Mo Precursor for Low Resistivity MoN Thin Film Formation
TP1-048	Myeong-Ho Kim, Yun-Gyeong Yi, Su-min Kim, In-Jae Lee, and Jin-Sik Kim
	UP Chemical
	Physical Properties of GeS _x Thin Films Deposited by RF Sputtering
TP1-049	Ju Sung Kim, Wan Sun Kim, and Hyunchul Sohn
	Department of Materials Science and Engineering, Yonsei University
	Threshold Switching Characteristics of (ZnTe) _x (ZnS) _{1-x} Chalcogenide Alloy Deposited by RF Sputtering
TP1-050	Wansun Kim, Jusung Kim, and Hyunchul Sohn
	Department of Materials Science and Engineering, Yonsei University
	Achieving Molecular Alignment in Semiconducting Polymers: A Step towards Improved Electrical
TP1-051	Performance
	Jin Seok Yoon, Nak Hee Kang, Sam Nyung Yi, Hyung Soo Ahn, Kyung Hwa Kim, and Young Tea Chun
	Division of Electronics and Electrical Information Engineering, Korea Maritime and Ocean University
	Atomic Layered Deposition of SnO ₂ Thin Films Using a Novel Sn Precursor
TP1-052	Jeong Eun Shin ¹ , Heesun Kim ² , Bo Keun Park ² , and Jeong Hwan Han ¹
	¹ Department of Materials Science and Engineering, Seoul National University of Science and Technology, ² Thin Film Materials Research Center, KRICT
	Effect of Electrodes on $(NiO)_x(La_2O_3)_{1-x}$ Thin Films
TP1-053	Jeongwoo Lee and Hyunchul Sohn
	Department of Materials Science and Engineering, Yonsei University
	40.68 & 60 MHz 주파수를 이용한 2단계 PE-ALD 공정이 적용된 Silicon Nitride 박막의 특성 비교
TP1-054	Da-Eun Bae ¹ , Hyung Min Kim ¹ , Sang Ick Lee ² , Jae Ho Choi ¹ , and Jae Hak Jeong ¹
	¹ CN1 Co., Ltd., ² DNF Co., Ltd.
TP1-055	Random-Network Silicon Nanowire Channel Based High-Performance Ca ²⁺ -Selective Dual-Gate Ion Sensitive Field-Effect Transistors
TFT=000	Tae-Gyu Hwang, Tae-Hwan Hyun, and Won-Ju Cho
	Department of Electronic Materials Engineering, Kwangwoon University
	Morphotropic Phase Boundary 구조를 갖는 Hf 유전막 특성 연구
TP1-056	이찬빈, 김승모, 황현준, 이병훈
	CSTC, Department of Electrical Engineering, POSTECH
	Thermal Annealing of Solution-Processed P-type NiO Transistor
TP1-057	Yerim Lee ¹ , Tae-Gyu Hwang ² , Won-Ju Cho ² , and Hamin Park ¹
191-057	¹ Department of Electronic Engineering, Kwangwoon University, ² Department of Electronic Materials Engineering, Kwangwoon University

D. Thin Film Process Technology 분과

ZONE 2 (2층 로비)

TP1-058	Supercycle of AlO and HfO Using Atomic Layer Deposition for Nanolaminate Capacitor Hyunseok Son, Beomhee Yoon, Hyunho Lee, and Hamin Park Department of Electronic Engineering, Kwangwoon University
TP1-059	LiNbO ₃ Thin Film for Photonics Devices by Sputtering Method Namhoon Kim ^{1,2} , Seunghwi Koo ^{1,2} , Haeri Park ^{1,3} , Roju Chae ^{1,3} , Heonjin Choi ² , and Donghee Park ¹ ¹ KIST, ² Department of Material Science and Engineering, Yonsei University, ³ School of Electrical Engineering, Korea University
TP1-060	Study of Metal-doped Zinc Oxide-Based Electron Extraction Layer to Improved Performance of Inverted Organic Photodetectors Jaebum Jeong and Jun Young Kim Department of Semiconductor Engineering, Gyeongsang National University
TP1-061	A Study of Defect Control Through Heat Transfer in a Furnace System Daeman Seo ^{1,2} , Sungman Lee ¹ , Sungho Jegal ¹ , Seungjae Baek ¹ , and Inho Lee ² ¹ Device Solution, Samsung Electronics Co., Ltd., ² Major of Electronic Engineering, Hankyong National University
TP1-062	Analysis of Growth Behavior and Electrical Property of TiO ₂ Thin Film by Atomic Layer Deposition Jae Hun Hwang, Jong ho Song, and Taeyong Eom Thin Film Materials Research Center, KRICT
TP1-063	Demonstration of Atomic Layer Deposition of BeO Using Discrete Feeding Method JongHyun Bae ¹ , YoonSeo Jang ¹ , Juyung Chae ¹ , Christopher W. Bielawski ² , and Jungwoo Oh ¹ ¹ School of Integrated Technology, Yonsei University, ² CMCM IBS, Department of Chemistry, UNIST
TP1-064	Understanding Al ₂ O ₃ Infiltration in PDMS via Atomic Layer Deposition Pengfei Liu ¹ and In Soo Kim ^{1,2} ¹ KIST, ² Sungkyunkwan University
TP1-065	Accurate Regulation of Dopant Distribution in both Lateral and Vertical Directions in Sn-doped In ₂ O ₃ Grown via Atomic Layer Deposition Tae Seok Kim ^{1,2} , Han Kim ^{1,2} , and Seong Keun Kim ^{1,2} ¹ KU-KIST Graduate School of Converging Science and Technology, Korea University, ² Electronic Materials Research Center, KIST

TP1-066	Growth Characteristics and Film Properties of Molybdenum Oxide Thin Films by Atomic Layer Deposition with Different Oxygen Sources Haram Yang ¹ , Hyeongjun Kim ² , and Woongkyu Lee ¹ ¹ Department of Materials Science and Engineering, Soongsil University, ² Department of Green Chemistry and Materials Engineering, Soongsil University
TP1-067	Novel Molybdenum N-alkoxy Carbothioamide Complexes for 2D MoS ₂ Thin Films Sung Kwang Lee ^{1,2} , Seung Uk Son ² , and Taek-Mo Chung ^{1,3} ¹ Thin Film Materials Research Center, KRICT, ² Department of Chemistry, Sungkyunkwan University, ³ Department of Chemical Convergence Materials, UST
TP1-068	Inhibitor Assisted Si-HfO ₂ ALD Process to Improve Si Doping Uniformity Duck Hyeon Seo, Jae Min Kim, Ha Na Kim, Ji Yeon Han, Hyeon Sik Cho, Ju Hwan Jung, Hyun Ju Jung, Sun Young Baik, and Kyu Ho Cho EGTM Co. R&D Center
TP1-069	Characterization of Capacitors with ITO/HfAIO(HAO)/ITO Structures Deposited by RF-sputtering Operted at Low Frequency In-Pyo Hong, He Rui, Ma-Ro Kim, and Chung Wung Barki Gachon University
TP1-070	Improving Electrical Properties Using New Al Precursor for Doping Sung-Woo Ahn, Jae-Young Min, Ki-Chang Song, and Dr. Jin-Sik Kim UP Chemical
TP1-071	Characteristics of Molybdenum Dioxide Atomic Layer Deposition Process Hyun June Park, Min Su Cho, and Sung Woong Chung POSTECH
TP1-072	Experimental Realization Strain-induced Room Temperature Ferroelectricity in SrMnO ₃ Thin Films on Si and Nb-SrTiO ₃ Substrates through RF-sputtering High-Temperature Depositation Rui He, Maro Kim, and ChungWung Bark Gachon University
TP1-073	Delay Time Variation with Applied Voltage Pulses in Te-Based Ovonic Threshold Switching Selectors Sangyeop Kim ^{1,2} , Young-Min Kim ^{1,2} , Su-Bong Lee ¹ , and Jong-Souk Yeo ^{1,2} ¹ School of Integrated Technology, College of Computing, Yonsei University, ² BK21 Graduate Program in Intelligent Semiconductor Technology
TP1-074	Growth of Scandium – doped Aluminum Nitride Thin Films by Plasma Enhanced Atomic Layer Deposition Seung Hoon Oh, Hyoeun Roh, and Taeyong Eom Thin Film Materials Research Center, KRICT

TP1-075	Ferroelectricity of $Hf_xZr_{1-x}O_2$ Thin Films Deposited on Epitaxial TiN Bottom Electrodes
	Yoogeun Han, Ju-Young Jeong, Jaeyoung Joo, and Hyunchul Sohn
	Department of Materials Science and Engineering, Yonsei University
TP1-076	New Tin Sulfide Precursor for Semiconductor Materials
	Heenang Choi ^{1.2} and Taek-Mo Chung ^{1,3}
	¹ Thin Film Materials Research Center, KRICT, ² Department of Chemistry, Sungkyunkwan University, ³ Department of Chemical Convergence Materials, UST
	Atomic Layer Deposition of In_2O_3 with Different Temperatures for n-Type Oxide Semiconductors
TP1-077	Kyunghun Lyu ¹ , Jaejun Lee ² , and Woongkyu Lee ^{1,2}
	¹ Department of Materials Science and Engineering, Soongsil University, ² Department of Green Chemistry and Materials Engineering, Soongsil University
	Combined Experimental and DFT Analysis of Initial Adsorption Behavior in ZfHfO ₂ Thin Films on TiN Surface
TP1-078	Jeong Hyeon Park, Jenam Kim, and Woojin Jeon
	Department of Advanced Materials Engineering for Information and Electronics, and Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University
	Enhancement of Resistance Switching Behavior of Au/TiO ₂ /Au Memristors on PDMS Substrate with Pyramid Structure
TP1-079	Jaejun Lee ¹ , Kyunghun Lyu ² , and Woongkyu Lee ^{1,2}
	¹ Department of Green Chemistry and Materials Engineering, Soongsil University, ² Department of Materials Science and Engineering, Soongsil University,
	Effects on Electrical Properties of TiO ₂ -Based Capacitors by Improving Bulk and Interface Properties
TD1 000	Taehyun Kim ¹ , Daeun Lim ² , Juan Hong ² , and Woongkyu Lee ^{1,2}
TP1-080	¹ Department of Green Chemistry and Materials Engineering, Soongsil University, ² Department of Materials Science and Engineering, Soongsil University
	Contact Resistance Improvement of a-IGZO TFT by Inserting ALD Based AZO Interlayer
TP1-081	Dongseon Kim and Jae Kyeong Jeong
	Department of Electronic Engineering, Hanyang University
	Development of High-performance Broadband Photodetectors Using Hydrogen Plasma-treated IGZO
TD1 000	Thin F 철회 ala ² , and SunKook Kim ¹
TP1-082	
	¹ Multifunctional Nano Bio Electronics Lab, School of Advanced Materials Science and Engineering, Sungkyunkwan University, ² Swiss Federal Institute of Technology Lausanne
	Growth of Highly Dense and Conformal GeSe Thin Films by Thermal Atomic Layer Deposition
TP1-083	Ye Bin Weon ¹ , Hyunwook Kim ¹ , Junyoung Lim ² , David Ahn ² , and Byung Joon Choi ¹
	¹ Department of Materials Science and Engineering, Seoul National University of Science and Technology, ² R&D Division, SK hynix Inc.
	Study on Multi-layer Stacking Effects of Oxide-Based Thin-films
TP1-084	Jinyeong Lee, Sungbin Jo, and Jaewook Jeong
	School of Information and Communication Engineering, Chungbuk National University
	Atomic Layer Deposition of Ir Thin Films with Tricarbonyl $(1,2,3,-\eta)-1,2,3-tri(tert-butyl)-$
	cyclopropenyl Iridium (TICP) and O_3
TP1-085	Han Kim ^{1,2} , Hong Keun Chung ^{2,3} , Tae Seok Kim ^{1,2} , and Seong Keun Kim ^{1,2}
	¹ KU–KIST Graduate School of Converging Science and Technology, Korea University, ² Electronic Materials Research Center, KIST, ³ Department of Materials Science and Chemical Engineering, Hanyang University

TP1-086	Enhancing the Electrical Properties of TiN/ZrO ₂ /TiN MIM Capacitor with In ₂ O ₃ Buffer Layer by Atomic Layer Deposition Yoona Choi ^{1,2} , Seungwoo Lee ^{1,2} , Donghyun Kim ³ , Hansol Oh ³ , Yongjoo Park ³ , and Woojin Jeon ^{1,2} ¹ Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University, ² Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University,
	³ Advanced Research Development Team, SK Trichem Co., Lt
TP1-087	Controlling Electrical Properties of ZrO ₂ -Based Metal-insulator-metal Capacitor via Gd Doping without Sacrificing Tetragonality Seungwoo Lee ^{1,2} , Yoona Choi ^{1,2} , Jonghwan Jeong ^{1,2} , Jihun Nam ^{1,2} , Han sol Oh ³ , Hanbyul Kim ³ , Yongjoo Park ³ , and Woojin jeon ^{1,2}
	² Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University, ³ Advanced Research Development Team, SK Trichem Co., Lt
TP1-088	Thermal Atomic Layer Deposition of Aluminum Nitride Thin Film Using Tris(dimethylamido)aluminum and Ammonia Okhyeon Kim, Hyunmin Han, Yerim Choi, Jian Heo, Changgyu Kim, Hye-Lee Kim, and Won-Jun Lee Department of Nanotechnology and Advanced Materials Engineering, Sejong University
	Thermal Atomic Layer Deposition of Gallium Nitride Films Using Tris(dimethylamido)gallium and
TP1-089	Ammonia
	Yerim Choi, Okhyeon Kim, Jian Heo, Hye-Lee Kim, and Won-Jun Lee Department of Nanotechnology and Advanced Materials Engineering, Sejong University
	Study of Electrical Properties of Vanadium Dioxide Thin Films on TiN by Reactive Sputtering Seunghwi Koo ^{1,2} , Haeri Park ^{1,3} , Namhoon Kim ^{1,2} , Roju Chae ^{1,3} , Heonjin Choi ² , and Donghee Park ¹
TP1-090	¹ Center for Opto-Electronic Materials and Devices, Post-Silicon Semiconductor Institute, KIST, ² Materials Science and Engineering, Yonsei University, ³ School of Electrical Engineering, Korea University
	Preparation of Cubic-phase-stabilized Y-doped ZrO ₂ Nanoparticles and Thin Films by Solution Methods for SOFC Application
TD1_001	Taeyoon Kim ¹ , Yunbin Kim ² , and Sangmoon Park ^{1,2,3}
TP1-091	¹ Department of Electronics-Energy Materials, Silla University, ² Division of Energy and Chemical Engineering Major in Energy and Applied Chemistry, Silla University, ³ Department of Fire Protection and Safety Management, Silla University
	Strategies for Precision Control of the Interfacial Layer in ZrO ₂ (or HfO ₂)/TiN Structures for DRAM
TP1-092	Capacitors Myoung Su Jang ^{1,2} , Ji Hoon Jeon ^{1,2} , Taikyu Kim ² , and Seong Keun Kim ^{1,2}
111 032	¹ KU-KIST Graduate School of Converging Science and Technology, Korea University, ² Electronic Materials Research Center, KIST
	Variation in Carrier Concentration and Sheet Resistance of Atomic-Layer Deposited InGaSnO Thin Films with Controlling Process Tomperatures
TP1-093	Films with Controlling Process Temperatures Jae-Hyuk Yoo ¹ , Shin-Ho Noh ¹ , Young-Ha Kwon ² , Nak-Jin Seong ² , Kyu-Jeong Choi ² , and Sung-Min
	Yoon ¹ ¹ Kyung Hee University, ² NCD Co., Ltd.
	Effects of Crystal Structures of TiO _{2-x} Film on Their Thermistor Resistance
TP1-094	Haeri Park ^{1,2} , Jeongeun Mo ^{1,3} , Won jun Choi ¹ , Gyutae Kim ² , and Donghee Park ¹
121-094	¹ KIST, ² School of Electrical Engineering, Korea University, ³ School of Advanced Materials Science and Engineering, Sungkyunkwan University

	Dielectric Engineering for High-Performance Top Gate SnO Thin-Film Transistors toward Vertically Stacked Complementary Inverters
TP1-095	MinKi Choe ¹ , Seung Ho Ryu ^{2,3} , Taeyong Eom ⁴ , Taek-Mo Chung ⁴ , Seong Keun Kim ^{2,3} , and In-Hwan Baek ¹
	¹ Department of Chemical Engineering, Inha University, ² KU–KIST Graduate School of Converging Science and Technology, Korea University, ³ Electronic Materials Research Center, KIST, ⁴ Division of Advanced Materials, KRICT
TP1-096	Area-selective-deposition (ASD) of Ruthenium (Ru) Thin Film Using Self-assembled Monolayer (SAM) through Surface Modification
	Ji hyeon Sim, Chae Won Kim, Hyun Jin Lim, Ki Sub Kim, Hyeong Jun Kim, Hyo Jin Ahn, and Changhwan Choi
	Division of Materials Science and Engineering, Hanyang University
	Tailoring of Ferroelectric Coercive Field and Polarization of MFM Capacitors with Hf _x Zr _{1-x} O ₂ Bilayer
TP1-097	Structure
	Geon Park and Rino Choi 3D Convergence Center and Materials Science and Engineering, Inha University
	Optoelectronic-synaptic Properties of ReS ₂ /MoS ₂ Hetero-structure Synthesized by Chemical Vapor
	Deposition
TP1-098	Dong Geun Kim, Seung Won Lee, and Ji-Hoon Ahn
	Department of Materials Science and Chemical Engineering, Hanyang University
	Correlation between I-V and C-V Characteristics of InGaZnO TFTs Having AlOx Gate Insulator
TP1-099	Jaehyun Ahn, Seungkyun Ham, and Jaewook Jeong
	School of Information and Communication Engineering, Chungbuk National University
	The Effect of Seed Layer Engineering on the Performance of HZO-Based Ferroelectric Field Effect Transistor (FeFET)
TP1-100	JunHyeok Park ¹ , Chulwon Chung ² , Boncheol Ku ¹ , Seung Hyeon Yun ¹ , Kyungsoo Park ¹ , Yu Jeong Choi ¹ , and Changhwan Choi ¹
	¹ Division of Materials Science and Engineering, Hanyang University, ² Deparment of Energy Engineering, Hanyang University
	8-inch MgO Thin Film Technology for Next-Generation Memory Applications
TP1-101	Giryun Hong, Beomjoo Ham, Jongseo Park, Jehyun An, Bohyeon Kang, Sung-min Ahn, and Rock-Hyun Baek
	Department of Electrical Engineering, POSTECH
TP1-102	Development of ALD Based VO ₂ Thin Film FormationProcess Technology and Implementation of Firing Type Threshold Switching Device
	Yong Tae Kim ¹ , Yewon Seo ² , Pyeongkang Hur ² , Junwoo Son ² , and Jaeyeong Heo ¹
	¹ Chonnam National University, ² POSTECH
	Vertical Side-Wall MoS ₂ Channel Transistors : Thicknesses of 0.65nm and 6.5nm
TP1-103	Ki Han Kim ¹ , Huimin Lee ² , Joonki Suh ² , and Byung Chul Jang ¹
	¹ School of Electronic and Electrical Engineering, Kyungpook National University, ² Department of Materials Science and Engineering, UNIST
	Multiply and Accumulate Operation with 1 Selector 1 RRAM Device 1K Crossbar Arrays
TP1-104	June Hyuk Lee, Su Yeon Lee, Hyun Kyu Seo, Se Yeon Jeong, Min Kyung Lee, and Min Kyu Yang
	Intelligent Electronic Device Lab, Sahmyook University
	HfO _x -Based Synaptic Memristor for Neuromorphic Computing
TP1-105	Se Yeon Jeong, Hyun Kyu Seo, Su Yeon Lee, June hyuk Lee, Min Kyung Lee, and Min Kyu Yang
	Intelligent Electronic Device Lab, Sahmyook University

TP1-106	High Mobility Oxide Thin Film Transistor with Amorphous In-Ga-Sn-O Fabricated by RF-magnetron Sputtering
	Hyunil Jo ¹ , Juhan Kim ¹ , Sumi Kim ¹ , Eunji Kim ¹ , Yugyu Jang ¹ , Myeongcheol Jo ¹ , Boram Shin ¹ , Yiryeong Choi ¹ , Joon-Hyung Lee ¹ , Byeong-Seong Jeong ² , and Young-Woo Heo ¹
	¹ School of Materials Science and Engineering, Kyungpook National University, ² Department of Hydrogen and Renewable Energy, Kyungpook National University,
	Methane Gas Detection Sensors based on Carbon Nano Tube
TP1-107	Da Gyo Yoo, Kyung Eun Kim, Ryang Ha Kim, and Young Lae Kim
	Department of Electronic Engineering, Gangneung-Wonju National University
	Nitric Oxide Gas Detection and Analysis by Single-walled Carbon Nanotubes-Based Sensor
TP1-108	Kyung Eun Kim, Ryang Ha Kim, and Young Lae Kim
	Gangneung-Wonju National University
TP1-109	Mechanism of Hydrogen-Induced Negative Threshold Voltage Shift in InSnZnO TFT under Positive Bias Temperature Stress
161-109	Seong-In Cho and Sang-Hee Ko Park
	KAIST
	Non-volatile Behavior in ZrO ₂ -Based Ferroelectric-like Memory Devices Using Asymmetric Metal Work-function Engineering
TP1-110	SeungHyeon Yun ¹ , Chulwon Chung ² , Boncheol Ku ¹ , Junhyeok Park ¹ , Kyungsoo Park ¹ , Yu Jeong Choi ¹ , and Changhwan Choi ¹
	¹ Division of Materials Science and Engineering, Hanyang University, ² Deparment of Energy Engineering, Hanyang University
	Improved DRAM Electrode/Dielectric Interface Properties Using Nb $_2O_5$ and Ta $_2O_5$ Ultrathin Layer
TP1-111	Yong ju Kwon, Woo hyuk Kim, and Woo-Hee Kim
	Department of Materials Science and Chemical Engineering, BK21 FOUR ERICA-ACE Center, Hanyang University
TP1-112	Develop Behavior of Low Temperature Chemical Vapor Deposited Sn-Based Inorganic Dry Resist for Next-generation EUV Lithography
191-112	Hye Kyung Kim and Woo-Hee Kim
	Department of Materials Science and Chemical Engineering, Hanyang University
	Multilevel Switching Behavior in Physically Transient Memristor for Biodegradable Electronics
TP1-113	Mohammad Tauquir Alam Shamim Shaikh ^{1,2} and You Seung Rim ^{1,2}
11110	¹ Department of Semiconductor Systems Engineering, Sejong University, ² Department of Intelligent Mechatronics Engineering and Convergence Engineering for Intelligent Drone, Sejong University
	Morphology Engineering in Mo–Hf $_{0.5}$ Zr $_{0.5}$ O $_2$ –Mo Metal-ferroelectric-metal Capacitor with Surface Pre-treatment
TP1-114	Seung Yeon Kim, Dong Hee Han, and Woojin Jeon
	Department of Advanced Materials Engineering for Information and Electronics, Integrated Education Program for Frontier Science & Technology (BK21 Four), Kyung Hee University

F. Silicon and Group-IV Devices and Integration Technology 분과

TP1-115	수직 적층 실리콘 나노와이어 FBFET의 메모리 특성 연구 류승호 ¹ , 조경이 ² , 김상식 ^{1,2} ¹ 고려대학교 반도체시스템공학과, ² 고려대학교 전기전자공학과
TP1-116	피드백 전계효과 트랜지스터 기반 링 오실레이터 동작 특성 연구 손재민, 조경아, 김상식 고려대학교 전기전자공학과
TP1-117	나노시트 피드백 전계효과 트랜지스터의 Neural Oscillation 동작 연구 허효주 ¹ , 신연우 ¹ , 류승호 ² , 조경아 ¹ , 김상식 ^{1,2} ¹ 고려대학교 전기전자공학과, ² 고려대학교 반도체시스템공학과
TP1-118	Radiofrequency Switches based on Vanadium Oxide Dahyeon Kim ¹ , Jiyeon Ryu ² , Changwoo Pyo ¹ , Seung Chan Lee ¹ , Tae-Sik Yoon ^{2,3} , and Myungsoo Kim ^{1,2} ¹ Department of Electrical and Computer Engineering, UNIST, ² Graduate School of Semiconductor Materials and Devices Engineering, UNIST, ³ Department of Materials Science and Engineering, UNIST
TP1-119	Characterization Si Micro-single Crystals with Chiral Properties Jeongbin Heo ¹ , Kyoung Hwa Kim ¹ , Suhyun Mun ¹ , Seonwoo Park ¹ , Hyung Soo Ahn ¹ , Jae Hak Lee ^{1,2} , Min Yang ¹ , Young Tea Chun ¹ , Sam Nyung Yi ¹ , Yeon-Suk Jang ³ , Won Jae Lee ³ , Myeong-Cheol Shin ⁴ , and Sang-Mo Koo ⁴ ¹ Department of Nano-Semiconductor Engineering, Korea Maritime and Ocean University, ² LNBS Co., Ltd., ³ Department of Advanced Materials Engineering, Dong-Eui University, ⁴ Department of Electronic Materials Engineering, Kwangwoon University
TP1-120	HV CMOS 공정에서 인접 소자 영향으로 인한 Parasitic Leakage 개선 강형근, 이도현, 고대현, 황수진, 이문영, 남명희, 박정수 Department of Technology Development, SK hynix system ic
TP1-121	Impact of Work-function Variation in Ferroelectric Field-Effect Transistor Su Yeon Jung and Jang Hyun Kim Department of Intelligence Semiconductor Engineering, Ajou University
TP1-122	Effects of Pre-heating Zone on Dopant Activation by Continuous-wave Laser at High Scan Speed Dong Hyeok Choi ¹ , Seung Hwan Kim ¹ , Nak Sun Sung ¹ , Sung Wook Jang ² , and Sang Hee Yang ³ ¹ Laser Advanced Technology Team, AP Systems Co., Ltd., ² Laser Equipment Department, AP Systems Co., Ltd., ³ Display Equipment Division, AP Systems Co., Ltd.
TP1-123	메쉬 타입 플로팅게이트를 이용한 시냅스 트랜지스터 제작 정소연 ¹ , 양정목 ¹ , 김재민 ¹ , 채수현 ¹ , 구태환 ¹ , 장문규 ^{1,2} ¹ School of Nano Convergence Technology, Hallym University, ² Center of Nano Convergence Technology, Hallym University
TP1-124	금 나노 입자 양자점을 이용한 플래시 메모리 기반 시냅스 단일 소자 전기적 특성 연구 Jae Min Kim ¹ , Jeong Mok Yang ¹ , So Yeon Jung ¹ , Su Hyeon Chae ¹ , Tae Hwan Koo ¹ , and Moon Gyu Jang ^{1,2} ¹ School of Nano Convergence Technology, Hallym University, ² Center of Nano Convergence Technology, Hallym University
TP1-125	Large-LRS Vertical ReRAM with Network Semiconductor CNT-Edge Electrodes for Large-scale Array Size Sungmin Eum ¹ , Haksoon Jung ² , Hyunho Gu ¹ , and Jimin Kwon ¹ ¹ Department of Electrical Engineering, UNIST, ² Department of Chemical Engineering, POSTECH

TP1-126	Achieving Both Enhancement-mode Operation and Large On-current in Bottom-gate Indium Tin Oxide Transistors
	Hyeonho Gu ¹ , Haksoon Jung ² , and Jimin Kwon ¹
	¹ Department of Electrical-Engineering, UNIST, ² Department of Chemical Engineering, POSTECH
TP1-127	The Impact of Interconnect Resistance on DC Measurement of Test Vehicle at Sub-3nm Technology Node Jung Su Kim and Changhwan Shin School of Electrical Engineering, Korea University
TP1-128	Quantitative Analysis on Read/Write Performance in GAAFET-Based 6T-SRAM Bit Cell Changwoo Han ¹ , Yejoo Choi ² , and Changhwan Shin ¹ ¹ School of Electrical Engineering, College of Engineering, Korea University, ² Department of Electrical and Computer Engineering, Sungkyunkwan University
TP1-129	밀리미터파 III-V/Si 단일 3차원 (M3D) 집적을 위한 고성능 Si 수동소자 집적기술 개발 성민경 ¹ , 박민식 ² , 정재용 ³ , 임정택 ⁴ , 송재혁 ⁴ , 송종현 ^{1,4} , 이원철 ^{1,4} , 심갑섭 ^{1,4} , 서동주 ¹ , 임유리 ¹ , 고형호 ⁴ , 김철영 ⁴ , 설우석 ¹ , 김상현 ³ , 이종원 ¹ ¹ 나노종합기술원, ² 한밭대학교, ³ 한국과학기술원, ⁴ 충남대학교
TP1-130	Investigation of Oxygen-Scavenging Effect on Hf _{0.5} Zr _{0.5} O ₂ Metal-Ferroelectric-Insulator- Semiconductor (MFIS) Stack with CMOS Compatible Gate Structure Jinhwan Jung, Seonggeun Kim, and Sangwan Kim Department of Electronic Engineering, Sogang University
TP1-131	Charge Storage Memory Utilizing Ge Quantum Dots Gyu Bin Lee, Gyeong Min Seo, and Byoung Don Kong Department of Electrical Engineering, POSTECH
TP1-132	Impact of RTA on the Way of Forming Filaments in Oxide RRAM Bit Cell Gwon Kim ¹ and Changhwan Shin ² ¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² School of Electrical Engineering, Korea University
TP1-133	Optimization of T-CMOS Based Ternary Content-Addressable Memory Cell for High-density Application Jun Young Park ¹ , Young-Eun Choi ¹ , Woo-Seok Kim ¹ , Myoung Kim ¹ , Yesong Jeong ¹ , Sang Hun Yeo ¹ , Kwan Yong Lee ¹ , In Jun Jang ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
TP1-134	Study of Spacer Material Effects on 3D NAND Flash Memory Characteristics in Retention State Yun-Jae Oh ¹ , Yunejae Suh ² , Inyoung Lee ¹ , Daewoong Kang ³ , and II Hwan Cho ¹ ¹ Department of Electronic Engineering, Myongji University, ² Department of Electronic Engineering, Soongsil University, ³ Department of Next Generation Semiconductor Convergence and Open Sharing System, Seoul National University
TP1-135	Analysis of Interface Trap Density in Metal-Ferroelectric-Insulator-Semiconductor (MFIS) Capacitor with High-k Dielectrics Chankoo Kim ¹ , Dong Keun Lee ¹ , Seonggeun Kim ¹ , Tae-Hyeon Kim ² , Sihyun Kim ¹ , and Sangwan Kim ¹ ¹ Department of Electronic Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
TP1-136	모오스 구동 싸이리스터 기반 전기자동차용 전자식 프리차지 스위치 모듈 정동윤 ¹ , 박건식 ¹ , 김상인 ² , 원종일 ¹ , 장현규 ¹ , 이용하 ³ ¹ 한국전자통신연구원 반도체소부장기술센터, ² 갑승파워시스템, ³ ㈜와이테크

	Analysis on the Impact of Charge Traps in FeTFET
TP1-137	Yun Seo Choi ¹ , Seungwon Go ¹ , Tae-Hyeon Kim ² , Sihyun Kim ¹ , and Sangwan Kim ¹
	¹ Department of Electronic Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
	A Simulation Study of Heterojunction FeFET with SiGe Body for Efficient Erase Operation
TP1-138	Taegun Kim, Dong Keun Lee, Sihyun Kim, and Sangwan Kim
	Department of Electronic Engineering, Sogang University
TP1-139	Multi-bit Vertical Ferroelectric-Metal Field-Effect Transistor (V-FeMFET) Weight Cell for Neuromorphic Computing Heebum Kang ¹ , Seungmin Kang ¹ , Tae-Hyeon Kim ² , Sangwan Kim ¹ , and Sihyun Kim ¹ ¹ Department of Electronic Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
TP1-140	The Impact of Annealing Conditions on the Switching Performance and Grain Size of Metal-Ferroelectric (Hf _{0.5} Zr0. ₅ O ₂)-Metal Capacitor
171-140	Jiyeong Yoon and Changhwan Shin
	School of Electrical Engineering, Korea University
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TP1-141	Hyeonjung Park ¹ and Changhwan Shin ²
	¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² School of Electrical Engineering, Korea University
	Simulation Studies of Gate-injection Ferroelectric Flash (GI FeFlash)
TP1-142	Yelim Jeon ¹ , Hyungju Noh ¹ , Tae-Hyeon Kim ² , Sihyun Kim ¹ , and Sangwan Kim ¹
	¹ Department of Electrical Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
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	¹ Center for Neuromorphic Engineering, KIST, ² Department of Materials Science and Engineering, Korea University, ³ Materials Science & Engineering, Seoul National University, ⁴ Department of Materials Science & Engineering, Seoul National University of Science and Technology, ⁵ Division of Nano & Information Technology, Korea University of Science and Technology
	T-CMOS SPICE Compact Modeling for Low Power Ternary-SRAM Design
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	¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
	Simulation Study on Accumulation Mode GAAFET with Low Threshold Voltage
TP1-145	Eungyo Jang and Changhwan Shin
	School of Electrical Engineering, Korea University
TP1-146	Machine-learning Model to Predict the LER (line-edge-roughness)-induced Random Variation in
	GAAFET
	Myongjin Kim and Changhwan Shin
	Department of Electrical Engineering, Korea University
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TP1-147	Min Jae Kim ¹ , Sang Hyo Ahn ¹ , Yoo Bin Song ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Ternell Corp

TP1-148	Non-uniform Interface Trap Density by Halo Ion-implantation Process for Flicker Noise Estimation Yoo Bin Song, Sang Hyo Ahn, Min Jae Kim, Min Woo Ryu, and Kyung Rok Kim Department of Electrical Engineering, UNIST
TP1-149	Roadmap for Ferroet And Ferroet Memory Taebaek Lee and Cha
TP1-150	Comparative TCAD Analysis of Single-event Transients in Forksheet FETs and Bottom Dielectric Isolation-integrated Forksheet FETs Gunhee Choi and Jongwook Jeon Department of Electrical and Computer Engineering, Sungkyunkwan University
TP1-151	Effects of Tunnel Oxide on Reliability in Charge Trap Flash Memory Devices Jaekyun Son ¹ , Jae Yeon Park ¹ , Tae-Hyeon Kim ² , Sihyun Kim ¹ , and Sangwan Kim ¹ ¹ Department of Electronic Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
TP1-152	Analysis of Memory Characteristics in Charge Trap Flash Devices Depending on Tunnel Oxide Jinhong Lee ¹ , Jae Yeon Park ¹ , Jaekyun Son ¹ , Tae-Hyeon Kim ² , Sihyun Kim ¹ , and Sangwan Kim ¹ ¹ Department of Electronic Engineering, Sogang University, ² School of Electrical and Computer Engineering, Georgia Institute of Technology
TP1-153	High-Speed THz Imager for Low-Noise THz Imaging Applications Sang Hyo Ahn ¹ , Min Jae Kim ¹ , Yoo Bin Song ¹ , Myoung Kim ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
TP1-154	Characteristics of Molybdenum and Molybdenum Dioxide Thin Films for IGZO-Based Transistors Min-Su Cho, Hyun-June Park, and Sung-Woong Chung POSTECH
TP1-156	M3D Hybrid Inverter Using Si p-FETs and Indium-Based Oxide Semiconductor n-TFTs Sun Bum Kim ¹ , Chan Seul Lee ¹ , Gyu Lee Kim ¹ , Jae Seok Hur ² , Ho Young Lee ³ , Jae Kyeong Jeong ² , and Changhwan Choi ¹ ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Electronic Engineering, Hanyang University, ³ Department of Nanoscale Semiconductor Engineering, Hanyang University
TP1-157	A Novel Hybrid Ferroelectric Charge Trap Layer Gate-Injection Flash Hyungju Noh, Yelim Jeon, Sihyun Kim, and Sangwan Kim Department of Electrical Engineering, Sogang University
TP1-158	A 28-nm Ternary Dual-Port SRAM Cell for Area and Power Efficient On-chip Memory Myoung Kim ^{1,2} , Young Eun Choi ¹ , Woo-Seok Kim ¹ , Yesong Jeong ¹ , Jun Young Park ¹ , Sang Hun Yeo ¹ , Kwan Yong Lee ¹ , In Jun Jang ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
TP1-159	Binary-to-Ternary and Ternary-to-Binary Data Converters for Ternary Memory Interface Yesong Jeong ¹ , Myoung Kim ^{1,2} , Young-Eun Choi ¹ , Woo-Seok Kim ¹ , Jun Young Park ¹ , Sang Hun Yeo ¹ , Kwan Yong Lee ¹ , In Jun Jang ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2} ¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
TP1-160	Heterogeneous Inverter Using N-type IGTO TFT Prepared by Atomic-layer Deposition with Various Channel Compositions on the P-type Si FET Chan Seul Lee, Sun Bum Kim, Gyu Ri Kim, and Changhwan Choi Division of Materials Science and Engineering, Hanyang University

TP1-161	Investigation of Self-heating Effects in SOI L-shaped MOSFET with Various Position of the AI_2O_3 Heat Sink Using 3-D TCAD Simulation
	Un-hyun Im ¹ , Dogyun Ahn ¹ , Tae-young Yun ¹ , Jang Hyun Kim ¹ , and Sangwan Kim ² ¹ Ajou University, ² Sogang University
	Implementing Excitatory and Inhibitory Properties in a Neuron Circuit Using Feedback Field Effect
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	Department of Electronic Engineering, Gangneung-Wonju National University
	TCAD-Based Analysis and Modeling of Process Variations in 28-nm Ternary-CMOS Technology
TP1-163	Kwan Yong Lee ¹ , Woo-Seok Kim ¹ , Young-Eun Choi ¹ , Myoung Kim ^{1,2} , Sang Hun Yeo ¹ , Ye song Jeong ¹ , Jun Young Park ¹ , In Jun Jang ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2}
	¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
	A Novel Variation Tolerant Silicon-Based Source/Channel Junctionless Ternary Tunnel FET for Energy Efficient Logic and Memory Applications
TP1-164	Sang Hun Yeo ¹ , Woo-Seok Kim ¹ , Young-Eun Choi ¹ , Myoung Kim ^{1,2} , Kwan Yong Lee ¹ , In Jun Jang ¹ , Jun Young Park ¹ , Yesong Jeong ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2}
	¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
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TP1-165	Woo-Seok Kim ¹ , Young-Eun Choi ¹ , Myoung Kim ^{1,2} , Sang Hun Yeo ¹ , Kwan Yong Lee ¹ , In Jun Jang ¹ , Jun Young Park ¹ , Yesong Jeong ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2}
	¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
	Highly Scalable Nanosheet Based Ternary-CMOS Technology
TP1-166	In jun Jang ¹ , Woo-Seok Kim ¹ , Young-Eun Choi ¹ , Myoung Kim ¹ , Sang Hun Yeo ¹ , Kwan Yong Lee ¹ , Jun Young Park ¹ , Yesong Jeong ¹ , Min Woo Ryu ^{1,2} , and Kyung Rok Kim ^{1,2}
	¹ Department of Electrical Engineering, UNIST, ² Ternell Corp.
	Nanohole Patterning by Inner-Sidewall Spacer through Time-Controlled Dry Etching
TD1 167	Soomin Kim ¹ , Gyuhoon Lee ² , Yeji Lee ^{3,} Sungjun Kim ² , and Seongjae Cho ¹
TP1-167	¹ Department of Electronic and Electrical Engineering, Ewha Womans University, ² Department of Electronics and Electrical Engineering, Dongguk University, ³ Department of Electrical and Electronic, and Control Engineering, Hankyoung National University
	A Single-Body Integrated Ultra-Low-Power Logic-Memory Cell
TP1-168	Gyuhoon Lee ¹ , Soomin Kim ² , Yeji Lee ³ , Myounggon Kang ⁴ , Sungjun Kim ¹ , and Seongjae Cho ²
	¹ Department of Electronics and Electrical Engineering, Dongguk University, ² Department of Electronic and Electrical Engineering, Ewha Womans University, ³ Department of Electrical and Electronic, and Control Engineering, Hankyoung National University, ⁴ Department of Electronics Engineering, Korea National University of Transportation

I. MEMS & Sensors Systems 분과

TP1-169	A Microfabrication Technology Platform of MEMS Actuating Structure and its Pull-in Voltage Characteristics Ju Chan Choi, Kwan Soo Kim, Seung Han Ryu, Ji Do Kim, Kwang Woong Jeong, Tae Won Lee, Jung Hun Choi, Myung Hee Nam, and Jeong Soo Park SK hynix system ic
TP1-170	MEMS Microphone의 Wafer Level Pull-in Voltage 측정 안정화를 위한 Probe Station의 Wafer Edge Vacuum 방식 Chuck 적용 효과 Keun Hye Choi, Ju Chan Choi, Jung Hun Choi, Myung Hee Nam, and Jeong Soo Park SK hynix system ic
TP1-171	Wafer Level Test에서 Probe Tip의 Contact 압력 및 위치에 따른 MEMS Microphone의 Pull In Voltage 특성 변화 Se Been Jung, Ju Chan Choi, Jung Hun Choi, Myung Hee Nam, and Jeong Soo Park SK hynix system ic
TP1-172	Ultra-Sensitive Charge Based Antibiotics Detection Inside a Micro-Dielectrophoretic Device Young Woo Gwak, Gyeong Jun Min, Jin Seon Park, and Sang Woo Lee Department of Biomedical Engineering, Yonsei University
TP1-173	HMDSO Poisoning Prevention with CeO ₂ -rGO/Pd/ZnO MOS Film Rajesh Gudala, Seung Jun Jeon, Gab Joong Jeong, and Yun Sik Lee SensorWithU Co., Ltd.
TP1-174	Nanoporous MoS ₂ FET-Based Bioelectric Sensor Capable of Highly Selective and Sensitive Detection of Ethanol by Mimicking Drosophila Olfactory System Junoh Shim, Anamika Sen, Heekyeong Park, Arindam Bala, Mincheol Park, and Sunkook Kim Department of Advanced Materials Science and Engineering, Sungkyunkwan University
TP1-175	Effective Detection of BSA with High-performance DG-ISFET and Surface Treated Sensing Membranes Dong-Gyun Mah, Yeong-Ung Kim, and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University
TP1-176	 pH-dependent Tunable Sensitivity in Electric-Double-Layer Transistors with Extended-Gate for Neuromorphic Biosensors Dong Hee Lee, Hwi-Su Kim, and Won-Ju Cho Department of Electronic Materials Engineering, Kwangwoon University
TP1-177	Nano-physical Unclonable Function Created by Nanopatterns from Block Copolymer Self-assembly Hyeon Ju Ko, Jang Hwan Kim, and Sang Ouk Kim Department of Material Science & Engineering, KAIST
TP1-178	High-current, Large-area Organic Photovoltaics for Indoor Applications Jooyeong Kim ¹ , Hyojeong Choi ² , Selim Han ² , Biswas Swarup ² , and Hyeok Kim ² ¹ Department of Intelligent Semiconductor Engineering, University of Seoul, ² School of Electrical and Computer Engineering, University of Seoul
TP1-179	Magnetic Hydrogel를 활용한 초소형 생분해성 로봇 구동 이어진, 정구윤, 유정민, 박윤석 경희대학교 정보전자신소재공학과

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TP1-180	Youngbin Yoon, Youngki Kim, Wan Sik Hwang, and Myunghun Shin
	Korea Aerospace University
TP1-181	환경 반응형 마이크로 액추에이터
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	경희대학교 정보전자신소재공학과
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	Electrical and Computer Engineering, Sungkyunkwan University
	Magnetically Actuated TENG for Wireless Energy Transfer
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	² Department of Electronic Engineering, Gachon University
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TP1-187	정구윤, 김준엽, 최태훈, 박윤석
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	Department of Electronic Engineering, Jeju National University
	Effect of Forming Gas Annealing on Reliability of Embedded Poly-silicon Micro-heater
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	Shin, Min-Kyu Park, Joon Hwang, Jae-Joon Kim, and Jong-Ho Lee
	Department of Electrical and Computer Engineering and ISRC Seoul National University
	Dry EEG Electrodes Integrated with Earphones
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	Department of Electrical and Computer Engineering, Sungkyunkwan University
	Wireless, Epidermal Platform for Diagnosing Pulmonary Disease
TP1-191	Hee Kyu Lee and Sang Min Won
	Department of Electrical and Computer Engineering, Sungkyunkwan University
	인체부착형 마찰전기 기반 움직임 감지 센서
TP1-192	한윤승 ¹ , 윤홍준 ² , 박윤석 ¹
	¹ 경희대학교 정보전자신소재공학과, ² 가천대학교 전자공학부
	Enhancement of Gas Sensing Performance in CuO Resistor-type Gas Sensors via Pre-Bias Voltage
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	Department of Electrical and Computer Engineering and Inter–university Semiconductor Research Center, Seoul National University

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TP1-195	Self-driven Resistive Pulse Sensing System for Point-of-care Diagnostic Hyunjun Kim, June Soo Kim, Jae Yong Lee, Seung Deok Kim, Noah Jang, Jiajie Wang, Da Ye Kim, Yujin Nam, Jinkyung Kim, Maeum Han, and Seong Ho Kong Kyungpook National University
TP1-196	Wearable Capacitive Type Hydration Sensor Packaged in Porous PDMS for Breathability Hyejun Kim, Seongu Kim, and Jeonghyun Kim Department of Electronics Convergence Engineering, Kwangwoon University
TP1-197	Development of Flexible and Transparent Bilayer Electrodes for Aptamer Biosensors Using Graphene and PEDOT:PSS Sookyeong Kim and Dong-Wook Park School of Electrical and Computer Engineering, University of Seoul
TP1-198	IZO/ZnO Nanowire Heterostructure for Enhanced Biomolecule Sensing Performance of Heterostructure-Based TFT June Soo Kim, Jae Yong Lee, Seung Deok Kim, Da Ye Kim, Hyunjun Kim, Noah Jang, Jiajie Wang, Yujin Nam, Jinkyung Kim, Maeum Han, and Seong Ho Kong School of Electronics and Electrical Engineering, Kyungpook National University
TP1-199	Nanostructured Zinc Oxide Thin Film : An Innovative pH Sensing Solution Noah Jang, June Soo Kim, Maeum Han, Seung Deok Kim, Jae Yong Lee, Jiajie Wang, Hyunjun Kim, Da Ye Kim, Yujin Nam, Jinkyung Kim, and Seong Ho Kong School of Electronic and Electrical Engineering, Kyungpook National University
TP1-202	SPAD-Based LiDAR Sensor with Adaptive Power Saving Scheme Dahwan Park, Eun-chang Lee, Min-Kyu Kim, Sang-Young Lee, Yong-Seop Lee, Min-Seok Shin, and Hoesam Jeong SK hynix
TP1-203	A Smart Ring for Real-Time Blood Pressure Monitoring Junyeong Lee, Minjoo Lee, and Jeonghyun Kim Department of Electronic Convergence Engineering, Kwangwoon University
TP1-204	Non-Invasive Real-Time Blood Glucose Monitoring for Reducing Variations in Diabetes Care Seongu Kim and Jeonghyun Kim Department of Electronic Convergence Engineering, Kwangwoon University
TP1-205	Integration of Semiconductor Components Using 3D Stacking Technology and Sensory-Neuromorphic Application Se Gi Lee and Sang Min Won Sungkyunkwan University
TP1-206	Application of Innovative Wiring Method of In-vivo Experiments Janghoon Joo and Sang Min Won Sungkyunkwan University
TP1-207	Flexible Artificial Tactile System Using Morphotropic Phase Boundary of Hf _x Zr _{1-x} O ₂ Thin Film by Low-Temperature Annealing Seungyeob Kim, Minhyun Jung, Jinwook Ha, and Sanghun Jeon School of Electrical Engineering, KAIST

TP1-208	단일 벽 탄소 나노튜브를 이용한 황화수소 가스 센서의 제작과 검출
	Ryang Ha Kim, Kyung Eun Kim, Beom Jun Jung, and Young Lae Kim
	Electronic Engineering, Gangneung-Wonju National University
TP1-209	Wireless, Battery Free Temperature Sensor based on Morphotropic Phase Boundary of $Hf_xZr_{1\mathchar`s}O_2$ Thin Film
	Ketong Yang, Seungyeob Kim, Minhyun Jung, and Sanghun Jeon KAIST
TP1-210	In-Situ Electron Density Measurement in Inductively Coupled Plasma Using Microwave Reflectometer by Wi-Fi Antenna on Wafer
	Seong-Yong Lim, Gi-Won Shin, Woo-Jae Kim, Hee-Tae Kwon, Ji-Hwan Kim, In-Young Bang, Jae-Hyeon Kim, Hyeon-Jo Kim, Seong-Hee Cho, Seo-Yeon Kim, and Gi-Chung Kwon
	Department of Electrical and Biological Physics, Kwangwoon University
	P-Type Copper Oxide-Based Solar-blind Ultraviolet (UV) Photodetector Capable of Low-
TP1-211	PhotocurrentOperation with Plasma-Enhanced Atomic Layer Deposition (PEALD)
	Minah Park ¹ , Jaewook Yoo ¹ , Hyeonjun Song ¹ , Soyeon Kim ¹ , Hongseung Lee ¹ , Seongbin Lim ¹ , Seohyeon Park ¹ , Peide D. Ye ² , and Hagyoul Bae ¹
	¹ Jeonbuk National University, ² Purdue University

O. System LSI Design 분과

TP1-212	Two-step Classification Neuron Circuits for Highly Integrated SNN Systems Dahyeon Youn and Soo Youn Kim Department of Semiconductor Science, Dongguk University
TP1-213	An Automatic Salt-water-spray Roadway-deicing System with Surface Detection Ki-Duk Kim ¹ and Hyung-Min Lee ² ¹ C&Tech Co., Ltd., ² Korea University
TP1-214	Aggregator Hardware Design for Preventing Backdoor Attacks in Federated Learning YeJi Lee, JoonSeok Kim, KyuMin Cho, and SeokHyung Kang Department of Electrical Engineering, POSTECH
TP1-215	50G-PON 용 LDPC 부호기 FPGA 구현 최정원 ¹ , 김광옥 ² , 두경환 ² , 정환석 ² , 이영주 ¹ ¹ 포항공과대학교 전자전기공학과, ² 한국전자통신연구원 입체통신연구소 네트워크연구본부 광네트워크연구실
TP1-216	HDL Code Coverage Verification Method 엄유진, 양희훈, 김도훈, 유호영 충남대학교 전자공학과
TP1-217	GPS CRC-24Q 디코더 구현 황용택, 황지우, 유호영 충남대학교 전자공학과
TP1-218	노외중성자속 감시계통 검증을 위한 FPGA 신호생성기 개발 신건, 양희훈, 박요한, 노윤진, 유호영 충남대학교 전자공학과
TP1-219	링 오실레이터 인버터 수에 따른 PUF 성능 분석 박지호, 양희훈, 유호영 충남대학교 전자공학과
TP1-220	Verification Methodology for Rate Control Unit in VDC-M Decoder Jiyoung Lee, Huijin Roh, Sohyeon Kim, Saeyeon Kim, Hannah Yang, and Ji-Hoon Kim Department of Electronic and Electrical Engineering, Ewha Womans University
TP1-221	Multi-Stage Rate Control Architecture for VDC-M Decoder Huijin Roh, Jiyoung Lee, Sohyeon Kim, Saeyeon Kim, Hannah Yang, and Ji-Hoon Kim Department of Electronic and Electrical Engineering, Ewha Womans University
TP1-222	Design of Custom DRAM Memory Controller for ALPG Testing Seoyeon Park, Saeyeon Kim, Eunkyung Ham, Sunyoung Park, and Ji-Hoon Kim Department of Electronic and Electrical Engineering, Ewha Womans University

Q. Metrology, Inspection, Analysis, and Yield Enhancement 분과

ZONE 3 (2층 로비)

TP1-223	비 균일 격자 마크를 이용한 라지 오버레이 문제 개선 방법
	Hyun Chul Lee ^{1,3} , Hyun Jin Chang ¹ , Ho Sung Woo ² , and Won Gyu Lee ³
	¹ AUROS Technology, Inc., ² Korea National Open University, ³ Korea University
TP1-224	Power Spectral Density Analysis for SEM Line Pattern Roughness
	Ra Seong Ki ¹ , Jong Hoi Cho ¹ , Sung Hun Lim ¹ , Jun Ho Lee ¹ , Hyun Jin Lee ¹ , Ran Alkoken ² , You Jin Kim ² , Jeong Ho Yeo ² , Jung Woo Sung ³ , Kyung Ju Han ³ , and Kyu Young Kim ¹
	¹ SK hynix, ² PDC Business Group, Applied Materials, Israel, ³ PDC Business Group, Applied Materials, Korea
TP1-225	Fourier Transform Infrared Spectroscopy(FTIR) in Characterizing Borophosphosilicate Glass(BPSG)
	MinYoung Lee
	Semilab Korea Co., Ltd.
	Development of Dual-rotating Polarizer Spectroscopic Ellipsometry
TP1-226	Jongkyoon Park, Sukhyun Choi, Yong Jai Cho, Junho Choi, and Chegal Won
	Division of Advanced Instrumentation Institute, KRISS
TP1-227	CMOS Image Sensors의 암 전류를 감소시키는 Shallow Trench Isolation(STI) Sac Oxide Process Module 적용
11 1 227	Cheoleon Park, Jea Young Park, Ki Young Kim, Sun Choi, and Won Ho Lee
	R&D Division, SK hynix system ic
	Determination of Outdoor Airborne Nano-particle Impact on Defect by Development of New Data
TP1-228	Processing Algorithm
	Jongmin Lee ^{1,2} , Jungtae Park ^{1,2} , II–Jin Kim ² , Haeun Lee ² , and Sehoon Park ²
	¹ Department of Materials Science & Engineering, Yonsei University, ² Samsung Electronics Co., Ltd.
TD1 000	CIS (CMOS Image Sensor) BSI 제품 ML (Micro Lens) Stripe Defect 개선 방법
TP1-229	Joo Young Jeong, Ki Young Kim, Han Yi Jin, Sun Choi, and Won Ho Lee
	R&D Division, SK hynix system ic
TD1_220	Real-time 3D Surface Reconstruction of Deflectometry Using Deep Learning In-Kyu Park ^{1,2} , Young-Sik Ghim ^{1,2} , and Hyug-Gyo Rhee ^{1,2}
TP1-230	
	¹ KRISS, ² Department of Precision Measurement, UST
	In-depth DOS Profiles in Solution-processed IZO Semiconductor Depending on the In Doping Using Photocurrent Spectroscopy
TP1-231	Dongwook Kim ¹ , Hyeonju Lee ¹ , Soo-Kyoung Cha ² , Chanho Jeong ² , Youngjun Yun ² , and Jaehoon Park ²
	¹ School of Information Science, Hallym University, ² School of Semiconductor Display Technology, Hallym
	University
	Contact Holes in Vertical Electrode Structures Analyzed by GISAXS
TP1-232	Gyungtae Kim ¹ , Tae Gun Kim ¹ , Young Jun Chang ² , and Young Yong Kim ³
	¹ NNFC, ² University of Seoul, ³ Pohang Accelerator Laboratory
	Physically Unclonable Functions via Disordered Heteronanostructure of 2D Semiconducting Material:
TP1-233	Enhancing Entropy and Parameter Space
	Jaeseo Park ¹ , Jun Oh Kim ¹ , Won Chegal ¹ , and Sang-Woo Kang ^{1,2}
	¹ Advanced Instrumentation Institute, KRISS, ² Precision Measurement, UST
	Enhancement of Electrical Properties in MOCVD–Grown MoS ₂ –Based Field–Effect Transistors: A Comparative Study of Contact Strategies
TP1-234	Junghyun Lee ^{1,2} , Jaeseo Park ¹ , Hyeonji Kim ^{1,3} , Bongjoong Kim ² , Sang-Woo Kang ^{1,4} , and Jun Oh Kim ¹
	¹ KRISS, ² Hongik University, ³ Kyungpook National University, ⁴ UST

TP1-235	EUV 펠리클에 포집된 임계 크기의 입자가 마스크 이미지 전사특성에 미치는 영향에 대한 실험적 연구
	문승찬 ^{1,3} , 이동기 ^{2,3} , 홍준호 ^{2,3} , 안진호 ^{1,2,3}
	¹ 한양대학교 나노반도체공학과, ² 한양대학교 신소재공학과, ³ EUV-IUCC
	Parameter Optimization for Precision Improvement in Thickness Measured with Spectroscopic
TD1 000	Ellipsometry
TP1-236	Inhee Joh, Seojin Park, Myeongrok Oh, Hwanseong Moon, Mita Park, Kyusik Kim, and Tae Dong Kang
	AUROS Technology, Inc.
	Imaging Spectroscopic Ellipsometer based on One-piece Polarizing Interferometer: Characterization
	of the 2D Van Der Waals Materials
TP1-237	Suk Hyun Choi ^{1,2} , Guk Hyeon Hwang ¹ , Saeid Kheiryzadehkhanghah ¹ , Yong Jai Cho ² , Junho Choi ² ,
	Jongkyoon Park ² , Won Chegal ² , and Dae Suk Kim ¹
	¹ Jeonbuk National University, ² KRISS
	Optical Simulation of Measurement Sensitivity on Critical Dimension of Cu Micro-bumps for Semiconductor Packaging Process
TP1-238	Shinyoung Ryu ¹ , Jiwon Lee ¹ , Minhyeok Lee ¹ , Kwangwoo Kim ² , Jongjeong Kim ² , and Tae Dong Kang ¹
	¹ AUROS Technology, Inc., ² Haedosa, Inc.
	Development of High Precision Micro-ellipsometer Enhanced by Pixelated Polarizing Camera
TP1-239	Dong-Geun Yang ^{1,2} , Young-Sik Ghim ^{1,2} , and Hyug-Gyo Rhee ^{1,2}
	¹ Optical Imaging and Metrology Team, KRISS, ² Department of Measurement Engineering, UST
	반사형 대물렌즈를 이용한 분광 타원계측기 편광상태 변화 보정
TP1-240	서선일, 주기남
	조선대학교
	고분해능 3차원 패턴 조사 현미경
TP1-241	조민서, 박종규, 주기남
	In-situ Liquid Cell TEM Study for Water Splitting Using Mesoporous Graphitic Carbon Nitride Hetero-structures
TP1-242	V. Navakoteswara Rao, Jung Ho Yoo, and Jun-Mo Yang
	Nano-convergence Technology Division, NNFC, KAIST
	Reverse Engineering Case Study Using Atomic Layer Ion Beam Delayer
TP1-243	Jon Won Koh, Myung Keun Lee, Seung Joon Cha, and Yun Chang Park
	NNFC
	터보분자펌프 성능 평가 지표의 신뢰성 향상을 위한 계측기 보정 및 분산 데이터의 통계학적 프로세싱 방법에
	관한 연구
TP1-244	민병현 ^{1,3} , 임성규 ² , 문지훈 ¹ , 강상우 ^{1,3}
	¹ 한국표준과학연구원 첨단측정장비연구소, ² 나노종합기술연구원 나노공정기술부, ³ 과학기술연합대학원대학교 정밀측 정전공
	벽면 오염에 따른 식각 드리프트 제어 운전 알고리즘 개발
TP1-245	이인규, 유상원, 권지원, 박지훈, 김곤호
	서울대학교 공과대학 에너지시스템공학부
	TLB Coalescing Using Page Table Compression
TP1-246	Tran Dai Duong and Jae Young Hur
	Department of Electronic Engineering, Jeju National University
	점진적 패리티로 대용량 NAND Flash Memory의 Open Block 문제 완화
TP1-247	Min-Jin Oh ¹ and Jaeho Kim ^{1,2}
	¹ School of Aerospace and Software Engineering, Gyeongsang National University, ² Department of Al Convergence Engineering, Gyeongsang National University

TP1-248	Deep Learning Computation Acceleration through Automatic Tuning of Execution Code Yongin Kwon ^{1,2} ¹ ETRI, ² UST
TP1-249	ZNS SSD의 자원 경쟁 분석 Gyupin Moon and Donghyun Kang Gachon University
TP1-250	Improved Mobile Application Performance through Database Redesign Jung Kyu Park ¹ , and Eun Young Park ² ¹ Changshin University, ² Shinhan University
TP1-251	PoP-Cache: Hit Ratio예측과 사전 퇴출에 의한 캐시 교체 정책의 성능 향상 Hyemi Jeong ¹ , Kyeongmin Kim ¹ , and Jeaho Kim ^{1,2} ¹ School of Aerospace and Software Engineering, Gyeongsang National University, ² Department of Al Convergence Engineering, Gyeongsang National University

S. Chip Design Contest 분과

TP1-252	An Arbitrary-Waveform-Capable Neural Stimulator in Standard CMOS
	Jeongyoon Wie and Junghyup Lee
	Electrical Engineering and Computer Science, DGIST
TP1-253	A Pulse Counting Digital FOCV-Based 41ms Fast Startup Compact Boost Converter for TEG in 28 nm CMOS Seong-Yeon Moon, R. M. I. U. Rajapaksha, and Jong-Wook Lee
	Department of Electronics and Information Convergence Engineering, Kyung Hee University
TP1-254	Object Tracking Lensless Light Field Image Sensor through a Parallel Bank of Pulse Computational Blocks and Post DNN Using Robot Arm-Based Data Collection Sang Hoon Hong, Eon Gyeong Lee, and Han Joon Kim Kyung Hee University
TP1-255	Wide Bandwidth Continuous-time Noise Shaping SAR ADC Sein Oh, Gichan Yun, and Minkyu Je School of Electrical Engineering, KAIST
TP1-256	Wide Bandwidth and High PSR Capacitorless LDO Regulator based on Flipped-voltage Follower Jaerim Baek, Daejeong Kim, and Hyun sun Mo Kookmin University
TP1-257	Design of a Phase-locked Loop with Improved Jitter Performance Using Phase-lock Detector Young Hun Kim, Dae Jeong Kim, and Hyun Sun Mo Kookmin University
TP1-258	A 1.4-8 Gb/s Low Power Quarter-rate Single-loop Reference-less Clock and Data Recovery Circuit with Unlimited Capture Range Jin-Ho Kim, Yujin Na, and Jin-Ku Kang Department of Electrical and Computer Engineering, Inha University
	부채널 신호 분석을 위한 하드웨어 백도어 IC 칩 설계
TP1-259	Sun Bhin Kim, Jun Hui Nam, and Dong Kyue Kim Department of Electronics Engineering, Hanyang University
	Power Management IC for Supercapacitor Applications
TP1-260	Donghyun Kim, Jaehyeong Lee, Jinwoo Oh, Seungyeon Lee, Jeonghee Jeon, and Joongho Choi University of Seoul
TP1-261	Portable Functional Brain Imaging Chip for Frequency-Domain NIRS Method Bumjun Koh, Kyeongha Kwon, and Hyeon-Min Bae KAIST
TP1-262	A Mobile 3D-CNN Processor with Dual-Grained Sparsity-Aware Computation Utilizing Inter-Frame Variation Seungbin Kim, Hoichang Jeong, Wuyoung Jang, Keonhee Park, Bokyoung Seo, Jueun Jung, and Kyuho Lee UNIST
TP1-263	A 8GHz Delay Locked Loop with 16 Multi-phases in 65nm CMOS for BOST Skew Compensation Jinsoo Bae, Jongchan An, Gwangmyeong An, Hyunsu Jang, Taeho Kim, Myeongju Park, Yoonsang Lee, Songi Cheon, Hyanghee Park, Yunseo Song, and Junyoung Song Department of Electronics Engineering, Incheon National University

TP1-265	Dual-band CMOS Down-conversion Mixer for 5G NR FR2 Applications Eunsoo Kim, Segyeong Kim, Gyuwon Kim, and Junghwan Han Chungnam National University
TP1-266	A Temperature Insensitive 4GS/s 7-bit Time-domain ADC Using Replica Feedback Loop Gyuchan Cho and Jintae Kim Konkuk University
TP1-267	Binary Neural Networks Using Nanoelectromechanical Memory Switches Geun Tae Park ^{1,2} and Woo Young Choi ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² ISRC, Seoul National University
TP1-268	Switching Voltage Analysis of Nanoelectromechanical Memory Switches Jin Wook Lee ^{1,2} and Woo Young Choi ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² ISRC, Seoul National University
TP1-269	Energy-efficient Computing-in-memory Based System-on-chip Controlled by RISC-V Processor Jihoon Park ¹ , Hyunmyung Oh ² , Jehun Lee ¹ , Jaeyong Jang ¹ , Sanghyeok Han ¹ , and Jae-Joon Kim ¹ ¹ Seoul National University, ² POSTECH
TP1-270	Upper-mid Band 6G향 커플러 기반 차동 신호 보상 기법이 적용된 전압제어 발진기 소형화 연구 윤태영 ^{1,2} , 오정석 ^{1,2} ¹ 서울대학교 전기정보공학부, ² 서울대학교 뉴미디어통신공동연구소
TP1-271	Seamless Noise Buck-boost Converter with Continuous Input/Output Current Seokhee Han, Jaewon Ryu, and Chulwoo Kim Department of Electrical Engineering, Korea University
TP1-272	Efficient CNN Accelerator: Enabling Inference and Training Geonhui Jang, SangBo Park, GiTae Park, Thaising Taing, Bogeun Jung, and Hyungwon Kim Chungbuk National University
TP1-273	A 4-GS/s 6-bit PVT-Variation Tolerant Time-Domain ADC with Delay Locked Loop Doona Song, Gyuchan Cho, and Jintae Kim Konkuk University
TP1-274	Triple-stacked Distributed Amplifiers Using CMOS 28 nm Process Hosung Kang, Cheonsang Song, and Jihoon Kim Kyonggi University
TP1-275	Area Efficient DAC with Switched Capacitor Amplifier Scheme for AMOLED Source Driver IC Min-Woo Kim, Sang-Min Lee, Yu-Guan Kim, Won-Jo Lee, Yun-Su Kim, and Byung-do Yang Department of Electronics Engineering, Chungbuk National University
TP1-276	A D-band Low Noise Amplifier based on 28-nm CMOS Technology Wooyong Keum, Jaeman Lee, Giyeong Nam, Jaewon Jang, Minsuk Choi, and Jae-Sung Rieh School of Electrical Engineering, Korea University

TP1-277	180nm 공정을 사용한 최대 1.6A의 전류를 구동할 수 있는 다상 통합 전압 레귤레이터 설계 김기원, 김경민, 정현준, 김소영 성균관대학교 정보통신대학
TP1-278	A Single-TL, Simultaneous, Bi-directional, Skew-compensated and Multi-access Transceiver Link System Seong-Min Ko, Jun-Hyeok Park, and Dong-Woo Jee Ajou University
TP1-279	시스템 EMI 노이즈 분석을 위한 On-chip Embedded 오실로스코프의 설계 Kyung Hoon Lee and Jin Gook Kim UNIST
TP1-280	LR-SoC: A Lightweight RISC-V SoC Min Young Lee ¹ , Soo Min Rho ¹ , Chan Hoon Kim ¹ , Dae Eun Wi ¹ , Sang Soo Park ² , and Ki-Seok Chung ¹ ¹ Hanyang University, ² Device Solutions, Samsung Electronics Co., Ltd.
TP1-281	다중 분할 병렬 결합 변압기를 사용한 5G 고효율 전력증폭기 설계 김근태, 오규택, 유상진, 이옥구 부산대학교 전기전자공학과
TP1-282	 Highly Sensitive Plasmonic Terahertz Detector with Integrated Sub-wavelength Aperture based on Trantenna Min Jae Kim¹, Sang Hyo Ahn¹, Yoo Bin Song¹, Min Woo Ryu^{1,2}, and Kyung Rok Kim^{1,2} ¹Department of Electrical Engineering, UNIST, ²Ternell Corp.
TP1-283	저전력 37uW 10-bit 모노토닉 축차비교형 아날로그-디지털 변환기 신솔몬 ¹ , 최예광 ¹ , 권미정 ¹ , 박유현 ¹ , 이원제 ² , 장성민 ² , 김영식 ¹ ¹ 한동대학교 전산전자공학부, ² 한동대학교 전산전자공학과
TP1-284	A 28-Gb/s ISI-Resistant Digital CDR with Extended Pattern Utilization Suil Kang and Kwanseo Park Yonsei University
TP1-285	A 57-66 GHz Up-converter in 65-nm CMOS for WiGig Applications Geon Woo Park, Jin Man Myung, Ho Kim, Seungjik Lee, and Ilku Nam Pusan National University
TP1-286	Two-stage Operational Transconductance Amplifier with Controllable Reference Voltage Jiwon Lee and Byoungho Kim Hanyang University
TP1-287	저전력 센서 어플리케이션을 위한 Glitch-free 포스트 디바이더를 포함한 광대역 주파수 생성기 정상돈, 전정훈 Department of Semiconductor and Display Engineering, Sungkyunkwan University
TP1-288	A 230-GHz ×12 Frequency Multiplier Chain in 250-nm InP HBT Technology Giyeong Nam, Wooyong Keum, Jaeman Lee, Jaewon Jang, Minseok Choi, Myeongjae Kim, and Jae Sung Rieh School of Electrical Engineering, Korea University
TP1-289	Electromagnetic Analysis Countermeasure Circuit for AES Crypto Module Dongmin Lee and Byong-Deok Choi Department of Electronic Engineering, Hanyang University

	교차 쌍대의 증폭 구조를 활용한 소형화된 Upper-mid 대역 이득 가변 가능한 능동형 양방향 위상천이기
TP1-290	박의찬 ^{1,2} , 오정석 ^{1,2}
	¹ 서울대학교 전기정보공학부, ² 서울대학교 뉴미디어통신공동연구소
	실시간 저면적 BDS B1C 수신기 구현
TP1-291	황용택, 황지우, 이유석, 유호영
	충남대학교 전자공학과
	A Low Power and Compact 12bit 17MS/s SAR-ADC with Dual-Split Capacitor DAC
TP1-292	Taell Hwang, Malik Summair Asghar, and HyungWon Kim
	Department of Electronic Engineering, Chungbuk National University
	A Low-Power, Low-Noise 3rd-Order Delta-Sigma ADC Using an Inverter-Based Pseudo-Pseudo Differential Integrator
TP1-293	Dong-Jick Kim and Jae Hoon Shim
	Kyungpook National University
	A Bandwidth and Resolution Reconfigurable Noise-Shaping SAR ADC for PIM Applications
TP1-294	Dongwook Kim, Donggu choi, Junghyup Lee, and Jong-hyeok Yoon
111 204	DGIST
	Charge Sharing Based Computation-In-Memory for Energy Efficient Machine Learning Algorithm
	Jaehyeon Woo ¹ , Dongho Kim ¹ , Seokhun Kim ¹ , Hongwon Kim ¹ , Taesung Kim ¹ , Sangheon Lee ¹ ,
TP1-295	Junseo Lee ¹ , Jihwan Park ¹ , Inseong Jeon ¹ , Ijun Jang ¹ , Jisu Kang ¹ , Jaeseung Baik ¹ , and Hanwool
	Jeong ^{1,2}
	¹ Kwangwoon University, ² Articron Inc.
	Efficient Pillar-Based 3D Object Detection Accelerator
TP1-296	Minjae Lee, Dowon Kim, and Jungwook Choi
	Hanyang University
TD1 007	A 7-Bit 32x Time-Interleaved SAR ADC with 2-Then-1-Bit/Cycle Conversion
TP1-297	Kyungmin Lee, Jonghyun Kim, and Hyungil Chae
	Konkuk University
TD1_200	Low-voltage Charge Pump based on Internal Gate-bias Boosting for Energy Harvesting Systems
TP1-298	So-Bin Lee and Ickjin Kwon Department of Electrical and Computer Engineering, Ajou University
	A Low-Power IR-UWB CMOS Transmitter for Energy Harvesting Application
TP1-299	Dong-Won Lee and Ickjin Kwon
11 1 200	Department of Electrical and Computer Engineering, Ajou University
	A Scalable Dual-chip Neural Interface System
TP1-300	Joonyoung Lim, Chae-Eun Lee, Chieun Choi, and Yoon-Kyu Song
	Graduate School of Convergence Science and Technology, Seoul National University
	A Low-power 8-b 500MS/s Loop-unrolled SAR ADC with Comparator Offset Calibration
TP1-301	Seunghyun Kim and Minjae Lee
	School of Electrical Engineering and Computer Science, GIST
	A 500-kSPS Split-SAR ADC for Foreground Calibration
TP1-302	Myeong Gyu Gil ¹ and Byoung Ho Kim ¹
	Hanyang University
	A Wideband LO Generator for 5G FR1 Using a Single LC-VCO-Based SSPLL and a Ring-
TP1-303	VCO-Based Fractional-Resolution Frequency Multiplier
	Yongwoo Jo ¹ , Juyeop Kim ¹ , Yuhwan Shin ¹ , and Jaehyouk Choi ²
	¹ KAIST, ² Seoul National University

TP1-304	Dedicated Processing Engines for Depth-wise Separable Convolution Hyeon Seok Hong ^{1,2} and Hyun Kim ^{1,2} ¹ Department of Electrical and Information Engineering, Seoul National University of Science and Technology, ² Research Center for Electrical and Information Technology, Seoul National University of Science and Technology
TP1-305	SRAM-Based Near-Memory Computing for Accelerators for Efficient and Accurate Floating Point Computations Myeong Eun Kwon, Je Hun Lee, and Jae-Joon Kim Seoul National University
TP1-306	웨이블릿 계수 분포를 활용한 Fast Domain Generalization Jin Shin ^{1,2} and Hyun Kim ^{1,2} ¹ Department of Electrical and Information Engineering, Seoul National University of Science and Technology, ² Research Center for Electrical and Information Technology, Seoul National University of Science and Technology
TP1-307	정확하고 효율적인 부동소수점 행렬-곱 연산을 위한 정수 기반 이상치 인지 시스톨릭 배열 뉴럴 네트워크 가속기 Jehun Lee and Jae-Joon Kim Seoul National University
TP1-308	A CNN Accelerator based on 3D NAND Flash Memory with Input Reuse In-Seok Lee, Jae-Joon Kim, and Jong-Ho Lee Department of Electrical and Computer, Seoul National University
TP1-309	Analog 기반 Triplet-Based STDP 학습 알고리즘 On-chip Learning 구조 설계 Hyeon-Seong Im and Jung-Hoon Chun Department of Semiconductor and Display Engineering, Sungkyunkwan University
TP1-310	Neural Network Framework – SW Architecture and Implementation for a Hardware Accelerator for Deep Reinforcement Learning Dohyun Kim, Junghwan Choi, and Shiho Kim ¹ School of Integrated Technology, Yonsei University, ² BK21 Graduate Program in Intelligent Semiconductor, Yonsei University
TP1-311	플래시 메모리 기반의 SNN 인공지능칩 연구 이왕주, 이성현, 김진하, 김상훈, 박정우, 박민아, 정순규, 손민균, 서동우 한국전자통신연구원
TP1-312	Optimizing Cu-CMP via Deep Learning to Predict Polyurethane Pad Durability Seunghwan Lee ¹ , Jaewon Lee ¹ , Pengzhan Liu ¹ , Sanghuck Jeon ¹ , and Taesung Kim ^{1,2} ¹ School of Mechanical Engineering, Sungkyunkwan University, ² SKKU Advanced Institute of Nano Technology, Sungkyunkwan University
TP1-313	Highly Linear Charge Trap/Detrap of Charge Trap FET Using Regulated Pulse Jeong-In Choi and Kee-Won Kwon Department of Electrical and Computer Engineering, Sungkyunkwan University
TP1-314	Training-Aware Fixed-Point Simulation for Deep Learning Model Seung Hwan Yoon and Young Ho Seo Kwangwoon University

TP1-315	Effects of Nonlinear Conductance Update of Synaptic Devices on On-Chip Learning in Hardware Neural Network
	Seung Whan Kim, Jae-Joon Kim, and Jong-Ho Lee
	Seoul National University
TP1-316	CNN Preprocessing Based Embedded Al Strawberry Classifier
	Jinyeol Kim, Jongwon Oh, Joungmin Park, and Seung Eun Lee
	Department of Electronic Engineering, Seoul National University of Science and Technology
	keti.re.kr Neural Network Accelerator with Quantization for Edge Computing
TP1-317	Ji Hun Joe, Min Geon Shin, Han UI Ryu, and Sung Ho Lee
	KETI

U. Bio-Medical 분과

Granular Adhesive for Injection-on-skin Interface
Jaepyo Jang ¹ , Sungjun Yoon ² , and Donghee Son ^{1,2}
¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Artificial Intelligence System Engineering, Sungkyunkwan University
Enhanced Sensitivity of Si BioFETs with Ag Nanowire for CHIKV Virus Detection
Jongmin Son, Wonyoung Choi, Seonghwan Shin, Jeonghyeon Do, and Jeong-Soo Lee
Department of Electrical Engineering, POSTECH
임피던스 바이오센싱 응용 분야를 위한 ECIS 기반의 Multi-well Array Impedance Biosensor 제작 및 연구
Seok Gyu Kim ¹ , Da Hyun Kang ¹ , Su Gwon Nam ¹ , and Moon Gyu Jang ^{1,2}
¹ School of Nano Convergence Technology, Hallym University, ² Center of Nano Convergence Technology, Hallym University
An Implantable UVC Sterilization Capsule Device for Biomedical Applications
Keodan Kim, Sangho Park, and Gunchul Shin
School of Materials Science & Engineering, University of Ulsan
Adhesive Hydrogel-integrated Soft Wearable Liquid Metal Composite Electrode by Direct Laser
Patterning
Jaehyon Kim ¹ and Donghee Son ^{1,2}
¹ Department of Electrical and Computer Engineering, Sungkyunkwan University, ² Department of Artificial Intelligence System Engineering, Sungkyunkwan University
Injectable and Conductive Hydrogels for Neural Interfacing and Regeneration of Biological Tissues
Subin Jin, Heewon Choi, Donghee Son, and Mikyung Shin
Sungkyunkwan University
Comparison of NIH/3T3 Cell Capacitance according to Impedance Pattern Size
Da Hyun Kang ¹ , Seok Gyu Kim ¹ , Su Gwon Nam ¹ , and Moon Gyu Jang ^{1,2}
¹ School of Nano Convergence Technology, Hallym University, ² Center of Nano Convergence Technology, Hallym University
A Highly Power-efficient LDO with Reliable Low Input Voltage Operation
Woojin Jang and Junghyup Lee
DGIST
Self-healing Bioelectronic Artificial Vascular Graft with Antithrombotic Capability
Soojung An, Heewon Choi, and Donghee Son
Department of Electrical and Computer Engineering, Sungkyunkwan University

V. Quantum Technology분과

TP1-328	Quantum Monte Carlo Simulation for Predicting Radiation Therapy Dose Hyeon Seong Jung, Ui Min Lee, Pamul Yadav, Jun Yong Lee, and Shi Ho Kim School of Integrated Technology, Yonsei University
TP1-329	Efficient Node Search in Binary Tree Using Quantum Walk Pamul Yadav, Junyong Lee, Uimin Lee, Hyeonseong Jung, and Shiho Kim School of Integrated Technology, Yonsei University
TP1-330	Implementation of Five-qubit Quantum Information Processing in Silicon Device: A Preliminary Investigation Junghee Ryu and Hoon Ryu KISTI
TP1-331	Constructing Ytterbium Ion Trap System for Quantum Computing Using Cryostat Junhee Cho, Myunghun Kim, Sehyeon Gwon, Keumhyun Kim, Hyegoo Lee, Sangsoo Han, and Moonjoo Lee Department of Electrical Engineering, POSTECH

학부생포스터

TP1-332	A PVT-Compensated 14-Bit Time-to-Digital Converter for LiDAR Applications
	Yongjin Kwon, Yeseung Choi, and Shinwoong Kim
	Department of Electrical and Electronic Engineering, Handong Global University
TP1-333	Skin-adhesive Hydrocolloid Based OLED for Enhanced Light Therapeutics
	Yujin Kwak ¹ , Seohyeon Kim ¹ , Youngwoo Kim ² , DongWoon Lee ² , Yeji Shin ¹ , Eou-Sik Cho ² , Sang Jik Kwon ² , HyoungSoon Youn ³ , JinHong Jeong ³ , and Yongmin Jeon ¹
	¹ Department of Biomedical Engineering, Gachon University, ² Department of Electronic Engineering, Gachon University, ³ T&L Company
	Thickness-dependent Electrical Properties of SnSe ₂ Field-Effect Transistors Using Reactive Ion Etching
	HanWoong Choi ¹ , Jin-Hoo Seong ^{1,2} , Hyo-Chang Lee ³ , Sang-il Kim ⁴ , and TaeWan Kim ¹
TP1-334	¹ Department of Electrical Engineering and Smart Grid Research Center, Jeonbuk National University, ² Advanced Instrumentation Institute, KRISS, ³ Department of Semiconductor Science, Engineering and Technology, Korea Aerospace University, ⁴ Department of Materials Science and Engineering, University of Seoul
	Adaptability of 4D Radar in Autonomous Driving: A PointNet-Based Point Cloud Data Analysis
TP1-335	In Su Lee, Min Jun Kwon, Won Jun Choi, Ki Chan Kim, and Tae Ik Kang
	Department of Electronic Engineering, Myongji University
TP1-336	Double Gate MOSFET 에서의 HKMG 의 적용과 두께 조절을 통한 성능 최적화 연구
	최훈 ¹ , 김대솔 ¹ , 김도엽 ¹ , 전영우 ² , 김동현 ² , 김진우 ³
	¹ 중앙대학교 전자전기공학부, ² 중앙대학교 화학신소재공학부, ³ 광운대학교 전자공학과
	웨이퍼 레벨 3D 적층 메모리 제조의 수율 효율성에 관한 연구
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	서울과학기술대학교 지능형반도체공학과
TD1 000	1200V급 β-Ga ₂ O ₃ Schottky Barrier Diode의 Edge Termination에 대한 연구
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	부경대학교 전자공학과
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111 340	부경대학교 전자공학과
	구리/옥사이드 하이브리드 본딩 전 다양한 플라즈마 영향 연구
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	서울과학기술대학교 지능형반도체공학과
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	Shinhoi Kim ¹ and Byungjin Cho ^{1,2}
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	¹ Chungnam National University, ² KIMM

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	Sungbin Lee, Wonseop Lee, Taehwan Lee, Minju Kim, and Min Sup Choi
	Department of Materials Science and Engineering, Chungnam National University
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	¹ Division of Advanced Materials Engineering, Jeonbuk National University, ² Hydrogen and Fuel Cell Research Center, Jeonbuk National University
	Ferroelectric-metal Field-effect Transistor의 Metal Work Function Variation 특성에 대한 연구
TP1-346	하병주 ¹ , 김동영 ² , 윤택한 ² , 우솔아 ²
	¹ 부경대학교 물리학과, ² 부경대학교 전자공학과
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	¹ Department of Materials Science and Engineering, Seoul National University, ² Department of Physics and Astronomy, Seoul National University
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	¹ Department of Mechanical Engineering, Sungkyunkwan University, ² Department of Smart Fab. Technology, Sungkyunkwan University, ³ RnD Center, Esol Inc.
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	¹ Department of Advanced Material Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
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TP1-352	Yeongeun Kwon ¹ and Byungjin Cho ^{1,2}
	¹ Department of Advanced Material Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University
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TP1-353	Dielectric Properties of MIS Capacitors Utilizing the Nb ₂ O ₅ Oxidized from 2D NbS ₂
TP1-353	Dielectric Properties of MIS Capacitors Utilizing the Nb ₂ O ₅ Oxidized from 2D NbS ₂ Minhee Kim ¹ and Byungjin Cho ^{1,2} ¹ Department of Advanced Material Engineering, Chungbuk National University, ² Department of Urban, Energy, and Environmental Engineering, Chungbuk National University Enhancing Schottky Diodes Performance with MSM-structured Organic Semiconductors for
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	Department of Creative Convergence Engineering, Hanbat National University
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	¹ Department of Creative Convergence Engineering, Hanbat National University, ² Research Institute of Printed Electronics & 3D Printing, Hanbat National University, ³ Industry University Cooperation, Hanbat National University, ⁴ School of Electronic Engineering, Gachon University
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	¹ Department of Electronics Engineering, Pusan National University, ² Department of Electrical Engineering, Pusan National University
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TP1-480 반부록 대목표 (교실 신공 진공 대 모습 기관) TLP Bonding Using Sn/Ni/Sn-foil Laminated Solder Preform Deng-Bok Lee, Yeong-Jin Seo, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chunghuk National University 교선형성 기능치 프로그레잉이 가능한 IG20 2T Synaptic Device 교전형성, 성수원, 액질액, 영업, Sagel Sa	TD1_400	Cu Grain Size에 따른 SAC305/Cu 접합부 금속간화합물 성장 거동 분석
TP1-481 TLP Bonding Using Sn/Ni/Sn-foil Laminated Solder Preform Deng-Bok Lee, Yeong-Jin Seo, and Jeong-Won Yoon Degartment of Advanced Matonials Engineering, Chunghuk National University Z선행성 기용치 프로그웨QIO 가능한 IG20 ZT Synaptic Device 고친행(), 성수원, 백성민), 형태율(), 초현명), 초현명), 창용명 ^{1,23} 'Department of Electrical Engineering, POSTECH, ¹ Department of Semiconductor Engineering, POSTECH, ¹ CSTC, POSTECH TP1-483 Sh 2,304 of Long-term Temperature Reliability of Sn-2.3Ag Flip-Chip Solder Bump 행은, 운행 종료(배리고 dual-Materia) 물록(배리고 dual-Materia) Se A Lia XiPIO IME Ellis 2014 특징 업행부 특징 비교 연구 TP1-483 Se A Lia XiPIO IME Ellis 2014 측정 업행부 특징 비교 연구 Hyo-Won Lee, Eun-Chea Noh, Da-Gyeong Han, and Jeong-Won Yoon Department of Advanced Matonials Engineering, Chunghuk National University Keyword Spotting 성능 양상을 위한 CPU-NPU 8019El2E 프로세치 설계 Yoono Kim, Jawoong Chio, Gisan Ji, and Sungju Ru Department of Electronic Engineering, Sogang University Designing a Hybrid RISC-V CPU and NPU System for Image Classification Optimization Mines Kim, Junkne Lee, Gisan Ji, and Sungju Ru Department of Electronic Engineering, Sogang University A Simulation of Electronic Engineering, Sogang University A Random Number Generator based on Stochastic Ferroelectric Tunnel Junction TP1-488 Xeyword Spotting Hay Stock-Kyun Son ¹² 'Department of Electronic Engineering, POSTECH TP1-489 Xeyword Spo	TP1-480	한다경, 노은채, 윤정원
TP1-481 Dong-Bok Lee, Yeong-Jin Seo, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University 고선병성 가증지 프로그레밍이 가능한 IG20 ZI Synaptic Device 고전망, 성수력, 백성법, 아테로, 호텔, 25 월기 신뢰상 문가 'Department of Electrical Engineering, POSTECH, ² Department of Semiconductor Engineering, POSTECH, ⁶ CSTC, POSTECH Sn-2.33g Flip-chip Solder Bump의 고운 왕기 신뢰상 문가 A Study of Long-term Temperature Reliability of Sn-2.3Ag Flip-Chip Solder Bump 왕은수, 용왕됨 **ETP1-483 **EVALUE 2: 4AIR/93/A **EVALUE 2: 4AIR/93/A **E A List Xi200 대은 2/E/E/P 및 40/A 술대왕 업감부 특성 비교 연구 **HyWon Lee, Eun-Chae Noh, Da-Gyeong Han, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University Keyword Spotting 성분 양 일 위한 CPU-NPU 8/0/E2/E 프로세치 설계 Yoono Kim, Juwoog Chio, Gisan Ji, and Sungiu Ryu Designing a Hybrid RISC-Y CPU and NPU System for Image Classification Optimization Minse Kim, Junhee Lee, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, Sogang University A Bandom Number Generator based on Stochastic Ferroelectric Tunnel Junction Dong-Jun Kim', Seo-Eun Jang', Sungiun Kim', and Min-Hwi Kim' TP1-488 Yew Bit RISC-Y 프로세치 기반 CPU-NPU 8/0/E3/E 프로세치 설계 TP1-489 Desepartment of Electronic Engineering, Sogang University		
Department of Advanced Materials Engineering, Chungbuk National University 고선형실 가용치 프로그레밍이 가능한 IGZO 2T Synaptic Device 고선형실 가용치 프로그레밍이 가능한 IGZO 2T Synaptic Device TP1-482 'Department of Electrical Engineering, POSTECH, 'Department of Semiconductor Engineering, POSTECH, 'CSTC, POSTECH TP1-483 Sn-2.3Ag Flip-chip Solder Bump91 고은 장기 신뢰실 평가 A Study of Long-term Temperature Reliability of Sn-2.3Ag Flip-Chip Solder Bump 정은수, 운행원 용복대학교 신소자평양과 TP1-483 Se 시호 처리이 때문 리물은 및 식이저 순대형 합란부 특성 비교 연구 Hyo-Won Lee, Eun-Chae Noh, Da-Gyeong Han, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University Keyword Spotting 성능 향산을 위한 CPU-NPU 용이브린트 프로세시 설계 Yoono Kim, Juwoong Chio, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, Sogang University Despring a Hydrid RISC-V CPU and NPU System for Image Classification Optimization Minae Kim, Junhee Lee, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, Sogang University TP1-486 A Sandom Number Generator based on Stochastic Feroelectric Tunnel Junction Dong-Jun Kim', See-Eun Jang', Sungiun Kim', and Min-Hwi Kim' 'Chung-Ang University, "Dongguk University TP1-487 A Simulation of Electrical Properties in the Induced Systems Se Hun Kim', Jik Hyeon Ham', and Seock-Kyun Son ¹² 'Department of Physics, Kyung Hee University, "Department of Information Display, Kyung Hee University. TP1-489 Yew Sag RISC-V 프로세A 12t (CPU-NPU 6)DEJEE 프로세A/ 설계 Jaeseok Lee, Jeonghwan Ahn, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, Sogang University TP1-489 Yew Na Sen GZO TFT under PBS Condition by Passivation with Hydr	TP1-481	
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IPI-483 정은수, 운정원 정도가, 운장원 중록대학교 신소재공학과 SE 시료 지입니 따른 리플로우 및 레이지 술다령 잡발부 특성 비교 연구 Hyo-Won Lee, Eun-Chae Noh, Da-Gyeong Han, and Jeong-Won Yoon Department of Advanced Materials Engineering, Chungbuk National University Keyword Spotting 성능 항상을 위한 CPU-NPU お이브 리트 프로세서 설계 Yoono Kim, Jiwoong Chio, Gisan Ji, and Sungju Ryu Department of Electronic Engineering, Sogang University Designing a Hybrid RISC-V CPU and NPU System for Image Classification Optimization Minse Kim, Junkee Lee, Gisan Ji, and Sungju Ryu Department of Electronic Engineering, Sogang University A Random Number Generator based on Stochastic Ferroelectric Tunnel Junction Dong-Jun Kim ¹ , Seo-Eun Jang ¹ , Sungjun Kim ² , and Min-Hwi Kim ¹ TP1-487 A Simulation of Electrical Properties in the Induced Systems Se Hun Kim ¹ , Jik Hyeon Ham ¹ , and Seock-Kyun Son ^{1,2} TP1-488 A Simulation of Electronic Engineering, Sogang University Department of Physics, Kyung Hee University, ² Department of Information Display, Kyung Hee University TP1-489 Jaeseok Lee, Jeonghwan Ahn, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, Sogang University TP1-489 VewW 특화 RISC-V 프로세서 기번 CPU-NPU öloEl=E 프로세서 설계 Jaeseok Lee, Jeonghwan Ahn, Gisan Ji, and Sungiu Ryu Department of Electronic Engineering, POSTECH, ² Department of Semiconductor Engineering, POSTECH, ³ Center for Semiconductor Technology Co		Sn-2.3Ag Flip-chip Solder Bump의 고온 장기 신뢰성 평가
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Department of Electronic Engineering, Sogang University TP1-490 Improving the Current Density of IGZO TFT by Corrugated Substrate Ye Won Jeong ¹ , Jeong Ha Yoon ¹ , Taewon Seo ¹ , and Yoonyoung Chung ^{1,2,3} ¹ Department of Electrical Engineering, POSTECH, ² Department of Semiconductor Engineering, POSTECH, ³ Center for Semiconductor Technology Convergence, POSTECH TP1-491 Highly-stable a-IGZO TFT under PBS Condition by Passivation with Hydrogen Plasma Treatment Jeongha Yoon ¹ , Yewon Jeong ¹ , Taewon Seo ¹ , and Yoonyoung Chung ^{1,2,3} ¹ Department of Electrical Engineering, POSTECH, ² Department of Semiconductor Engineering, POSTECH, ³ Center for Semiconductor Technology Convergence, POSTECH BaZrO ₃ /MgO Templated Epitaxy Enables Three Orders of Magnitude Conductivity Increase in Ba _{0.95} La _{0.05} SnO ₃ Films on Al ₂ O ₃ Substrates, Promoting Very High Transparency and X-band Electromagnetic Shielding Youngkyoung Ha ¹ , Jingyeong Jeon ¹ , Subhin Hwang ¹ , Judith L. MacManus–Driscoll ² , and Shinbuhm Lee ¹		VWW 특화 RISC-V 프로세서 기반 CPU-NPU 하이브리드 프로세서 설계
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	¹ Department of Electronic Engineering, Gachon University, ² Department of Energy Engineering Convergence, Kumoh National Institute of Technolog, ³ School of Electronics and Electrical Engineering, Kyungpook University
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