2024년 1월 25일(목), 15:30-17:15

Room H(202),2층

S. Chip Design Contest 분과

[TH3-S] Chip Design Contest

좌장: 조건희 교수(경북대학교), 장영찬 교수(금오공과대학교)

TH3-S-1 15:30-15:45	Design of Single-ended PAM-3 Transmitter with Crosstalk Cancellation Scheme for Memory Interface Dongwoo Kang and Kwanseo Park Yonsei University
TH3-S-2 15:45-16:00	Design Points of Period-modulation Capacitance-to-digital Converter for Continuous Glucose Monitoring System Donghyun Youn and Minkyu Je School of Electrical Engineering, KAIST
TH3-S-3 16:00-16:15	**Design of Time-Based Switched-Capacitor Low-Dropout Regulator** Hyunjin Kim, Taehyeong Park, and Chulwoo Kim Department of Semiconductor System Engineering, Korea University
TH3-S-4 16:15-16:30	Hardware Implementation of a CNN-based Accurate and Efficient 3D Hand Pose Estimator Yongsoo Kim[1], Wencan Chen[2], Jaehyeon So[3], Siyeon Kim[3], Chanwook Hwang[3], Jong Hwan Ko[3], and Jaehyuk Choi[3] [1]Department of Semiconductor and Display Engineering, Sungkyunkwan University, [2]Department of Artificial Intelligence, Sungkyunkwan University, [3]Department of Electrical and Computer Engineering, Sungkyunkwan University