



제 31회 한국반도체학술대회

The 31st Korean Conference on Semiconductors

2024년 1월 24일(수)-26일(금) | 경주화백컨벤션센터(HICO)

2024년 1월 25일(목), 09:00-10:45
Room H(202), 2층

K. Memory (Design & Process Technology) 분과
[TH1-K] Processing In Memory

좌장: 구민석 교수(인천대학교)

<p>초청발표 TH1-K-1 09:00-09:30</p>	<p>A Fully Integrated Hybrid Memristor-CMOS System for Efficient Vector-Matrix Multiplication Operations Seung Hwan Lee Kyung Hee University</p>
<p>TH1-K-2 09:30-09:45</p>	<p>Compact and High-performance 4T Ternary Content-addressable Memory Utilizing Dual-gate Metal Oxide Transistors Taewon Seo¹, Seongmin Park¹, and Yoonyoung Chung^{1,2,3} ¹Department of Electrical Engineering, POSTECH, ²Department of Semiconductor Engineering, POSTECH, ³Center for Semiconductor Technology Convergence, POSTECH</p>
<p>TH1-K-3 09:45-10:00</p>	<p>Device-algorithm Co-optimization for an On-chip Trainable Capacitor Based Synaptic Device with IGZO TFT and Retention-centric Tiki-Taka Algorithm Jaehyeon Kang¹, Jongun Won¹, Narae Han¹, Minseung Kang¹, Yeaji Park¹, Youngchae Roh¹, Hyeongjun Seo¹, Changhoon Joe¹, Ung Cho¹, Minil Kang², Minseong Um², Hyung-Min Lee², Saeroonter Oh³, and Sangbum Kim¹ ¹Seoul National University, ²Korea University, ³Hanyang University</p>
<p>TH1-K-5 10:00-10:15</p>	<p>A Heater-embodying Rapid and Energy-efficient Mott True Random Number Generator Array for Secure Communication Gwangmin Kim, Jae Hyun In, and Kyung Min Kim Department of Materials Science and Engineering, KAIST</p>