2024년 1월 24일(수)-26일(금) | 경주화백컨벤션센터(HICO)

2024년 1월 25일(목), 09:00-10:45 Room G(201),2층

K. Memory (Design & Process Technology) 분과 [TG1-K] RRAM and Neuromorphic Device I

좌장: 김수길 팀장(SK하이닉스), 권건우 교수(홍익대학교)

소성: 검수걸 담성(SKOF)(탁스), 권건우 교수(용역대학교)	
TG1-K-1 09:00-09:15	CMOS-compatible, 2DEG-Based Three-terminal Dynamic Memristor Woon Hyung Cheong, Geunyoung Kim, and Kyung Min Kim KAIST
TG1-K-2 09:15-09:30	A Memristor-Based Elementary Motion Detector for a Maneuver Prediction Min Gu Lee, Hanchan Song, Gwangmin Kim, Kyung Min Kim Department of Materials Science and Engineering, KAIST
TG1-K-3 09:30-09:45	Reconfigurable Two-dimensional Tellurene Artificial Synapse for Bio-inspired Wearable Edge Computing Bolim You ¹ , Jeechan Yoon ¹ , Yuna Kim ¹ , Mino Yang ² , Jina Bak ¹ , Jihyang Park ¹ , Jihoon Huh ¹ , Myung Gwan Hahm ¹ , and Moonsang Lee ¹ ¹ Inha University, ² Korea Basic Science Institute Seoul
TG1-K-4 09:45-10:00	Strategy to Improve Synaptic Behavior of Ion-actuated Synaptic Transistors – the Use of Ion Blocking Layer for Reliable Multilevel Retention Seonuk Jeon ¹ , Nayeon Kim ¹ , Eunryeong Hong ¹ , Hyun Wook Kim ¹ , Yunsur Kim ² , Hyeonsik Choi ² , Hyoungjin Park ² , Jiae Jeong ² , and Jiyong Woo ^{1,2} ¹ School of Electronic and Electrical Engineering, Kyungpook National University, ² School of Electronics Engineering, Kyungpook National University
TG1-K-5 10:00-10:15	Role of Oxide Barrier in a NbO _x Layer with Noninert Electrodes for High-yield Threshold Switching Characteristics Hyeonsik Choi ¹ , Hyun Wook Kim ² , Eunryeong Hong ² , Nayeon Kim ² , Seonuk Jeon ² , Yunsur Kim ¹ , Hyoungjin Park ¹ , Jiae Jeong ¹ , and Jiyong Woo ^{1,2} ¹ School of Electronic Engineering, Kyungpook National University, ² School of Electronic and Electrical Engineering, Kyungpook National University



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TG1-K-6 10:15-10:30	A 10T2R Non-Volatile SRAM Cell Design with High-Reliability So Yeon Kwon, Woon San Ko, Jun Ho Byun, Do Yeon Lee, and Ga Won Lee Chungnam National University
TG1-K-7 10:30-10:45	Impact of Al ₂ O ₃ Layer on Nonlinearity of Selector-less HfO _x -Based RRAM for Neuromorphic Computing Applications Yunsur Kim ¹ , Hyun Wook Kim ² , Eunryeong Hong ² , Nayeon Kim ² , Seonuk Jeon ² , Hyeonsik Choi ¹ , Hyoungjin Park ¹ , Jiae Jeong ¹ , and Jiyong Woo ^{1,2} ¹ School of Electronic Engineering, Kyungpook National University, ² School of Electronic and Electrical Engineering, Kyungpook National University