



제 31회 한국반도체학술대회

The 31st Korean Conference on Semiconductors

2024년 1월 24일(수)-26일(금) | 경주화백컨벤션센터(HICO)

2024년 1월 25일(목), 15:30-17:15

Room E(105), 1층

N. VLSI CAD 분과

[TE3-N] AI to VLSI CAD

좌장: 송대건 교수(경북대학교)

초청발표 TE3-N-1 15:30-16:00	Ternary VLSI Design: A Circuits and Systems Perspective Sunmean Kim School of Electronic and Electrical Engineering, Kyungpook National University
TE3-N-2 16:00-16:15	Artificial Schematic Creator with Generative Model Yewon Hwang, Jakang Lee, and Seokhyeong Kang POSTECH
TE3-N-3 16:15-16:30	Design-technology Co-optimization for Standard Cell Pin Length Modulation Junghyun Yoon and Heechun Park Kookmin University
TE3-N-4 16:30-16:45	Impact Analysis of Coupling Effect Induced by Through-silicon Via for Static Timing Analysis in 3D IC Euntaek Oh, Mujun Choi, and Juho Kim Sogang University
TE3-N-5 16:45-17:00	Pin Accessibility Aware Routability Prediction Using Graph Neural Network Jiyun Park, Jongho Yoon, and Seokhyeong Kang POSTECH
TE3-N-6 17:00-17:15	Unsupervised Learning-Based Legalization with Graph Neural Network Dho Ui Lim and Heechun Park Kookmin University