



제 31회 한국반도체학술대회

The 31st Korean Conference on Semiconductors

2024년 1월 24일(수)-26일(금) | 경주화백컨벤션센터(HICO)

2024년 1월 26일(금), 09:00-10:45

Room K(205), 2층

G. Device & Process Modeling, Simulation and Reliability 분과

[FK1-G] Device Characterization & Modeling I

좌장: 우지용 교수(경북대학교), 김성호 교수(세종대학교)

<p>초청발표</p> <p>FK1-G-1</p> <p>09:00-09:30</p>	<p>Physical Modeling and Evaluation of 3D Memory: Interlayer Guarding Effects</p> <p>Jo-hak Jeong¹, Dongkyu Lee¹, Jin-Taek Lee¹, Ho Sung Lee¹, Sang Jun Hwang¹, Krishna Moorthy Ponnusamy^{1,2}, Hyun-Sik Jang¹, S. Chandramohan², and Keun Heo¹</p> <p>¹School of semiconductor science & technology, Jeonbuk National University, ²Department of Physics and Nanotechnology, SRM Institute of Science and Technology</p>
<p>FK1-G-2</p> <p>09:30-09:45</p>	<p>Carrier Transport in In_{0.8}Ga_{0.2}As HEMTs at Cryogenic Temperature from the Transconductance Modeling Technique in Saturation</p> <p>Min-Seo Yu¹, Ji-Hoon Yoo¹, In-Geun Lee¹, Jae-Hak Lee¹, Kyounghoon Yang², and Dae-Hyun Kim¹</p> <p>¹Kyungpook National University, ²KAIST</p>
<p>FK1-G-3</p> <p>09:45-10:00</p>	<p>Extraction of Individual Contact Resistance and Threshold Voltage in Carbon Nanotube Thin-film Transistors</p> <p>Jun-Ho Jang¹, Hanbin Lee¹, Jeonghee Ko¹, Yulim An¹, Hyo-In Yang¹, GyeongSu Min¹, So Jeong Park¹, Jeong Yeon Im¹, Dong Myong Kim¹, Dae Hwan Kim¹, Jong-Ho Bae¹, Min-Ho Kang², and Sung-Jin Choi¹</p> <p>¹School of Electrical Engineering, Kookmin University, ²Department of Nano-process, NNFC</p>
<p>FK1-G-4</p> <p>10:00-10:15</p>	<p>Virtual-Source Based Modeling of Charge-dependent Source Resistance and Drain Current of In_xGa_{1-x}As MBCFETs</p> <p>Su-Min Choi¹, Ji-Hoon Yoo¹, Hyeon-Bhin Jo¹, In-Geun Lee¹, Hyuk-Min Kwon², Takuya Tsutsumi³, Hiroki Sugiyama³, Hideaki Matsuzaki³, Jae-Hak Lee¹, and Dae-Hyun Kim¹</p> <p>¹Kyungpook National University, ²Polytechnics, ³NTT Co.</p>
<p>FK1-G-5</p> <p>10:15-10:30</p>	<p>Experimental Demonstration of Tunable Synchronization in Coupled NbO_x Artificial Neuron Systems for Neuromorphic Pattern Recognition</p> <p>Hyun Wook Kim, Eunryeong Hong, Nayeon Kim, Seonuk Jeon, and Jiyong Woo</p> <p>School of Electronic and Electrical Engineering, Kyungpook National University</p>



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FK1-G-6

10:30-10:45

Investigating Process-Dependent Variations in Amorphous IGZO TFTs for 2T-DRAM Application through Monochromatic Photonic C-V Analysis

Hyunwook Jeong, Junseong Park, Ha-Neul Lee, Yubin Choi, Sung-Jin Choi, Dong Myong Kim, Dae Hwan Kim, and Jong-Ho Bae

School of Electrical Engineering, Kookmin University