2024년 1월 26일(금), 09:00-10:45

Room E(105),1층

N. VLSI CAD 분과

[FE1-N] VLSI CAD to Future Technologies

좌장: 송대건 교수(경북대학교), 현대준 교수(세종대학교)

FE1-N-1 09:00-09:15	**A Novel Design of 8T Ternary SRAM for Low Power** Jihyeong Yun and Sunmean Kim School of Electronic and Electrical Engineering, Kyungpook National University
FE1-N-2 09:15-09:30	**High-throughput PIM (Processing in-memory) for DRAM Using Bank-level Pipelined Architecture** Hyunsoo Lee[1], Hyundong Lee[1], Minseung Shin[2], Gyuri Shin[2], Sumin Jeon[2], and Taigon Song[1,2] [1]School of Electronic and Electrical Engineering, Kyungpook National University, [2]School of Electronics Engineering, Kyungpook National University
FE1-N-3 09:30-09:45	**Mixed-Vth 셀을 활용한 누설전력 최적화 알고리즘** 안진일[1], 김경창[2], 현대준[1] [1]세종대학교, [2]청주대학교
FE1-N-4 09:45-10:00	**A Novel Ternary Flip-Flop for Low Area Overhead Ternary Scan Design** Hayeon Lee and Sunmean Kim School of Electronic and Electrical Engineering, Kyungpook National University
FE1-N-5 10:00-10:15	**GPHFGCN : General-purpose Computing on High-speed and Fully-optimized GCN Accelerator** Minseok Han and Taigon Song School of Electronic and Electrical Engineering, Kyungpook National University
FE1-N-6 10:15-10:30	**Automated Bitstream Analysis Method for FPGA Reverse Engineering** Mannhee Cho[1], Dongchan Lee[2], Sanghyun Lee[2], Youngmin Kim[2], and Hyung-Min Lee[1] [1]Korea University, [2]Hongik University
FE1-N-7 10:30-10:45	**타이밍 제약조건을 고려한 전력분배망 최적화** 송정식, 오제영, 현대준 세종대학교 반도체시스템공학과