



# 제 31회 한국반도체학술대회

The 31st Korean Conference on Semiconductors

2024년 1월 24일(수)-26일(금) | 경주화백컨벤션센터(HICO)

2024년 1월 26일(금), 09:00-10:45

Room B(102), 1층

## O. System LSI Design 분과

### [FB1-0] System LSI Design

좌장: 유호영 교수(충남대학교), 정준원 교수(숙명여자대학교)

<p>초청발표</p> <p>FB1-O-1</p> <p>09:00-09:30</p>	<p>A Key Building Block of AI Accelerator: In-memory Computing Macros</p> <p>Dong-Jin Chang</p> <p>Chungnam National University</p>
<p>FB1-O-2</p> <p>09:30-09:45</p>	<p>Efficient Hardware Implementation of a Non-Linear Activation Function Approximation</p> <p>ChanWoo Song, JoonSeok Kim, KyuMin Cho, and SeokHyung Kang</p> <p>Department of Electrical Engineering, POSTECH</p>
<p>FB1-O-3</p> <p>09:45-10:00</p>	<p>Space-Time Transformation을 이용한 Systolic Tensor Array 분석</p> <p>이동훈, 박지호, 유호영</p> <p>충남대학교 전자공학과</p>
<p>초청발표</p> <p>FB1-O-4</p> <p>10:00-10:30</p>	<p>Application of Machine Learning for Embedded Memory Circuit Design &amp; Characterization</p> <p>Hanwool Jeong<sup>1,2</sup></p> <p><sup>1</sup>광운대학교, <sup>2</sup>주식회사 아티크론</p>
<p>FB1-O-5</p> <p>10:30-10:45</p>	<p>Programmable ALPG Architecture for High-Speed DRAM Testing</p> <p>Saeyeon Kim, Sunyoung Park, Seoyeon Park, Eunkyung Ham, and Ji-Hoon Kim</p> <p>Department of Electronic and Electrical Engineering, Ewha Womans University</p>