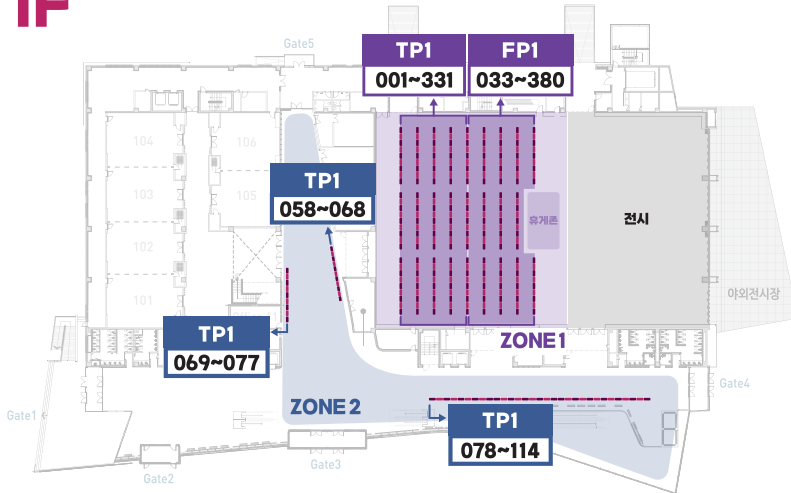


포스터 배치도

1F



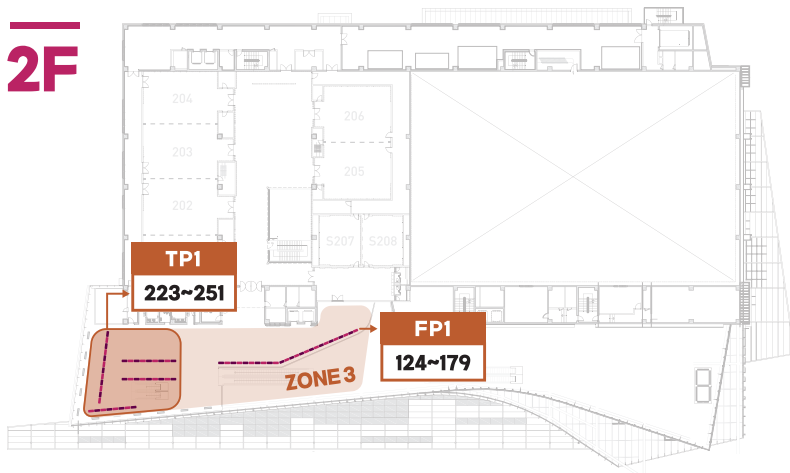
ZONE 1

| 분과 | 일시 | 포스터 발표기호 |
|--|--------------|-------------|
| D. Thin Film Process Technology | | TP1-001~057 |
| F. Silicon and Group-IV Devices and Integration Technology | | TP1-115~168 |
| I. MEMS & Sensors Systems | 25일(목) | TP1-169~211 |
| O. System LSI Design | [TP1] 저자 Q&A | TP1-212~222 |
| S. Chip Design Contest | 17:15-17:55 | TP1-252~303 |
| T. AI | | TP1-304~317 |
| U. Bio-Medical | | TP1-318~327 |
| V. Quantum Technology | | TP1-328~331 |
| B. Patterning (Lithography & Etch Technology) | 26일(금) | FP1-033~060 |
| H. Display and Imaging Technologies | [FP1] 저자 Q&A | FP1-180~215 |
| J. Nano-Science & Technology | 10:45-11:25 | FP1-216~284 |
| K. Memory (Design & Process Technology) | | FP1-285~380 |

ZONE 2

| 분과 | 일시 | 포스터 발표기호 |
|---------------------------------|--------------|-------------|
| D. Thin Film Process Technology | 25일(목) | TP1-058~114 |
| | [TP1] 저자 Q&A | 17:15-17:55 |

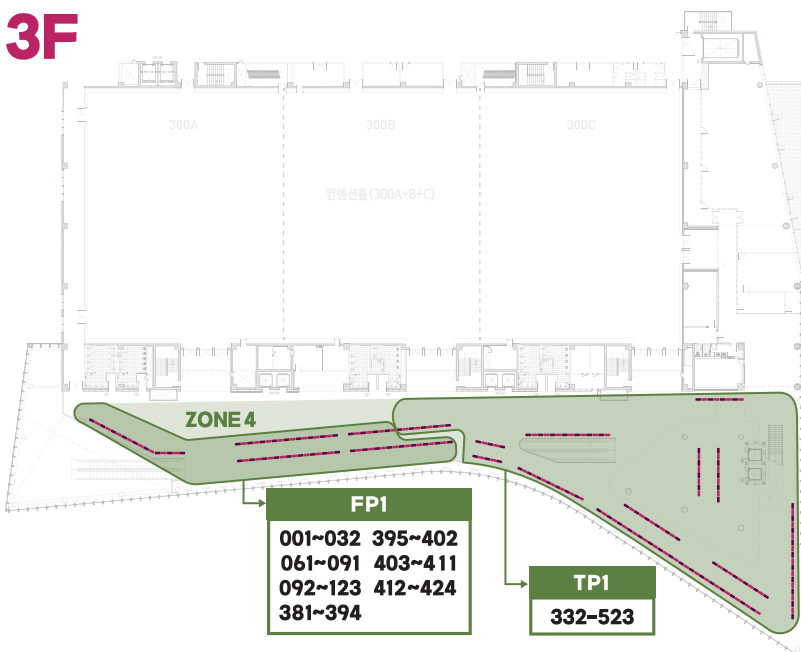
2F



ZONE 3

| 분과 | 일시 | 포스터 발표기호 |
|---|--------------|-------------|
| Q. Metrology, Inspection, Analysis, and Yield Enhancement | 25일(목) | TP1-223~245 |
| R. Semiconductor Software | [TP1] 저자 Q&A | 17:15-17:55 |
| | | TP1-246~251 |
| G. Device & Process Modeling, Simulation and Reliability | 26일(금) | FP1-124~179 |
| | [FP1] 저자 Q&A | 10:45-11:25 |

3F



ZONE 4

| 분과 | 일시 | 포스터 발표기호 |
|--|--------------|-------------|
| 학부생 Poster Session | 25일(목) | TP1-332~523 |
| | [TP1] 저자 Q&A | 17:15-17:55 |
| A. Interconnect & Package | | FP1-001~032 |
| C. Material Growth & Characterization | | FP1-061~091 |
| E. Compound Semiconductors | | FP1-092~123 |
| L. Analog Design | 26일(금) | FP1-381~394 |
| M. RF and Wireless Design | [FP1] 저자 Q&A | 10:45-11:25 |
| | | FP1-395~402 |
| N. VLSI CAD | | FP1-403~411 |
| P. Device for Energy (Solar Cell, Power Device, Battery, etc.) | | FP1-421~424 |