



# 제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월) ~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 15일(수), 16:00-17:45

Room H (하트 I, 6층)

## N. VLSI CAD 분과

### [WH3-N] Design Automation in Advanced Technology Nodes

좌장: 송대건 교수(경북대학교), 강석형 교수(POSTECH)

WH3-N-1 16:00-16:30 [초청]	<b>Applications of Deep Learning on EDA: Status and Challenges</b> Heechun Park Kookmin University
WH3-N-2 16:30-16:45	<b>MLB: Multi-Vth Tuning for Leakage Power Reduction via Bayesian Optimization</b> Jaeseung Lee, Jakang Lee, Seonghyeon Park, and Seokhyeong Kang POSTECH
WH3-N-3 16:45-17:00	<b>Optimizing Pin Accessibility of 3nm GAAFET Standard Cells</b> Jaehoon Jeong <sup>1</sup> and Taigon Song <sup>1,2</sup> <sup>1</sup> School of Electronic and Electrical Engineering, Kyungpook National University, <sup>2</sup> School of Electronics Engineering, Kyungpook National University
WH3-N-4 17:00-17:15	<b>전력분배망 최적화를 위한 고속 전압 강하 분석</b> 송정식, 오제영, 현대준 청주대학교 전자공학과
WH3-N-5 17:15-17:30	<b>Open-Source 3D Global Placer with D2D Vertical Connections Using Louvain Clustering and RePIAce</b> Minjae Kim, Dohun Kim, and Seokhyeong Kang POSTECH
WH3-N-6 17:30-17:45	<b>A Novel Processing Unit and Architecture for Process-In Memory (PIM) in NAND Flash Memory</b> Hyunwoo Kim <sup>1</sup> and Taigon Song <sup>1,2</sup> <sup>1</sup> School of Electronic and Electrical Engineering, Kyungpook National University, <sup>2</sup> School of Electronics Engineering, Kyungpook National University