



제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월) ~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 15일(수), 09:00-10:30

Room E (루비 II, 5층)

E. Compound Semiconductors 분과 [WE1-E] Compound Semiconductor I

좌장: 임유승 교수(세종대학교)

WE1-E-1 09:00-09:15	Record In_{0.8}Ga_{0.2}As Quantum-well HEMTs for 6G Applications Wan-Soo Park ¹ , Hyeon-Bhin Jo ¹ , Hyo-Jin Kim ¹ , Su-Min Choi ¹ , Ji-Hoon Yoo ¹ , Hyeon-Seok Jeong ¹ , Takuya Tsutsumi ² , Hiroki Sugiyama ² , Hideaki Matsuzaki ² , Jae-Hak Lee ¹ , and Dae-Hyun Kim ¹ ¹ School of Electronic and Electrical Engineering, Kyungpook National University, ² NTT Device Technology Laboratories
WE1-E-2 09:15-09:30	High-speed and Low-voltage Operating Charge Trap Device based on InGaAs TFETs for Low-power Neuromorphic Application Dae-Hwan Ahn ¹ , Suman Hu ^{1,2} , Kyeol Ko ¹ , Donghee Park ¹ , Hoyung Suh ¹ , Gyu-Tae Kim ² , Jae-Hoon Han ¹ , Jin-Dong Song ¹ , and Yeon Joo Jeong ¹ ¹ KIST, ² Korea University
WE1-E-3 09:30-09:45	Explicit Thermal Resistance Model of Self-heating Effects of AlGaIn/GaN HEMTs with Linear and Non-linear Thermal Conductivity Surajit Chakraborty ¹ , Walid Amir ¹ , Ju-Won Shin ¹ , Ki-Yong Shin ¹ , Takuya Hoshi ² , Takuya Tsutsumi ² , Hiroki Sugiyama ² , Hideaki Matsuzaki ² , and Tae-Woo Kim ¹ ¹ Department of Electrical, Electronic and Computer Engineering, University of Ulsan, ² NTT Device Technology Laboratories, NTT Corporation
WE1-E-4 09:45-10:00	Investigation for Spatial Distribution of Oxide Trap Density by Low-Frequency Noise Characterization in β-Ga₂O₃ FinFET Jae Wook Yoo ¹ , Hong Seung Lee ¹ , Hyeon Jun Song ¹ , Seongbin Lim ¹ , Jungsik Kim ⁴ , Kihyun Kim ¹ , Jun-Young Park ³ , Yang-Kyu Choi ² , and Hagyoul Bae ¹ ¹ Jeonbuk National University, ² KAIST, ³ Chungbuk National University, ⁴ Gyeongsang National University
WE1-E-5 10:00-10:15	Full Stepper-Based InP Double-Heterojunction Bipolar Transistors (DHBTs) with f_T & $f_{max} \geq 250$ GHz Hyeon-Seok Jeong ^{1,2} , Yong-Hyun Kim ¹ , Jacob Yun ¹ , Ji-Min Beak ² , Ji-Hoon Yoo ² , Su-Min Choi ² , Wan-Soo Park ² , Hyo-Jin Kim ² , In-Geun Lee ² , Ted Kim ¹ , Jae-Hak Lee ² , Hyuk-Min Kwon ³ , Seung Heon Shin ³ , and Dae-Hyun Kim ² ¹ QSI Inc., ² Kyungpook National University, ³ Korea Polytechnics
WE1-E-6 10:15-10:30	5-levels-stacked In_{0.53}Ga_{0.47}As MBCFETs with Regrown S/D Contacts In-Geun Lee ¹ , Hyeon-Bhin Jo ¹ , Ji-Hoon Yoo ¹ , Hyunchul Jang ² , Minwoo Kong ² , Hyeon-Seok Jeong ¹ , Wan-Soo Park ¹ , Sang-Kuk Kim ³ , Jae-Gyu Kim ³ , Jacob Yun ³ , Ted Kim ³ , Jae-Hak Lee ¹ , Chan-Soo Shin ² , Kwang-Seok Seo ² , and Dae-Hyun Kim ¹ ¹ Kyungpook National University, ² KANC, ³ QSI Inc.