



제 30회 한국반도체학술대회

The 30th Korean Conference on Semiconductors

2023년 2월 13일(월) ~ 15일(수) | 강원도 하이원리조트(그랜드호텔 컨벤션타워)

2023년 2월 14일(화), 10:55-12:40

Room F (스페이스 I, 6층)

K. Memory (Design & Process Technology) 분과

[TF2-K] RRAM and Synapse Device II

좌장: 김윤 교수(서울시립대학교), 정연주 박사(KIST)

TF2-K-1 10:55-11:10	NbO₂ Selector Device with Ge₂Sb₂Te₅ Thermal Barrier for Low Off Current and Low Power Operation Ohhyuk Kwon, Jangseop Lee, Kyumin Lee, Wooseok Choi, and Hyunsang Hwang POSTECH
TF2-K-2 11:10-11:25	A Study on Metal-oxide Based ECRAM Device Characteristics of Selector-free Cross-point Array for DNN Jeonghoon Son, Seungkun Kim, and Seyoung Kim Department of Materials Science and Engineering, POSTECH
TF2-K-3 11:25-11:40	Demonstration of Analog Computing Using HfO_x-Based 1T1M Array Device Eun Young Kim, Woon Hyung Cheong, Myeong Chan Ko, and Kyung Min Kim KAIST
TF2-K-4 11:40-11:55	Self-Compliance Effects of the SiO_x/SiN_y Bilayer in Co CBRAM Yeon-Joon Choi ^{1,2} , Tae-Hyeon Kim ^{1,2} , Kyungho Hong ^{1,2} , Sungjoon Kim ^{1,2} , Sungjun Kim ³ , and Woo Young Choi ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University, ³ Division of Electronics and Electrical Engineering, Dongguk University
TF2-K-5 11:55-12:10	Optimization of Strong Physical Unclonable Function based on Passive Memristive Crossbar Array for Highly Reliable Operation Jinwoo Park, Sangwook Youn, Kyu Ree Kim, Jungjin Lee, and Hyungjin Kim Department of Electrical and Computer Engineering, Inha University
TF2-K-6 12:10-12:25	Optimal Weight-Splitting in Resistive Random Access Memory-Based Computing-in-Memory Macros Choong Seok Song and Doo Seok Jeong Division of Materials Science and Engineering, Hanyang University
TF2-K-7 12:25-12:40	RRAM Array with a Pristine Low Resistance State Sungjoon Kim ^{1,2} , Kyungho Hong ^{1,2} , Tae-Hyeon Kim ^{1,2} , Yeon Joon Choi ^{1,2} , and Woo Young Choi ^{1,2} ¹ Department of Electrical and Computer Engineering, Seoul National University, ² Inter-university Semiconductor Research Center, Seoul National University