



# 제 29회 한국반도체학술대회

The 29th Korean Conference on Semiconductors

2022년 1월 24일(월)~ 26일(수) | 강원도 하이원 그랜드호텔(컨벤션타워)

2022년 1월 26일(수), 14:00-15:30

Room K (다이아몬드 I, 6층)

## F. Silicon and Group-IV Devices and Integration Technology 분과 [WK3-F] Advanced Device Technology

좌장: 김명수 교수(UNIST)

<p><b>WK3-F-1</b> 14:00-14:15</p>	<p><b>Material Effects of Resistive Switching Uniformity based on Bi-layer Structure Memristor</b> Yan-Han<sup>1</sup>, Ki-Woo Song<sup>1</sup>, Yeong-Jin An<sup>2</sup>, Seong-Hyun Kim<sup>1</sup>, Nguyen Kim Thanh<sup>1</sup>, Sunil Babu Eadi<sup>1</sup>, Hyuk-Min Kwon<sup>2</sup>, and Hi-Deok Lee<sup>1</sup> <i><sup>1</sup>Department of Electronics Engineering, Chungnam National University, <sup>2</sup>Department of Semiconductor Processing Equipment, Semiconductor Convergence Campus, Korea Polytechnics College</i></p>
<p><b>WK3-F-2</b> 14:15-14:30</p>	<p><b>Effects of Irradiation of High-Energy Hydrogen Ions on the Characteristics of poly-Si Floating-Gate Metal-Oxide-Semiconductor Field-Effect Transistor</b> Sibeom Kim<sup>1</sup>, Young Jun Yoon<sup>2</sup>, and Seongjae Cho<sup>1</sup> <i><sup>1</sup>Department of Electronic Engineering, Gachon University, <sup>2</sup>KAERI</i></p>
<p><b>WK3-F-3</b> 14:30-14:45</p>	<p><b>Multi-Valued Multiple-Sheets Field Effect Transistor</b> Sungsoo Lee and Changhwan Shin <i>Department of Electrical and Computer Engineering, Sungkyunkwan University</i></p>
<p><b>WK3-F-4</b> 14:45-15:00</p>	<p><b>Device Design of Ferroelectric-gated Feedback Field Effect Transistor with 4 States</b> Juho Sung and Changhwan Shin <i>Department of Electrical and Computer Engineering, Sungkyunkwan University</i></p>
<p><b>WK3-F-5</b> 15:00-15:15</p>	<p><b>Analysis of SET/RESET Process and Improvement of Resistance Uniformity in Memristor depending on the Difference in the Work Function</b> Yeong-Jin An<sup>1</sup>, Ki-Woo Song<sup>1</sup>, Nguyen Kim Thanh<sup>1</sup>, Tae-Gyu Ryu<sup>1</sup>, Sunil Babu Eadi<sup>1</sup>, Hyuk-Min Kwon<sup>2</sup>, and Hi-Deok Lee<sup>1</sup> <i><sup>1</sup>Department of Electronics Engineering, Chungnam National University, <sup>2</sup>Department of Semiconductor Processing Equipment, Semiconductor Convergence Campus, Korea Polytechnics College</i></p>
<p><b>WK3-F-6</b> 15:15-15:30</p>	<p><b>Gate-Normal Charge Trapped (GCT) Tunnel Field-Effect Transistors (TFETs)</b> Jae Seung Woo, Kyung Min Koo, and Woo Young Choi <i>Department of Electronic Engineering, Sogang University</i></p>