2022년 1월 26일(수), 14:00-15:30

Room H (하트 I, 6층)

L. Analog Design 분과
[WH3-L] Analog Design I

좌장: 엄지용 교수(금오공과대학교), 정영호 교수(대구대학교)

WH3-L-1 14:00-14:15	단일입력 비교기 기반의 저전력 2차 수동형 Noise-Shaping SAR ADC Dongsik Lee, Kihyun Kim, and Hyungil Chae *Department of Electronic Engineering, Konkuk University*
WH3-L-2 14:15-14:30	**High-Speed Column-Parallel ADC Using Data Feedforward Scheme in AMOLED External Compensation** Seunghun Oh, Kyeongmin Park, Haewan Cho, and Franklin Bien *UNIST*
WH3-L-3 14:30-14:45	**Design of A Low-Noise and High Dynamic-Range CMOS Image Sensor** Hyun Seon Choo, Minkyu Song, and Soo Youn Kim *Department of Semiconductor Science, Dongguk University*
WH3-L-4 14:45-15:00	**A 10-bit 1-MS/s Synchronous SAR ADC With A Hybrid RC DAC in 0.11μm CMOS for Image Sensor Application** Hyoung-Jung Kim, Ji-Whan Kim, Ho-Jin Kim, Jun-Ho Boo, Jaeyeop Na, and Gil-Cho Ahn *Department of Electronic Engineering, Sogang University*
WH3-L-5 15:00-15:15	**A 16-bit Current Mode Dual-slope ADC with Reduced Errors Caused by Process Variations** Yu-Rin Jin, Yeon-ho Seo, Dong-Han Ki, Sung-Mi Kim, and Seong-Ik Cho *Department of Electronic Engineering, Chonbuk National University*
WH3-L-6 15:15-15:30	**A 7-bit 1.3GS/s 3-bit/cycle SAR ADC with Time-Domain Quantization** Jihyun Baek and Hyungil chae *Department of Electrical Engineering, Konkuk University*