



제 29회 한국반도체학술대회

The 29th Korean Conference on Semiconductors

2022년 1월 24일(월)~ 26일(수) | 강원도 하이원 그랜드호텔(컨벤션타워)

2022년 1월 26일(수), 09:00-10:30

Room F (스페이스 I, 6층)

G. Device & Process Modeling, Simulation and Reliability 분과

[WF1-G] Memory Devices and Advanced Modeling

좌장: 나현철 상무(DB 하이텍), 김성호 교수(세종대학교)

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| <p>WF1-G-1 09:00-09:15</p> | <p>Analysis of Short-Term Retention in 3-D NAND Flash Memory Using Charge Control Pulse Scheme Donghwi Kim¹, GilSang Yoon¹, DongHyun Go¹, Junghun Park¹, Jungsik Kim², and Jeong-Soo Lee¹ <i>¹Department of Electrical Engineering, POSTECH, ²Division of Electrical Engineering, Gyeongsang National University</i></p> |
| <p>WF1-G-2 09:15-09:30</p> | <p>Scaling Analysis of NbO_x-Threshold Switching Devices Hyun Wook Kim^{1,2}, Sol Jin², Heebum Kang¹, Eun Ryeong Hong^{1,2}, and Jiyong Woo^{1,2} <i>¹School of Electronic and Electrical Engineering, Kyungpook National University, ²School of Electronics Engineering, Kyungpook National University</i></p> |
| <p>WF1-G-3 09:30-09:45</p> | <p>Variation of Electrical and Memory Characteristics of Non-Circular Cell in 3D-NAND Flash Memory DongHyun Go¹, GilSang Yoon¹, Jounghun Park¹, Dongwhi Kim¹, Jungsik Kim², and Jeong-Soo Lee¹ <i>¹Department of Electrical Engineering, POSTECH, ²Division of Electrical Engineering, Gyeongsang National University</i></p> |
| <p>WF1-G-4 09:45-10:00</p> | <p>Accurate Implementation of the Bernoulli Function for the Scharfetter-Gummel Scheme Jeong-Hyeon Do and Sung-Min Hong <i>School of Electrical Engineering and Computer Science, GIST</i></p> |
| <p>WF1-G-5 10:00-10:15</p> | <p>Physicochemical modeling of conformal coating on periodical high aspect ratio porous media via atomic layer deposition Nhat-Minh Phung^{1,2}, Sun-Young Park^{1,3}, Minh-Tan Ha^{1,2}, Soonil Lee², Se-Hun Kwon³, and Seong Min Jeong¹ <i>¹Energy Efficiency Materials Center, KICET, ²Department of Materials Science and Engineering, Changwon National University, ³School of Materials Science and Engineering, Pusan National University</i></p> |
| <p>WF1-G-6 10:15-10:30</p> | <p>TCAD Analysis of Single-Photon Avalanche Diodes in CMOS Technology Won-Yong Ha¹, Woo-Young Choi¹, and Myung-Jae Lee² <i>¹Department of Electrical and Electronic Engineering, Yonsei University, ²Post-Silicon Semiconductor Institute, KIST</i></p> |