제 29회 한국반도체학술대회 The 29th Korean Conference on Semiconductors 2022년 1월 24일(월)~26일(순) | 강원도 하이원 그랜드호텔(컨벤션타웨)

2022년 1월 26일(수), 15:45-17:30 Room D (사파이어 II+III, 5층)

K. Memory (Design & Process Technology) 분과 [WD4-K] Charge Trap Flash and PRAM

좌장: 김윤 교수(서울시립대학교), 권용우 교수(홍익대학교)

WD4-K-1 15:45-16:00	Sandwich Structure Charge Trap Memristor for Retention Secured Neuromorphic Synapse Application Geunyoung Kim, Seoil Son, and Kyung Min Kim Department of Materials Science and Engineering, KAIST
WD4-K-2 16:00-16:15	Schottky Barrier Based Double PN Junction Synapse with SONOS Structure Yijoon Kim, Hyangwoo Kim, and Chang-Ki Baek Department of Convergence IT Engineering, POSTECH
WD4-K-3 16:15-16:30	3차원 적층형 charge trap flash 소자의 적층 두께 제어를 통한 멀티레벨 컨덕턴스 변화 구현 Hyun-Seok Choi ¹ , Jun Park ¹ , Dong hyuk Chae ¹ , Minsuk Koo ² , and Yoon Kim ¹ ¹ Department of Electrical and Computer Engineering, University of Seoul, ² Department of Computer Science and Engineering, Incheon National University
WD4-K-4 16:30-16:45	Hybrid Three- and Two-Terminal Charge Trap Memristive devices for Heterosynaptic Plasticity Jae Bum Jeon, Seong-In Cho, Geunyoung Kim, and Kyung Min Kim <i>KAIST</i>
WD4-K-5 16:45-17:00	스퍼터 증착 ZnTe OTS의 성능 개선을 위한 공정 및 물성 평가연구 공동호, 심건호, 백승재 School of Electronic and Electrical Engineering, Hankyong National University
WD4-K-6 17:00-17:15	Investigation of Phase-change Synapse Architectures by Fully-coupled Electrothermal and Phase-change Simulation Ho Thi Thu Trang and Yongwoo Kwon Department of Materials Science and Engineering, Hongik University
WD4-K-7 17:15-17:30	Impact of Doping on the Characteristics of Binary Chalcogenide Ovonic Threshold Switch for Selector Devices Su-Bong Lee ^{1,2} , Yoon-Gu Lee ^{1,2} , Sang-Heon Park ^{1,2} , Chaebin Park ^{1,2} and Jong-Souk Yeo ^{1,2} ¹ School of Integrated Technology, Yonsei University, ² Yonsei Institute of Convergence Technology, Yonsei University