



제 29회 한국반도체학술대회

The 29th Korean Conference on Semiconductors

2022년 1월 24일(월)~ 26일(수) | 강원도 하이원 그랜드호텔(컨벤션타워)

2022년 1월 26일(수), 14:00-15:30

Room D (사파이어 II+III, 5층)

K. Memory (Design & Process Technology) 분과

[WD3-K] NAND Flash Memory

좌장: 강명곤 교수(한국교통대학교), 유경창 박사(삼성전자)

<p>WD3-K-1 14:00-14:30</p>	<p>Vertical NAND Technology Innovation for the Future Suk-Kang Sung <i>Samsung Electronics Co., Ltd</i></p>
<p>WD3-K-2 14:30-14:45</p>	<p>Vacuum Dielectric to Improve Cell-to-Cell Programming Interference in 3D NAND Flash Memory Woo-Jin Jung and Jun-Young Park <i>Chungbuk National University</i></p>
<p>WD3-K-3 14:45-15:00</p>	<p>Semiconducting한 소재의 유전 특성과 Trap 밀도 측정 방법에 대한 연구 심건호, 공동호, 백승재 <i>Department of Electronic and Electrical Engineering, Hankyong National University</i></p>
<p>WD3-K-4 15:00-15:15</p>	<p>Improved ISPP Scheme of 3-D NAND Flash for Narrow Threshold Voltage Distribution Giho Yang¹, Chanyang Park¹, Kihoon Nam¹, Min Sang Park², Hojung Kang², Jaesung Sim², and Rock-Hyun Baek¹ <i>¹Department of Electrical Engineering, POSTECH, ²SK hynix</i></p>
<p>WD3-K-5 15:15-15:30</p>	<p>Demonstration of Mechanical Stress during Thermal Recovery Configuration in a 3D NAND Flash String Yu-Jin Kim and Jun-Young Park <i>Chungbuk National University</i></p>