2022년 1월 26일(수), 10:45-12:30 Room D (사파이어 II+III, 5층)

K. Memory (Design & Process Technology) 분과 [WD2-K] Processing and Analysis of Emerging Memory

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van der Waals Assembled Non-volatile Memory Joonki Suh Department of Materials Science and Engineering, UNIST	
The Effect of Self-heating Effect on the Lifetime of the Time-dependent Dielectric Breakdown 임재원, 유창현, 전종욱 Department of Electrical and Electronics Enginnering, Konkuk University	
Multi-Gate BCAT Structure and Select Word-Line Driver in DRAM for Reduction of GIDL Chang Young Lim, Ju Han Ryu, Young Lae Kim, and Min-Woo Kwon Department of Electric Engineering, Gangneung-Wonju National University	
Hf _x Al _{1-x} O _y 트랩 층을 사용한 전하 트랩 플래시 메모리 셀의 전하 Retention 특 성 모델링 김건웅, 백승재 Department of Electrical and Electronic Engineering, Hankyong National University	
UV Light-assisted Erasing of In-Ga-Sn-O (IGTO) Thin Film Transistor with Al ₂ O ₃ /HfO ₂ /Al ₂ O ₃ for the Flash Memory Application Zeli Wang ¹ , Hongwei Xu ² , Jaekyeong Jeong ² , and Changhwan Choi ¹ ¹ Division of Materials Science and Engineering, Hanyang University, ² Department of Electronic Engineering, Hanyang University	
A Study on the Optimization Methodology of Steep-Switching Phase-FET and Its Application to 3nm Gate-All-Around Field Effect Transistor Hanggyo Jung, Yeji Lee, Jooyoung Oh, Changhyun Yoo, and Jongwook Jeon Department of Electrical and Electronics Engineering, Konkuk University	