



## Q. Metrology, Inspection, Analysis, and Yield Enhancement 분과

2021년 1월 29일(금), 10:45-12:15 / 채널 A

### [FA2-Q] Metrology, Inspection, and Yield Enhancement I

좌장: 정용우 박사 (SK하이닉스), 조용재 박사 (KRISS)

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| <p><b>FA2-Q-1</b><br/>10:45-11:15</p> | <p><b>[초청]</b><br/><b>Digital X-ray Sources based on Carbon Nanotube Field Emitters for Semiconductor Inspections</b><br/>Yoon-Ho Song<br/><i>Materials and Components Division, ICT Creative Research Laboratory, ETRI</i></p>                                                                                                                                                                                                                                                                                                    |
| <p><b>FA2-Q-2</b><br/>11:15-11:45</p> | <p><b>[초청]</b><br/><b>Real-time Monitoring of Contamination Particles in Semiconductor Process</b><br/>Jihun Mun<sup>1</sup>, and Sang-Woo Kang<sup>1,2</sup><br/><i><sup>1</sup>Advanced Instrumentation Institute, KRISS, <sup>2</sup>Science of Measurement, UST</i></p>                                                                                                                                                                                                                                                        |
| <p><b>FA2-Q-3</b><br/>11:45-12:00</p> | <p><b>A Hybrid Metrology &amp; Inspection Solution for Semiconductor Manufacturing Process</b><br/>Wookrae Kim, Gwangsik Park, Changhyeong Yoon, Jinseob Kim, Daehoon Han, Jaehwang Jung, and Myungjun Lee<br/><i>MI Equipment R&amp;D Team, Mechatronics R&amp;D Center, Samsung Electronics Co., Ltd.</i></p>                                                                                                                                                                                                                    |
| <p><b>FA2-Q-4</b><br/>12:00-12:15</p> | <p><b>An Instance Selection Algorithm under the Unsupervised Condition for a Virtual Metrology of Critical Dimension in Semiconductors</b><br/>In Seok Park<sup>1</sup>, JunHui Lee<sup>1</sup>, Min Su Kim<sup>1</sup>, Wan Sik Nam<sup>2</sup>, Heeyoon Han<sup>2</sup>, Sooseok Lee<sup>2</sup>, Chihoon Lee<sup>2</sup>, and PooGyeon Park<sup>1</sup><br/><i><sup>1</sup>Department of Electrical Engineering, POSTECH, <sup>2</sup>Measurement &amp; Inspection Team, Memory Business, Samsung Electronics Co., Ltd.</i></p> |