



F. Silicon and Group-IV Devices and Integration Technology 분과

2021년 1월 29일(금), 09:00-10:30 / 채널 B

[FB1-F] Neuromorphic Technology

좌장: 구민석 교수 (인천대학교), 최신현 교수 (KAIST)

<p>FB1-F-1 09:00-09:15</p>	<p>Demonstration of Integrate-and-fire Neuron Circuit for Spiking Neural Networks Sung Yun Woo^{1,2}, Won-Mook Kang^{1,2}, Young-Tak Seo^{1,2}, Soochang Lee^{1,2}, Seongbing Oh^{1,2}, and Jong-Ho Lee^{1,2} ¹Department of Electrical and Computer Engineering, Seoul National University, ²Inter-University Semiconductor Research Center, Seoul National University</p>
<p>FB1-F-2 09:15-09:30</p>	<p>Impact of Leakage Current of Synapse Array on Spiking Neural Networks Bosung Jeon, Seunghwan Song, Taejin Jang, and Byung-Gook Park Department of Electrical and Computer Engineering, Seoul National University</p>
<p>FB1-F-3 09:30-09:45</p>	<p>Effect of Layer-specific Synaptic Retention Characteristics on the Accuracy of Deep Neural Networks Ho-Nam Yoo, Min-Kyu Park, and Jong-Ho Lee School of ECE and ISRC, Seoul National University</p>
<p>FB1-F-4 09:45-10:00</p>	<p>Multi-level Synaptic Array based on AND-type Flash Memory for Hardware-based Neural Network Soochang Lee^{1,2}, Seongbin Oh^{1,2}, and Jong-Ho Lee^{1,2} ¹Department of Electrical and Computer Engineering, Seoul National University, ²Inter-University Semiconductor Research Center, Seoul National University</p>
<p>FB1-F-5 10:00-10:15</p>	<p>Neuron Circuit with Capacitive Transimpedance Amplifier Integrator for Improving Output Linearity in Spiking Neural Networks Youngsan Cha, Kyungchul Park, and Byung-Gook Park Department of Electrical and Computer Engineering, Seoul National University</p>
<p>FB1-F-6 10:15-10:30</p>	<p>Analysis of Line Resistance Effect on Neuromorphic System Jonghyuk Park^{1,2}, Taejin Jang^{1,2}, Sungmin Hwang^{1,2}, Bosung Jeon^{1,2}, and Byung-Gook Park^{1,2} ¹Inter-university Semiconductor Research Center, Seoul National University, ²Department of Electrical and Computer Engineering, Seoul National University</p>