

## D. Thin Film Process Technology 분과

## 2021년 1월 25일(월), 10:45-12:15 / 채널 A

## [MA2-D] Ferroelectric Films II

좌장: 김성근 박사 (KIST), 이웅규 교수 (명지대학교)

MA2-D-1 10:45-11:00	Pulsed I-V Method for Characterizations on Genuine Ferroelectric FieldEffects of the MFMIS-FETs Using Hf-Zr-O Gate Insulators Tae-Hyun Ryu, Dae-Hong Min, and Sung-Min Yoon Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University
MA2-D-2 11:00-11:15	철회
MA2-D-3 11:15-11:30	Defect Engineering을 통한 TiN/H <sub>0.5</sub> Z <sub>0.5</sub> O <sub>2</sub> /TiN 커패시터의 Wake-up Effect와 강유전성 개선 Hyungwoo Kim <sup>1,2</sup> , Alireza Kashir <sup>1,2</sup> , Seungyeol Oh <sup>1,2</sup> , and Hyunsang Hwang <sup>1,2</sup> <sup>1</sup> Center for Single Atom-based Semiconductor Device, POSTECH, <sup>2</sup> Department of Materials Science and Engineering, POSTECH
MA2-D-4 11:30-11:45	Seed Layer Effect of HZO Nanolaminate Structure on Tungsten Electrode Seung-Min Han <sup>1,2</sup> , Dae-Hwan Ahn <sup>1</sup> , Woo-Young Choi <sup>2</sup> , and Jae-Hoon Han <sup>1</sup> <sup>1</sup> Center for Opto-Electronic Materials and Devices, KIST, <sup>2</sup> Department of Electrical and Electronic Engineering, Yonsei University
MA2-D-5 11:45-12:00	Laser Drilling Via Process for 3-D Flexible Integrated Circuits Suwon Seong, Seongmin Park, Jueun Kim, and Yoonyoung Chung Department of Electrical Engineering, POSTECH
MA2-D-6 12:00-12:15	Study of Ferroelectric Characteristics of Hf <sub>0.5</sub> Zr <sub>0.5</sub> O <sub>2</sub> Thin Films Grown on Sputtered or Atomic-layer Deposited TiN Bottom Electrodes Beom Yong Kim <sup>1,2,3</sup> , Hyeon Woo Park <sup>1,2</sup> , Seung Dam Hyun <sup>1,2</sup> , Yong Bin Lee <sup>1,2</sup> , Suk Hyun Lee <sup>1,2</sup> , Minsik Oh <sup>1,2</sup> , Seung Kyu Ryu <sup>1,2</sup> , In Soo Lee <sup>1,2</sup> , Seung Yong Byun <sup>1,2</sup> , Soo Jin Jo <sup>1,2</sup> , Do Sup Shim <sup>1,2</sup> , and Cheol Seong Hwang <sup>1,2</sup> <sup>1</sup> Department of Materials Science and Engineering, Seoul National University, <sup>2</sup> Inter-University Semiconductor Research Center, Seoul National University, <sup>3</sup> R&D Division, SK Hynix Inc.