K. Memory (Design & Process Technology) 분과

2021년 1월 28일(목), 16:30–18:00 / 채널 D

[TD5-K] Emerging Memory I

좌장: 김상범 교수 (서울대학교), 김시준 교수 (강원대학교)

TD5-K-1 **16:30-17:00**	[초청] **Device Requirements and Applications of Memristor-Based Artificial Neural Network Hardware** Shinhyun Choi, Taeryong Kim, Beomjin Kim, and Sion Park *The School of Electrical Engineering, KAIST*
TD5-K-2 **17:00-17:15**	**Superlattice-like GeTe/Sb_2Te_3 Phase Change Material Synthesized by Atomic Layer Deposition and Its Electrical Performance** Chanyoung Yoo[1,2], Eui-sang Park[1,2], Woohyun Kim[1,2], Jeong Woo Jeon[1,2], Wonho Choi[1,2], Byongwoo Park[1,2], Gyuseung Han[1,2,3], Yoon Kyeung Lee[4], and Cheol Seong Hwang[1,2] *[1]Department of Materials Science and Engineering, Seoul National University,* *[2]Inter-University Semiconductor Research Center, Seoul National University,* *[3]Center for Electronic Materials, KIST, [4]Division of Advanced Materials Engineering, Jeonbuk National University*
TD5-K-3 **17:15-17:30**	**Memristive Reservoir Computing for Medical Diagnosis** Yoon Ho Jang[1,2], Ji Hun Kim[1,2], Jeong Woo Jeon[1,2], Woo Hyun Kim[1,2], and Cheol Seong Hwang[1,2] *[1]Department of Materials Science and Engineering, Seoul National University,* *[2]Inter-University Semiconductor Research Center, Seoul National University*
TD5-K-4 **17:30-17:45**	**Synaptic Weight Enhanced IGZO Nanofiber Channel Transistor with Ta_2O_5-Barrier Stacked Chitosan-Electric-Double Layer** Sung-Hun Kim and Won-Ju Cho *Department of Electronic Materials Engineering, Kwangwoon University*
TD5-K-5 **17:45-18:00**	**Interface Dipole Modulation Device: The New Candidate of Non-volatile Memory** Giuk Kim and Sanghun Jeon *School of Electrical Engineering, KAIST*