



D. Thin Film Process Technology 분과

2021년 1월 28일(목), 14:45-16:15 / 채널 A

[TA4-D] Emerging Devices I

좌장: 김정환 교수 (한밭대학교), 최창환 교수 (한양대학교)

<p>TA4-D-1 14:45-15:15</p>	<p>[초청] Highly Reliable Heterosynaptic Plasticity of Low-powered Memtransistor for Neuromorphic Applications Byungjin Cho <i>Department of Advanced Material Engineering, Chungbuk National University</i></p>
<p>TA4-D-2 15:15-15:30</p>	<p>Wafer-scale Striped Carbon Nanotube Network Transistor Ju Won Jeon¹, Yongwoo Lee¹, Geon-Hwi Park¹, Jueun Kim¹, Dong Myong Kim¹, Dae Hwan Kim¹, Min-Ho Kang,² and Sung-Jin Choi¹ ¹<i>School of Electrical Engineering, Kookmin University,</i> ²<i>Department of Nano-process, NNFC</i></p>
<p>TA4-D-3 15:30-15:45</p>	<p>Implementation of In-Ga-Zn-O Thin-Film Transistors with Vertical Channel Structures Designed with Silicon Spacer Steps Se-Na Choi and Sung-Min Yoon <i>Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University</i></p>
<p>TA4-D-4 15:45-16:00</p>	<p>Impact of Vertical-Channel Structure for Charge-Trap Memory Thin Film Transistors Using In-Ga-Zn-O Active and Coated SiO₂ Spacer Layers Soo Hyun Bae, Hyun Joo Ryu, and Sung Min Yoon <i>Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University</i></p>
<p>TA4-D-5 16:00-16:15</p>	<p>Atomic-Layer-Deposited Li Compound-Based Two-Terminal Artificial Synapse Devices Hye-Rim Kim¹, Hyun Seung Choi¹, Gun Hwan Kim², and Tae Joo Park¹ ¹<i>Department of Materials Science and Chemical Engineering, Hanyang University,</i> ²<i>Division of Advanced Materials, KRICT</i></p>