



L. Analog Design 분과

2021년 1월 27일(수), 16:30-18:00 / 채널 B

[WB5-L] Analog Design

좌장: 엄지용 교수 (한남대학교), 정영호 교수 (대구대학교)

<p>WB5-L-1 16:30-17:00</p>	<p>[초청] 아날로그 연산기 기반의 학습 가능한 신경망 시스템 손현우 경상대학교</p>
<p>WB5-L-2 17:00-17:15</p>	<p>An APD-based Optoelectronic Integrated Circuit for LiDAR Sensors Ji-Eun Joo¹, M. J. Lee², and Sung Min Park¹ ¹Department of Electronic and Electrical Engineering, Ewha Womans University, ²Post-Silicon Semiconductor Institute, KIST</p>
<p>WB5-L-3 17:15-17:30</p>	<p>Digital Control Readout Integrated Circuit(ROIC) For Gas Sensor Joo-Hwan Jin¹, Juyong Lee¹, Seungjun Lee¹, Kihyun Kim¹, Younggyun Oh², and Hyungil Chae¹ ¹Department of Electronic Engineering, Konkuk University, ²Department of Electronic Engineering, Kookmin University</p>
<p>WB5-L-4 17:30-17:45</p>	<p>A Design of LDO Based Supply Modulator for 5.8GHz DSRC Transceiver Power Amplifier's Amplitude Modulation Young-Uk Kim, Sung-Jin Kim, and Kang-Yoon Lee Department of Electrical and Computer Engineering, Sungkyunkwan University</p>
<p>WB5-L-5 17:45-18:00</p>	<p>A Design of Ultra Low Power 12-bit SAR ADC with Full-Range Input and Single-ended / Differential-ended Hybrid Mode for Battery Monitoring IC Dae-Young Choi, Young-Uk Kim, Sung-Jin Kim, and Kang-Yoon Lee Department of Electrical and Computer Engineering, Sungkyunkwan University</p>

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