

S. Chip Design Contest 분과

2020년 2월 13일(목), 09:00-10:30 / Room I (하트 II, 6층)

■ [T11-S] Selected Papers on Chip Design Contest

좌장: 차혁규 교수 (서울과학기술대학교), 고희호 교수 (충남대학교)

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| T11-S-1 09:00-09:15 | A Synchronous Buck Converter Using a Voltage Buffer Compensator Jun Tang and Jeongjin Roh <i>Department of Electronics and Communications Engineering, Hanyang University</i> |
| T11-S-2 09:15-09:30 | Monolithic Three-dimensional (M3D) CMOS- Nanoelectromechanical (NEM) Single-tile Reconfigurable Logic (RL) Hyug Su Kwon and Woo Young Choi <i>Department of Electronic Engineering, Sogang University</i> |
| T11-S-3 09:30-09:45 | Ultra-small IoT Gas Sensor for Sensing Hazardous Gas Seungjun Lee ¹ , Sein Oh ¹ , Younggyun Oh ¹ , Juyung Lee ¹ , Kihyun Kim ¹ , Joohwan Jin ¹ , and Hyung Il Chae ² <i>¹Department of Electronic Engineering, Kookmin University, ²Department of Electronic Engineering, Konkuk University</i> |
| T11-S-4 09:45-10:00 | Implementation of Low-Complexity Extended CCA Hardware Accelerator for Wearable Brain-Computer Interface SoC Dokyun Kim ¹ , Wooseok Byun ² , Sung Yeon Kim ¹ , Hyunji Kim ³ , Sunyoung Park ³ , and Ji-Hoon Kim ³ <i>¹SEOULTECH, ²Chungnam National University, ³Ewha Womans University</i> |
| T11-S-5 10:00-10:15 | Low Noise, Low Power 5-Channel Sonar Signal Conditioning Receiver with 1.5 MS/s – 12.5 MS/s 16-bit Sigma-Delta ADC for Ocean Acoustic Measurements Sang-Gyu Jeon, Tae-Young Yoon, Byeong-Gi Jang, and Kang-Yoon Lee <i>Sungkyunkwan University</i> |
| T11-S-6 10:15-10:30 | 용량형 센서용 저잡음 16 비트 2차 델타-시그마 커패시턴스-디지털 컨버터 김형섭, 김재성, 한권상, 유동근, 허현우, 권용수, 고희호 <i>충남대학교 전자공학과</i> |