

## L. Analog Design 분과

2020년 2월 13일(목), 09:00-10:30 / Room E (루비 II, 5층)

### ■ [TE1-L] Analog

좌장: 지동우 교수 (아주대학교), 엄지용 교수 (한남대학교)

TE1-L-1 09:00-09:15	<b>A Low-Luminance Compensation Current Driver for AMOLED Displays</b> JeeHun Yeom, Minku Song, and Soo Youn Kim <i>Department of Semiconductor Science, Dongguk University</i>
TE1-L-2 09:15-09:30	<b>Shift Register for Depletion Mode a-IGZO TFTs Using Dual Pull-Down Structure</b> Jongsu Oh, Jungwoo Lee, Eun Kyo Jung, and Yong-Sang Kim <sup>1</sup> <i>Department of Electrical and Computer Engineering, Sungkyunkwan University</i>
TE1-L-3 09:30-09:45	<b>A WLAN 2.45-GHz RF Energy Harvester with DC-DC Converter for Wireless Sensor Network</b> Chae-Hyun Kim and Hyungmin Lee <i>School of Electrical Engineering, Korea University</i>
TE1-L-4 09:45-10:00	<b>Design of Low-Loss, High-Efficiency Step-Up Hybrid Switched-Capacitor Converter for IoT Smart Nodes</b> Joonho Park and Jaeha Kim <i>Department of Electrical and Computer Engineering, Seoul National University</i>
TE1-L-5 10:00-10:15	<b>A Two-Step Coarse-Fine Time-to-Digital Conversion Technique Using Oscillation Collapse-Based Ring Oscillator</b> Wooryeol Kim and Jaeha Kim <i>Department of Electrical and Computer Engineering, Seoul National University</i>
TE1-L-6 10:15-10:30	<b>A 9-bit, 1<sup>st</sup> Order Noise Shaping SAR ADC with Embedded Passive Gain</b> Chang-Hyung Choi and Jong-Wook Lee <i>Department of Electronic Engineering, Kyung Hee University</i>